Today, we created a memory structure. The memory structure contains a predefined program. Here, we can store and load data and words. We are planning to find a way to read a program file and store it in the memory. We designed a new structure for the controller. We discarded the micro store. For this new structure, a FSM was created and the FSM has partially been implemented within the code. Tomorrow, we will continue on the code. In addition, we worked on a 7 segment display function. This works with integers and hexadecimal numbers. Tomorrow, we will test it on the FPGA. At last, we worked on the data path. We wanted to update signals into variables, but that did not work. Tomorrow we look for a solution and connect with other components of the processor.