

Main Processor

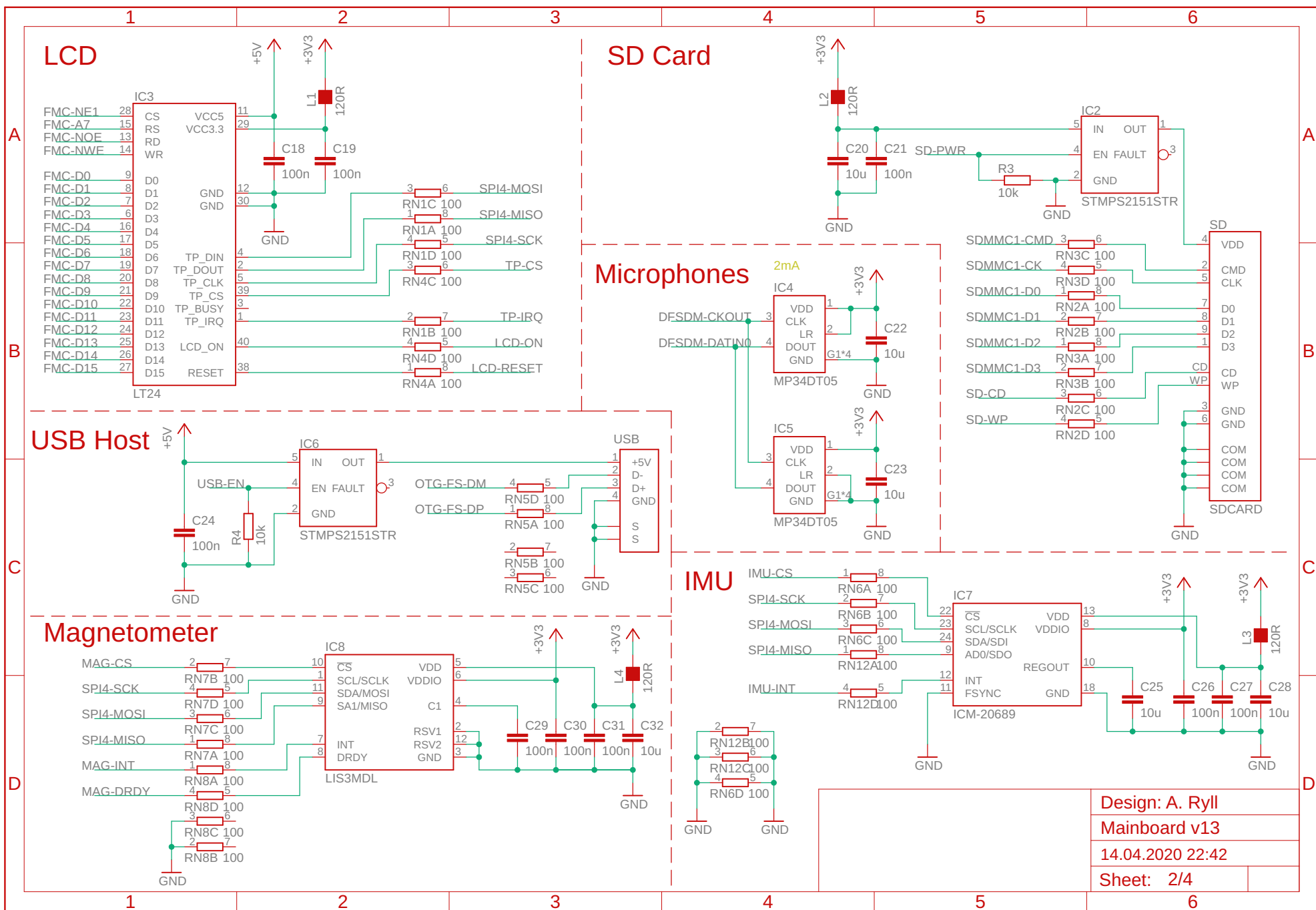
IC1A	IC1B	IC1C	IC1D	IC1E	IC1F	IC1G
UART4-TX 34 P0	TIM3-CH3 46 P0	LED1 26 P0	FMC-D2 114 P0	UART8-RX 141 P0	SMB-SDA 10 P0	TP-CS 56 P0
UART4-RX 35 P1	TIM3-CH4 47 P1	DFSDM-DATIN0 27 P1	FMC-D3 115 P1	UART8-TX 142 P1	SMB-SCL 11 P1	LCD-RESET 57 P1
USART2-TX 36 P2	WF-QN 48 P2	DFSDM-CKOUT 28 P2	SDMMC1-CMD 116 P2	SPI4-SCK 1 P2	SMB-ALRT 12 P2	IDD0 87 P2
USART2-RX 37 P3	JTAG-RESV2 133 P3	LED2 29 P3	PDN 117 P3	IMU-INT 3 P3	WE-CPS 13 P3	IDD1 88 P3
TP-Irq 40 P4	JTAG-RESV3 134 P4	WE-BUSY 44 P4	FMC-NOE 118 P4	SPI4-MISO 4 P4	WE-CRX 14 P4	IDD2 89 P4
SPI1-SCK 41 P5	UART5-RX 135 P5	WE-Irq 45 P5	FMC-NWFE 119 P5	SPI4-MOSI 5 P5	WE-CTX 15 P5	IDD3 90 P5
SPI1-MISO 42 P6	UART5-TX 136 P6	TIM3-CH1 96 P6	KILL 122 P6	SPI4-MOSI 5 P6	UART7-RX 18 P6	IDD4 91 P6
SPI1-MOSI 43 P7	IMU-CS 137 P7	TIM3-CH2 97 P7	FMC-NE1 123 P7	FMC-D4 58 P7	UART7-TX 19 P7	IDD5 92 P7
TIM1-CH1 100 P8	TIM4-CH3 139 P8	SDMMC1-D0 98 P8	FMC-D13 77 P8	FMC-D5 59 P8	TIM13-CH1 20 P8	IDD6 93 P8
USART1-TX 101 P9	TIM4-CH4 140 P9	SDMMC1-D1 99 P9	FMC-D14 78 P9	FMC-D6 60 P9	TIM14-CH1 21 P9	USART6-RX 124 P9
USART1-RX 102 P10	USART3-TX 69 P10	SDMMC1-D2 111 P10	FMC-D15 79 P10	FMC-D7 63 P10	WE-CS 22 P10	KICK-ERR 125 P10
OTG-FS-DM 103 P11	USART3-RX 70 P11	SDMMC1-D3 112 P11	SD-PWR 80 P11	FMC-D8 64 P11	SD-CD 49 P11	KICK-CHG 126 P11
OTG-FS-DP 104 P12	SD-WP 73 P12	SDMMC1-CK 113 P12	TIM4-CH1 81 P12	FMC-D9 65 P12	OSC_IN 50 P12	KICK-CS 127 P12
SWDIO 105 P13	SPI2-SCK 74 P13	MAG-CS 7 P13	TIM4-CH2 82 P13	FMC-D10 66 P13	FMC-A7 53 P13	SMB-RST 128 P13
SWCLK 109 P14	SPI2-MISO 75 P14	MAG-DRDY 8 P14	FMC-D0 85 P14	FMC-D11 67 P14	USB-FN 54 P14	USART6-TX 129 P14
JTAG-RESV1 110 P15	SPI2-MOSI 76 P15	MAG-INT 9 P15	FMC-D1 86 P15	FMC-D12 68 P15	LCD-QN 55 P15	USB-ATT 132 P15

STM32H743ZST STM32H743ZST STM32H743ZST STM32H743ZST STM32H743ZST STM32H743ZST STM32H743ZST

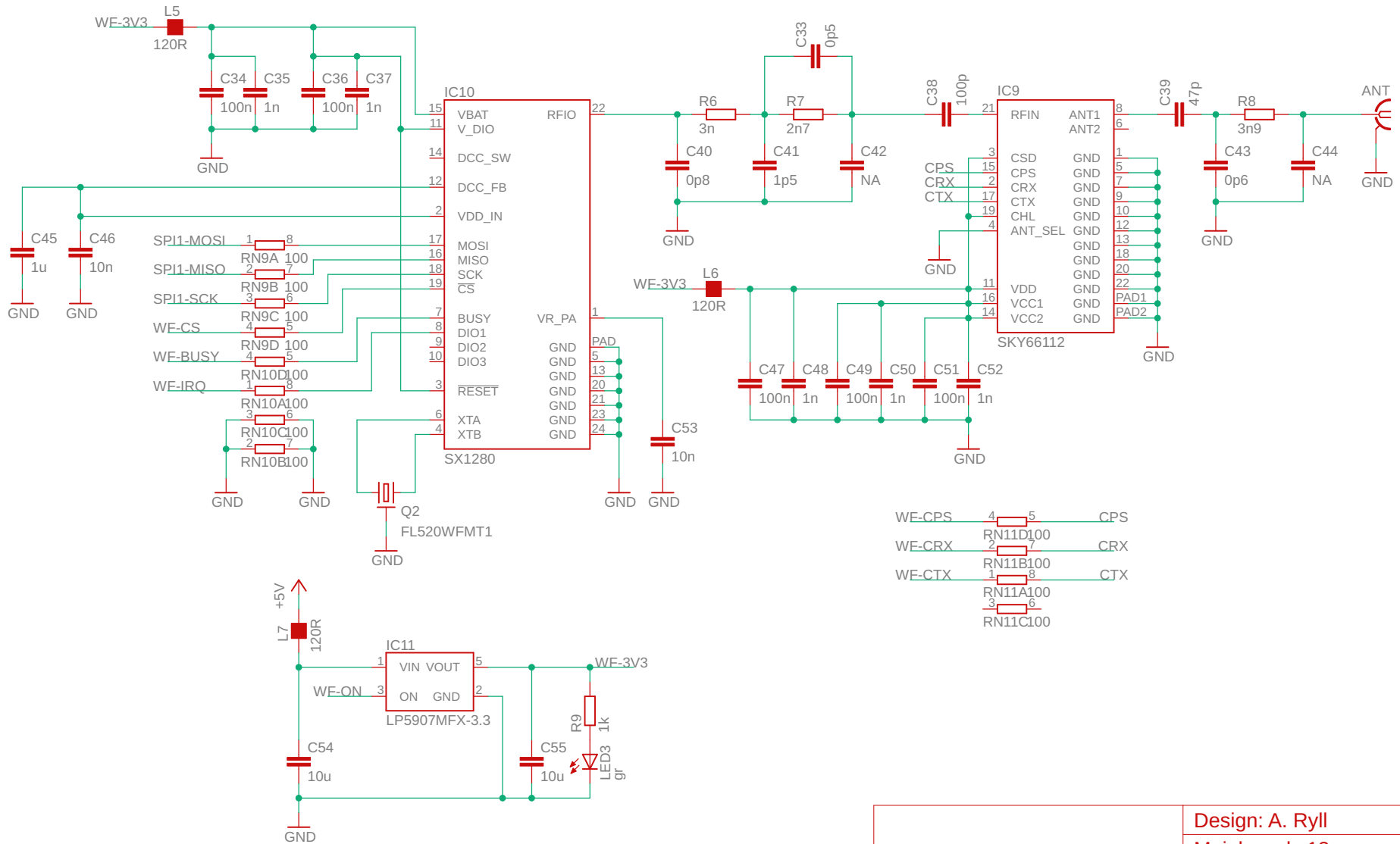
The diagram illustrates the main processor circuit for the STM32H743ZST. It includes several key components and their connections:

- SRST:** Reset signal input.
- SW1:** A switch labeled KT11P4SA8M used for reset or boot selection.
- C2, C3:** Capacitors (100nF) connected to the VBAT and BOOT pins.
- LED1, LED2:** Status LEDs connected through resistors R1 and R2.
- Q1:** An oscillator driver (ASE-4.000MHZ-LC-T) connected to the OSC_IN and OSC_OUT pins.
- IC1A_PWR:** Power management component connected to VDDA and VREF+.
- IC1OSC:** Oscillator component connected to the oscillator output.
- Capacitors:** Multiple capacitors (C4-C17) are shown, including two large electrolytic capacitors (C4, C5) and several smaller ceramic capacitors (C6-C17) connected to various power rails (+3V3, GND).

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Wireless Interface

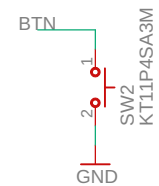


Design: A. Ryll

Mainboard v13

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	A1*2	IDD-1
IDD0	A2*2	IDD-2
IDD1	A3*2	IDD-3
IDD2	A4*2	IDD-4
IDD3	A5*2	IDD-5
IDD4	A6*2	IDD-6
IDD5	A7*2	IDD-7
IDD6	A8*2	IDD-8

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