

KapitanPOP Manual

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1 Metrics Computation

This section accurately describes how KapitanPOP computes its metrics from the data contained in the HDF5 trace file. First, it starts with a depiction of how raw metrics are extracted, i.e. *Useful Computation Time*, evolving gradually to the higher level metrics, like *Parallel Efficiency*. Before continuing, though, it is convenient to give a solid definition to some very used terms in order to avoid confusions.

- **Useful:** this adjective refers to any value belonging to the *Running* state defined by Extrae. In other words, *useful* is anything that belongs to user code, or in other words, that is not part of system's -the kernel- or runtime's -OpenMP, pthreads, MPI- code. Input/Output or I/O can also be considered as *useful*, this will depend of how Extrae was configured when tracing.
- **Maximum:** refers to the maximum existing value when grouping the data by application, process and thread. For example, *Maximum Time in MPI* is the maximum time one process -MPI rank- has spent inside MPI compared to the rest of processes.
- **Runtime:** is the elapsed time since the start of an application to its end as recorded by Extrae.
- **Ideal:** this adjective refers to any value that has been computed from a Dimemas trace with ideal network -zero latencies, infinite bandwidth.

- **Reference:** is used for scalability purposes and refers to the metric coming from the run with less number of cores.

Raw Metrics

- Runtime
- Useful Compute Time
 - Total Useful Compute Time
 - Maximim Useful Compute Time
 - Average Useful Compute Time
- Total Useful Instructions
- Total Useful Cycles
- Ideal Times
 - Ideal Runtime
 - Maximum Ideal Useful Compute Time

Runtime

The *runtime* is extracted from Paraver's header, thus it is already provided and not computed.

Useful Compute Time

The Useful Compute Time is the time the application spends running user code. It is computed from *States* records where state is equal to *Running*.

$$\begin{aligned} \text{Total Useful Compute Time} = \\ \sum StateRecord_{time_end} - \sum StateRecord_{time_start} \end{aligned} \quad (1)$$

where $StateRecord_{state} = \text{Running}$

$$\begin{aligned} \text{Max. Useful Compute Time} = \\ \max(\sum StateRecord_{time_end} - \sum StateRecord_{time_start}), \end{aligned} \quad (2)$$

where $StateRecord_{state} = \text{Running}$, $\text{groupby}(\text{appID}, \text{taskID}, \text{theadID})$

$$\begin{aligned} \text{Average Useful Compute Time} = \\ \text{Total Useful Compute Time} / \text{number of threads} \end{aligned} \quad (3)$$

Total Useful Instructions

$$\begin{aligned} \text{Total Useful Instructions} = \\ \sum EventRecord_{EvValue}, \\ \left(\text{where } EventRecord_{EvType} = PAPI_TOT_INS \wedge \right. \\ \left. EventRecord_{time} = StateRecord_{time_end}, \text{ where } StateRecord_{state} = \text{Running} \right) \\ \text{groupby}(\text{appID}, \text{taskID}, \text{theadID}) \end{aligned} \quad (4)$$

Total Useful Cycles

$$\begin{aligned} \text{Total Useful Cycles} = \\ \sum EventRecord_{EvValue}, \\ \left(\text{where } EventRecord_{EvType} = PAPI_TOT_CYC \wedge \right. \\ \left. EventRecord_{time} = StateRecord_{time_end}, \text{ where } StateRecord_{state} = \text{Running} \right) \\ \text{groupby}(\text{appID}, \text{taskID}, \text{theadID}) \end{aligned} \quad (5)$$

Ideal Times

Ideal Runtime and *Maximum Ideal Useful Compute Time* are computed the same way as *Runtime* and *Maximum Useful Compute Time* with the difference that the data used comes from a Dimemas trace simulating an ideal network.

1.1 Multiplicative Performance Metrics

- Parallel Performance Metrics
 - Load Balance
 - Communication Efficiency
 - Transfer Efficiency
 - Serializtion Efficiency

- Parallel Efficiency
- Global Efficiency
- Scalabilities
 - IPC Scalability
 - Frequency Scalability (GHz)
 - Strong
 - * Speedup
 - * Computation Scalability
 - * Instruction Scalability
 - Weak
 - * Load Increase Factor (LIF)
 - * Speedup
 - * Computation Scalability
 - * Instruction Scalability
- Serial Performance Metrics
 - Average IPC
 - Average Frequency

Parallel Performance Metrics

$$\text{Load Balance} = \frac{\text{Average Useful Compute Time}}{\text{Max. Useful Compute Time}} * 100 \quad (6)$$

$$\text{Communication Efficiency} = \frac{\text{Max. Useful Compute Time}}{\text{Runtime}} * 100 \quad (7)$$

$$\text{Transfer Efficiency} = \frac{\text{Ideal Runtime}}{\text{Runtime}} * 100 \quad (8)$$

$$\text{Serialization Efficiency} = \frac{\text{Communication Efficiency}}{\text{Transfer Efficiency}} * 100 \quad (9)$$

$$\text{Parallel Efficiency} = \frac{\text{Load Balance} * \text{Communication Efficiency}}{100} \quad (10)$$

Global Efficiency

$$\text{Global Efficiency} = \frac{\text{Parallel Efficiency} * \text{Computation Scalability}}{100} \quad (11)$$

Scalabilities

$$\text{IPC Scalability} = \frac{\text{Average IPC}}{\text{Reference Average IPC}} * 100 \quad (12)$$

$$\text{Frequency Scalability (GHz)} = \frac{\text{Average Frequency}}{\text{Reference Average Frequency}} * 100 \quad (13)$$

Strong

$$\text{Speedup} = \frac{\text{Reference Runtime}}{\text{Runtime}} \quad (14)$$

$$\text{Strong Computation Scalability} = \frac{\text{Reference Total Useful Compute Time}}{\text{Total Useful Compute Time}} * 100 \quad (15)$$

$$\text{Strong Instruction Scalability} = \frac{\text{Reference Total Useful Instructions}}{\text{Total Useful Instructions}} * 100 \quad (16)$$

Weak

LIF (Load Increase Factor) is the factor by which the load (amount of work) increases. Ideally, it should be equal to the increase in number of processes/threads.

$$\text{LIF} = \frac{\text{Number of threads}}{\text{Reference Number of Threads}} \quad (17)$$

$$\text{Speedup} = \frac{\text{Reference Runtime}}{\text{Runtime}} * \text{LIF} \quad (18)$$

$$\text{Weak Computation Scalability} = \frac{\text{Reference Total Useful Compute Time}}{\text{Total Useful Compute Time}} * \text{LIF} * 100 \quad (19)$$

$$\begin{aligned} \text{Weak Instruction Scalability} = \\ \text{Reference Total Useful Instructions} / \text{Total Useful Instructions} * LIF * 100 \end{aligned} \quad (20)$$

Serial Performance Metrics

$$\begin{aligned} \text{Average IPC} = \\ \text{Total Useful Instructions} / \text{Total Useful Cycles} \end{aligned} \quad (21)$$

$$\begin{aligned} \text{Average Frequency (GHz)} = \\ \text{Total Useful Cycles} / \text{Total Useful Time (ms)} / 1000 \end{aligned} \quad (22)$$