



SCL: IIC timing transmission interface

SDA: IIC Data transmission interface

IIC address: 0x50

GND: Ground

IIC rating: 50kbit max

Operating Voltage: 3.3V , 5V

DH: XFS5152CE

Module size: 45.3mm*28.7mm*15.5mm

VCC: power interface(3.3V/5V)

IIC rating: 50kbit max

Interface mode: Alligator clip interface, Dupont wire trip button, PH2.0 cable interface

1.Introduction

Speech Synthesis module comes with a highly integrated speech synthesis chip-XFS5152CE, which can realize Chinese and English speech synthesis, and supports mixed reading of Chinese and English.

The module uses I2C for communication, which is convenient and quick to operate.

It can be connected with alligator clip/DuPont line/PH2.0 cable.

2.Function

The chip can synthesize any Chinese and English texts. It can use four encoding methods: GB2312, GBK, BIG5 and UNICODE.

The amount of text synthesized each time is up to 4K bytes.

For texts in common formats such as numbers, time, date, weights and measures symbols, the chip can correctly recognize and process according to the built-in text matching rules;

There are multiple speakers built in the chip, and the corresponding speakers



can be selected for voice playback.

Users can set speed, intonation, and volume of the voice broadcast by yourself.