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ECE 6240

Lab#7 Dynamic Circuits using Domino Logic
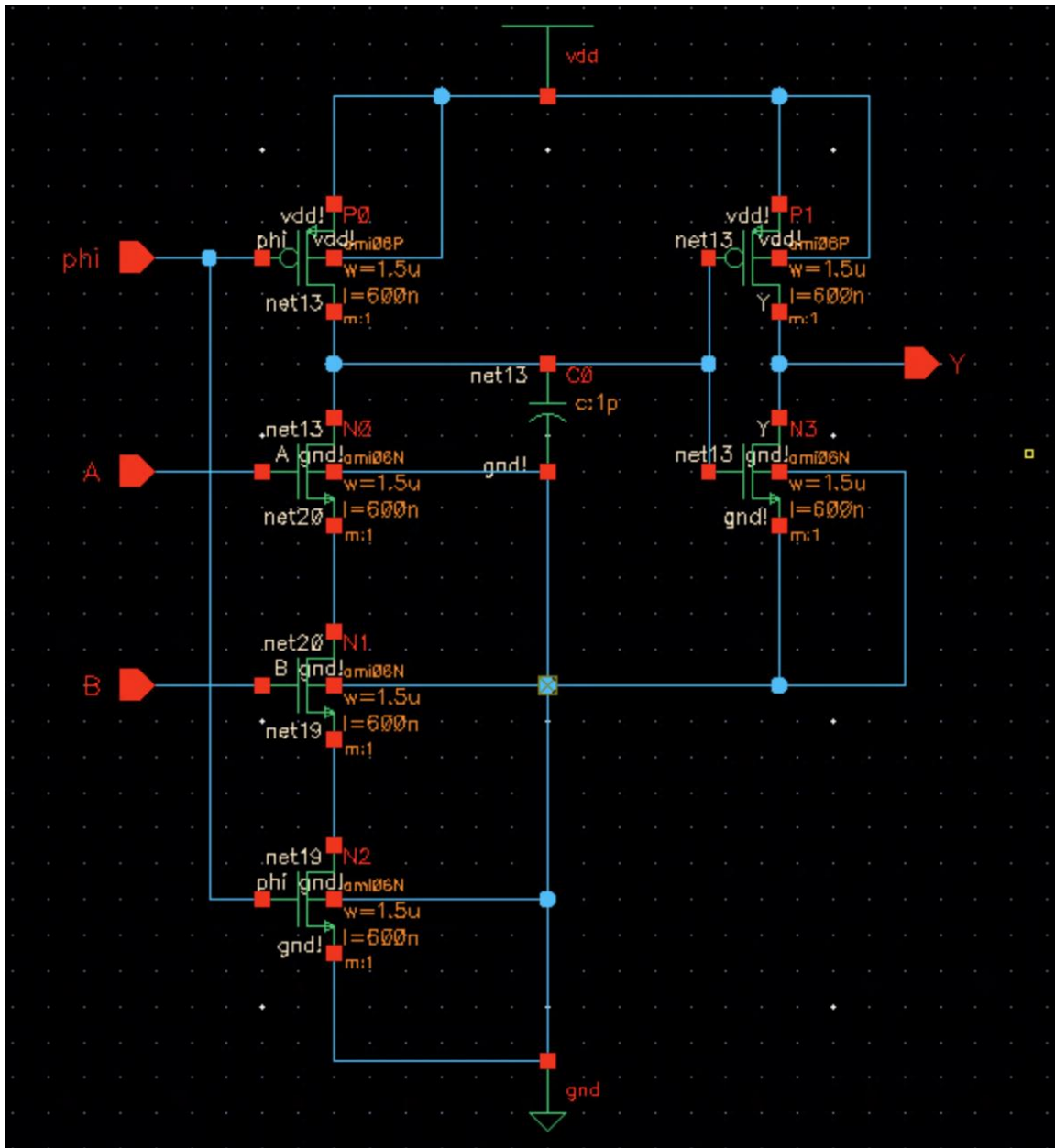

Figure 1 Schematic
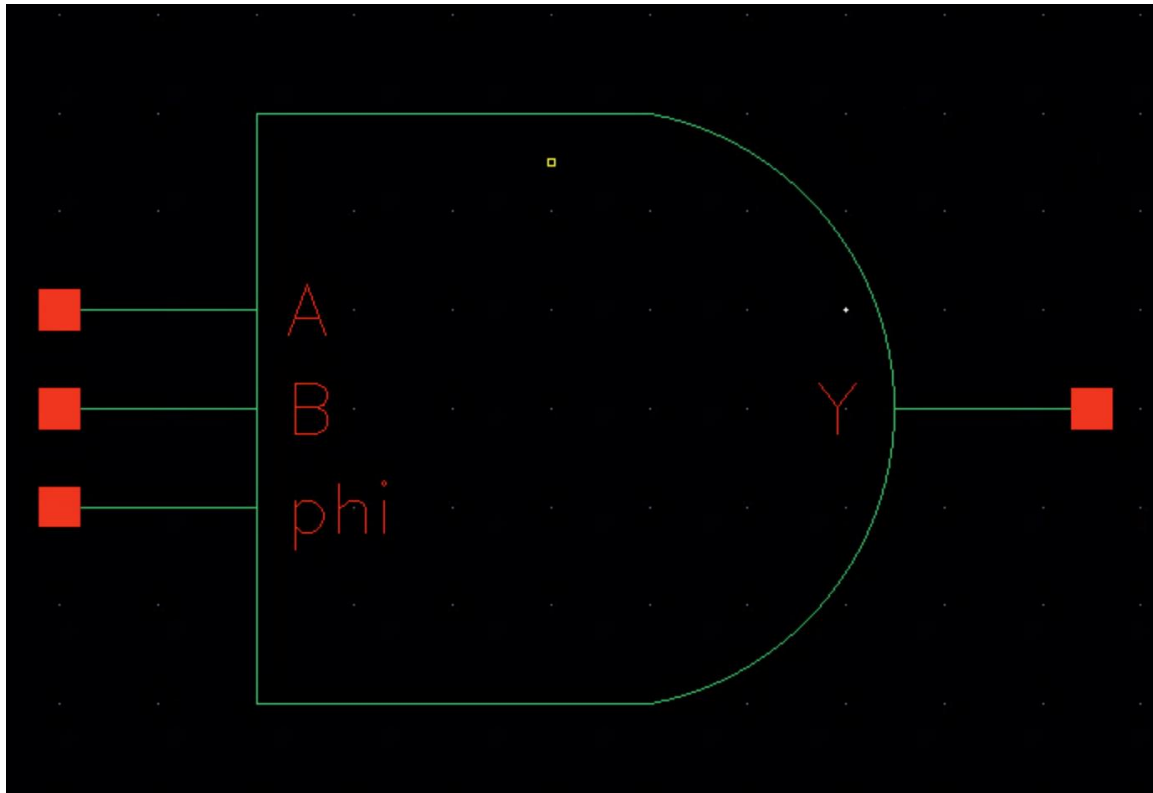
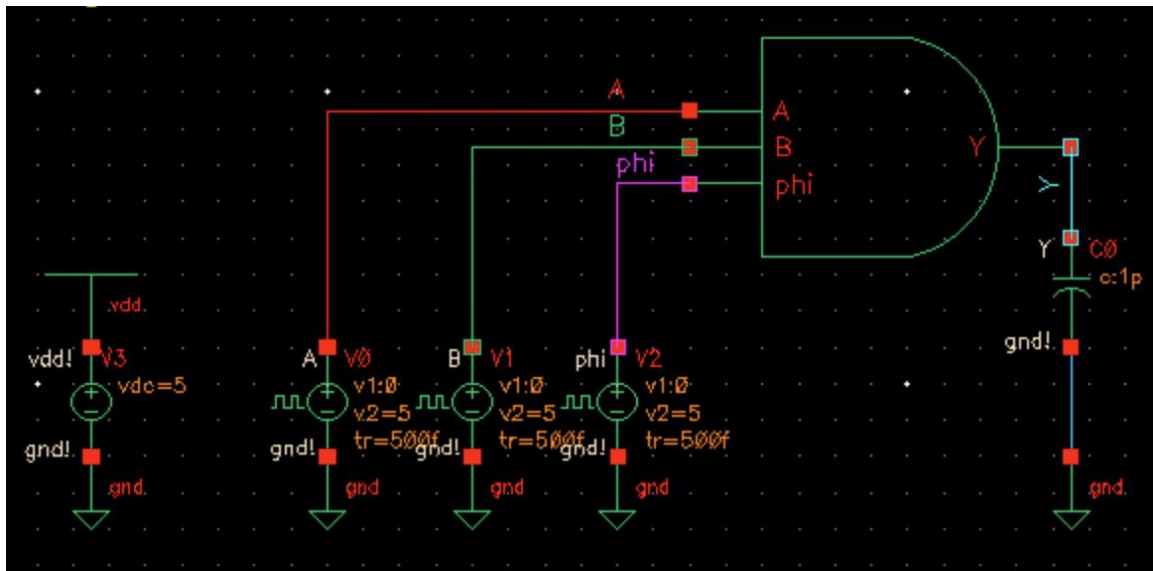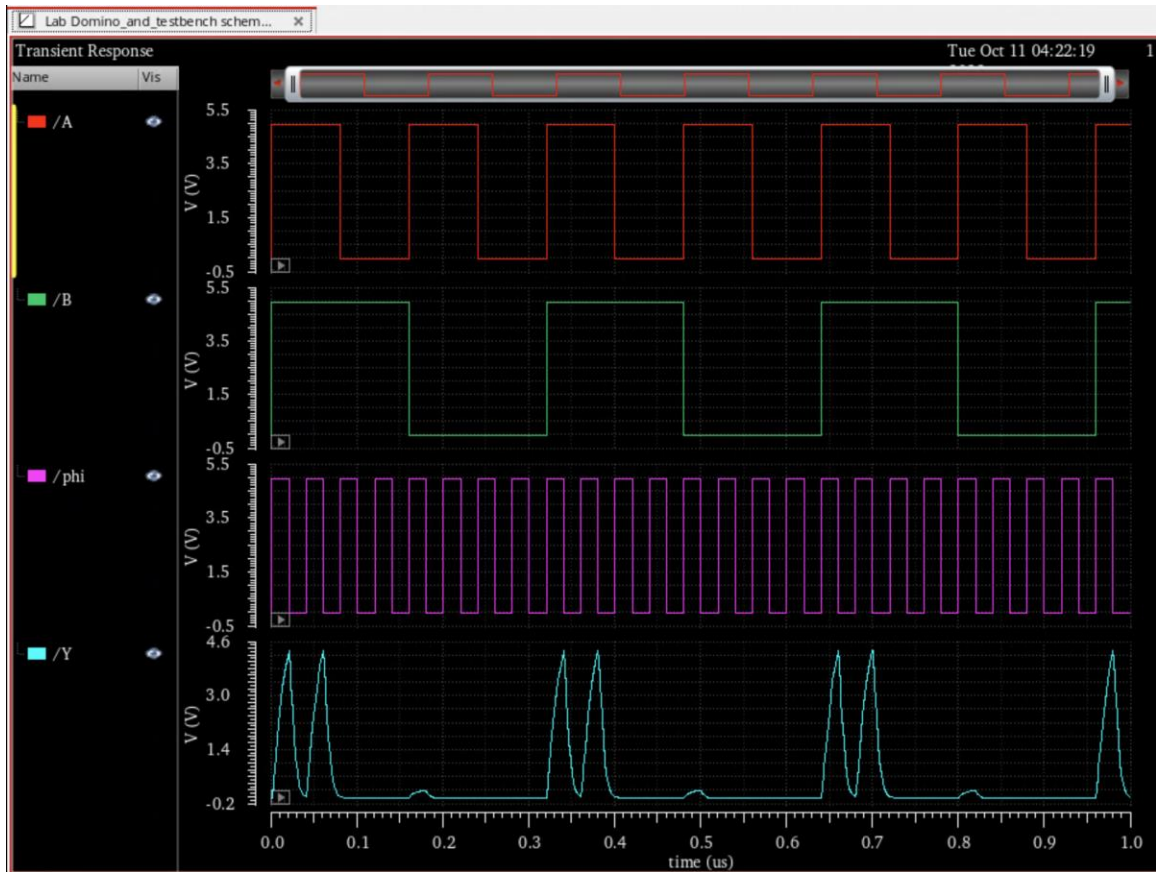*Figure 2 Symbol*

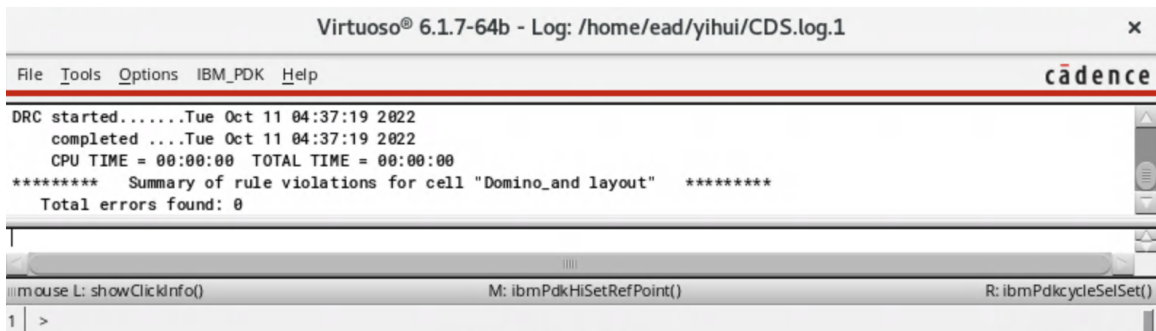*Figure 3 Testbench Schematic*

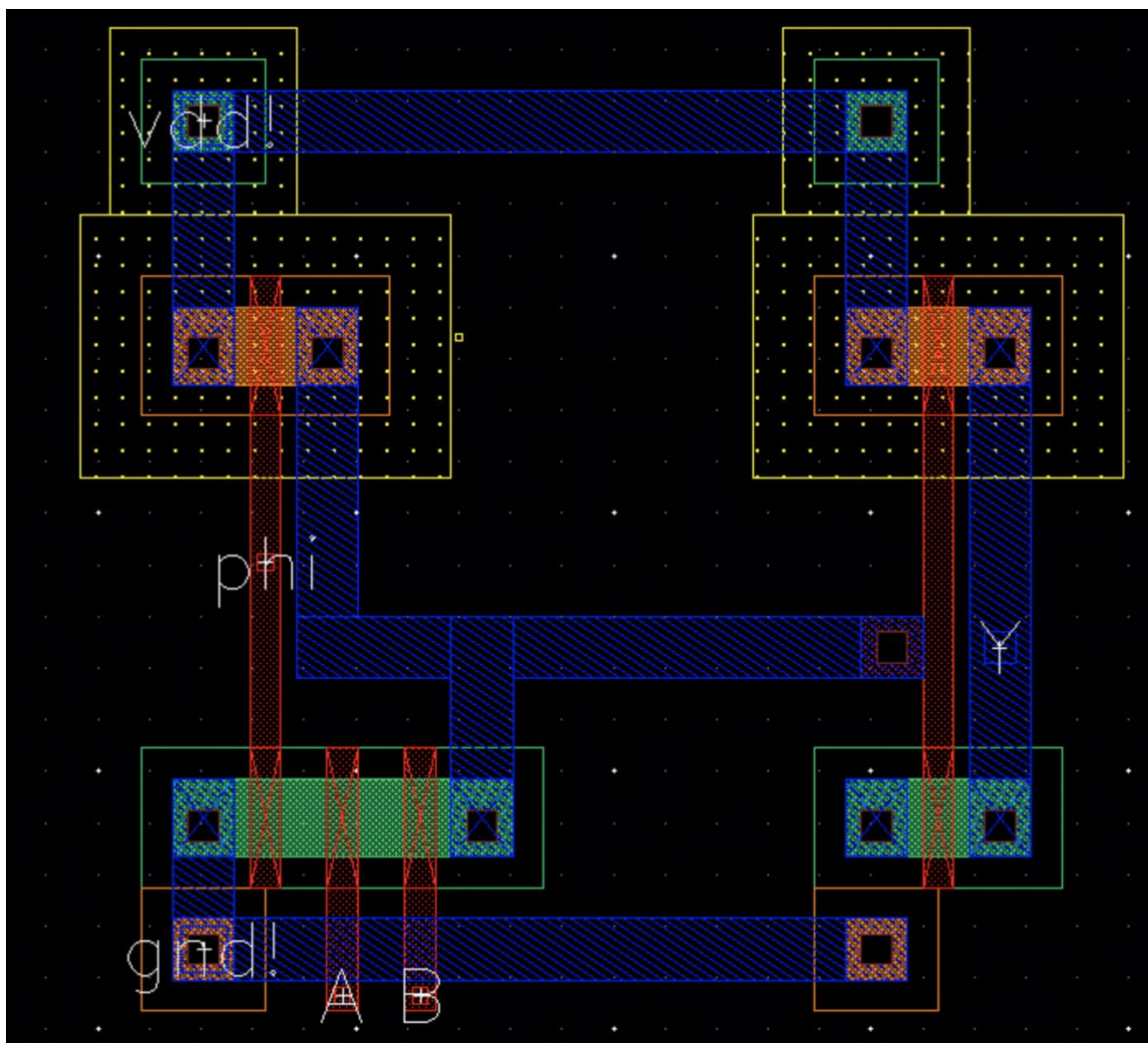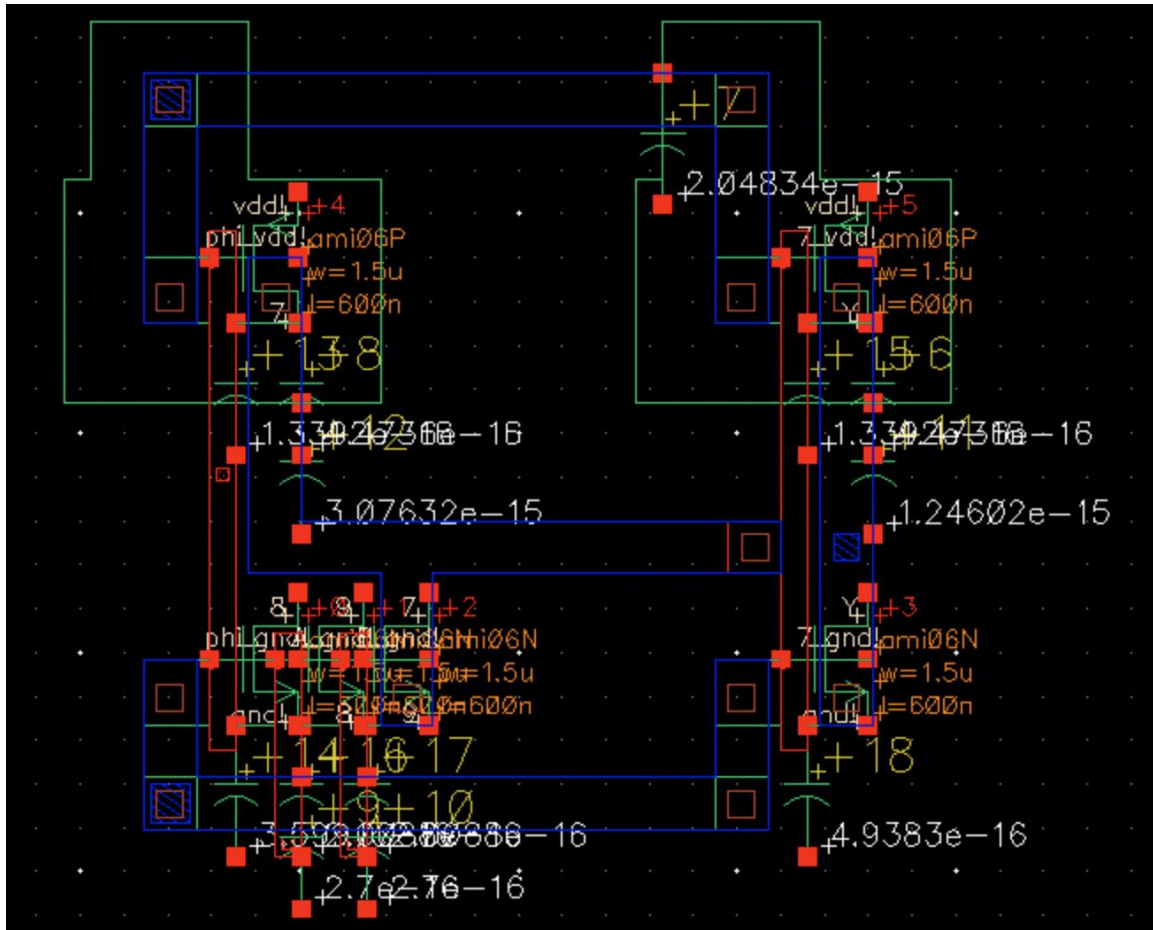*Figure 4 Testbench Result*

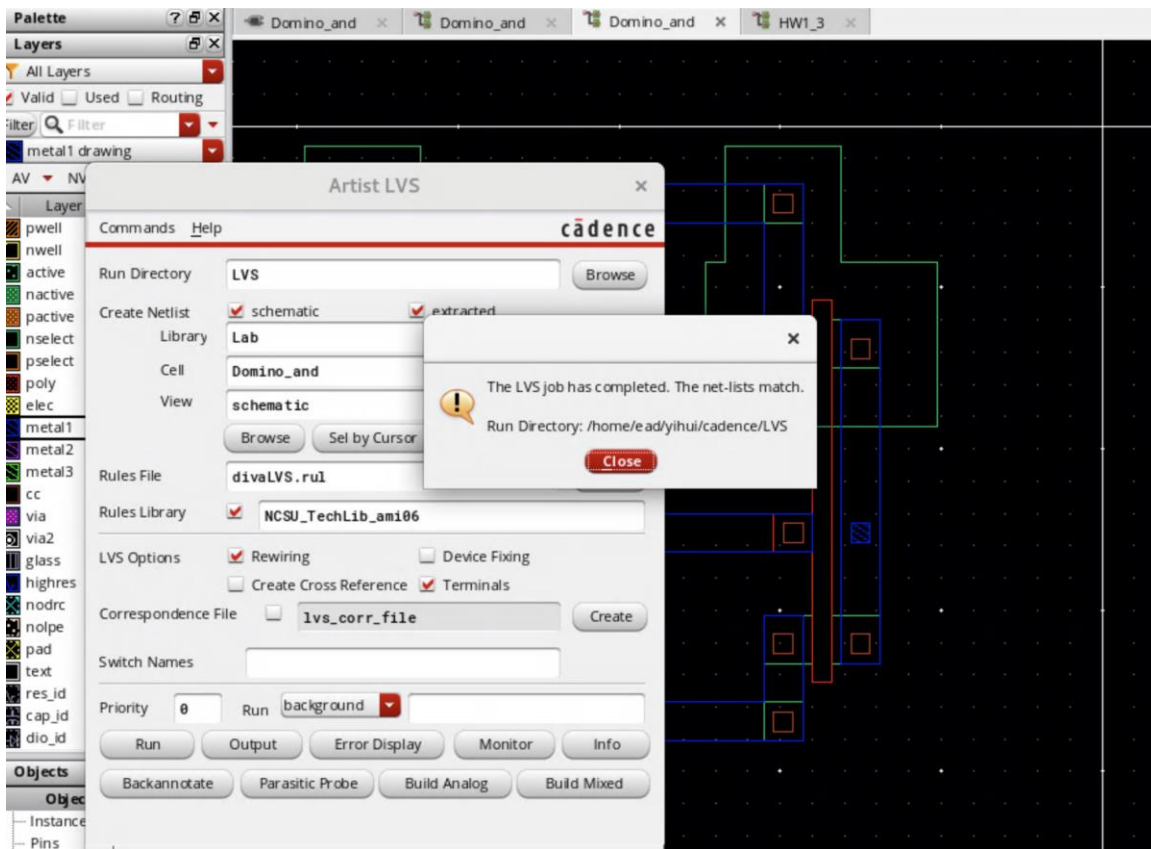*Figure 5 DRC Result*

Figure 6 Layout

Figure 7 Extracted Layout

Figure 8 LVS Result

For static AND gate: 18*22.35=402.3um^2, for domino AND gate: 20.25*19.05=385.76 um^2