# Storage Systems (StoSys) XM\_0092

# **Lecture 7: Networked NVM Storage**

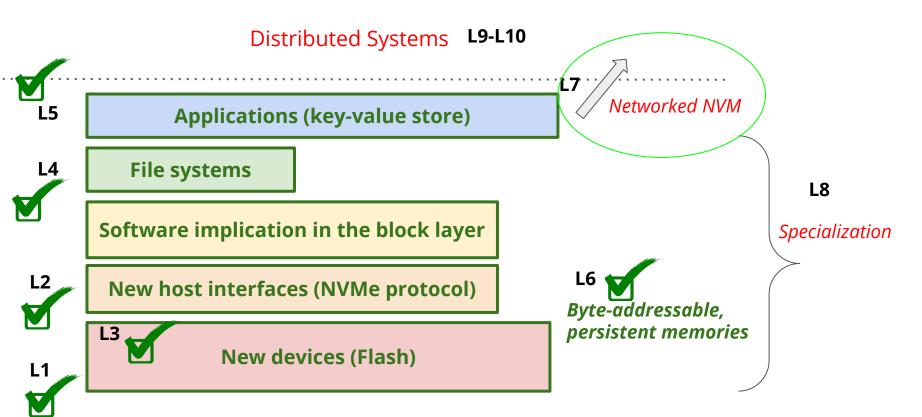
Animesh Trivedi Autumn 2020, Period 2



### Syllabus outline

- 1. Welcome and introduction to NVM (today)
- 2. Host interfacing and software implications
- 3. Flash Translation Layer (FTL) and Garbage Collection (GC)
- 4. NVM Block Storage File systems
- 5. NVM Block Storage Key-Value Stores
- 6. Emerging Byte-addressable Storage
- 7. Networked NVM Storage
- 8. Trends: Specialization and Programmability
- Distributed Storage / Systems I
- 10. Distributed Storage / Systems II

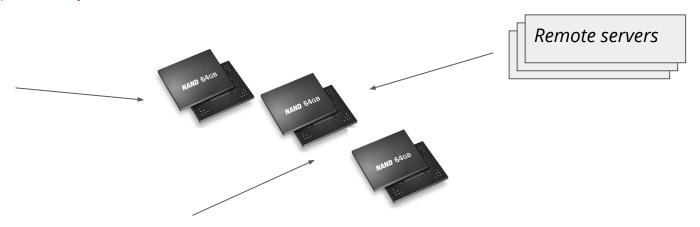
### The layered approach in the lectures



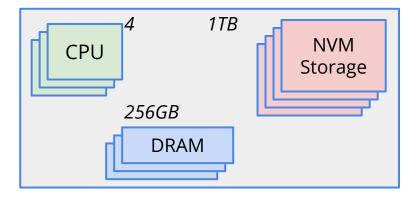
# **Networking Storage**

**Question 1:** why do we want to network storage?

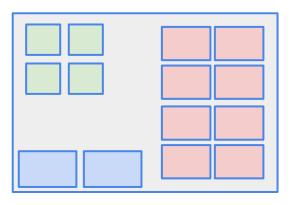
**Question 2:** what do you think when I say networked storage? (ever heard of NAS, SAN, FC, iSCSI?)



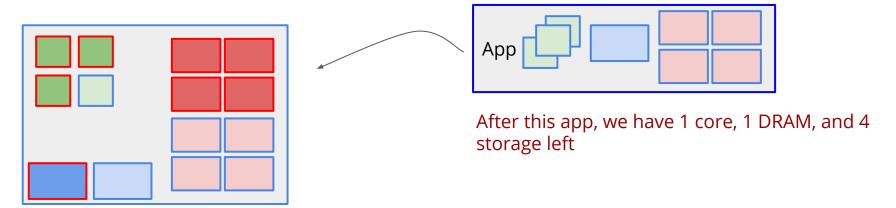
What do we have inside a single server: CPU cores, DRAM, and some storage



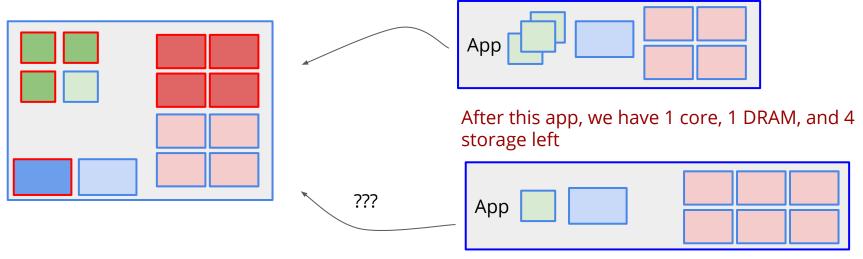
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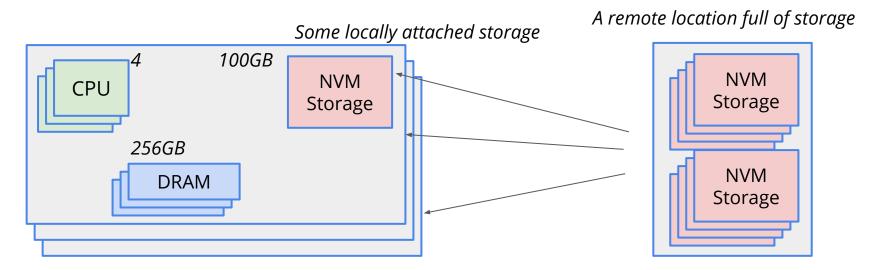
#### What happens if an application needs

- 3 cores only, or 5 cores?
- 1.1 TB of NVM or only 500 GB?
- 128GB of DRAM, or 512 GB of DRAM

#### Issues

- Low resource utilization
- High cost of running infrastructure
  - Total cost of ownership (TCO)

### Idea: Disaggregation (Storage)

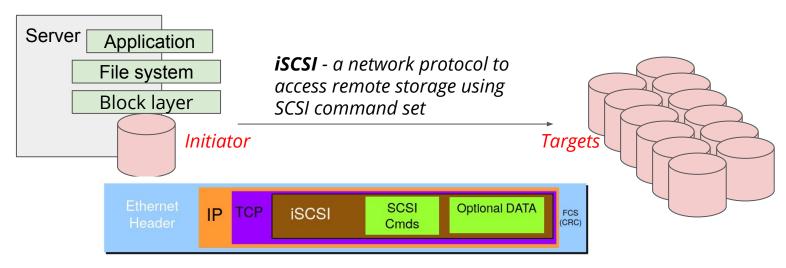


Slice out and give out storage capacity from a remote location (dedicated storage servers)

**The idea is not new**: this is how even HDD based storage systems are also deployed Benefits:

- (i) on-demand device capacity provisioning, no underutilization
- (ii) centralized provisioning, and management, a single point of upgrade to all
- (iii) low cost TCO, as systems resources are fully utilized (with a mix of workloads)

### **How to Access Remote Storage - SAN**



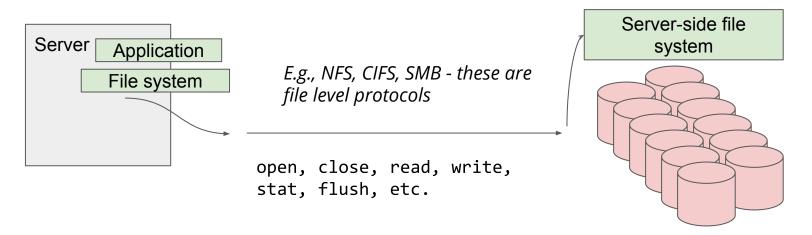
#### **Storage Area Network (SAN)**

- One of the most popular way of deploying "remote" block storage
- Block storage size can be anything, configured on demand (persistent or ephemeral)
- Deployable on the common data center networking infrastructure: Ethernet, TCP, IP There are other ways to do SAN as well like ATA over Ethernet (AoE), Fiber Channel (FC), etc.

IP Storage Protocols: iSCSI,

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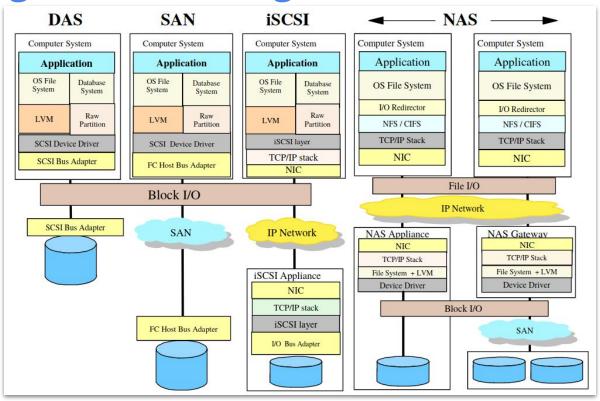
# **How to Access Remote Storage - NAS**



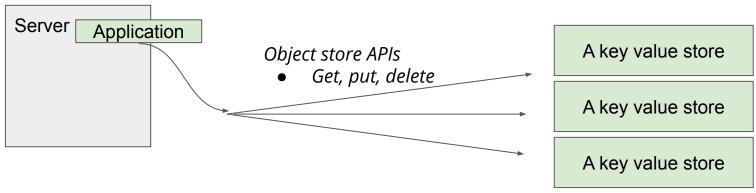
#### **Network Attached Storage (NAS)**

- Deployment abstraction is a file
  - can be a just a point-to-point file system (NFS)
  - o a shared, parallel file system (like GPFS, GFS) running on distributed block devices
- Capacity provisioning and scaling is done at the file system level In the cloud, similar example would include Hadoop FS

#### **Accessing Remote Storage**



#### **How to Access Remote Storage - Object**



If not being restricted to files or blocks for storage, objects are **flexible** (flat namespace, simple locking), **scalable** (can be distributed over multiple servers), and can support **multiple consistency models** 

Examples:







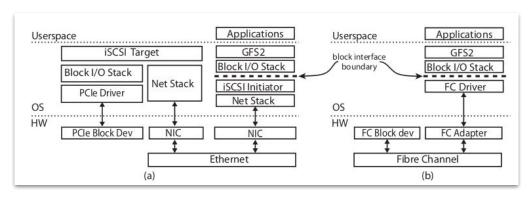
### What is the Basic Challenge Here?

Software Time

Network Time

Storage Media Time

Total operation latency. (Often) Mostly dominated by the storage media access time, that was HDD performance



### What is the Basic Challenge Here?

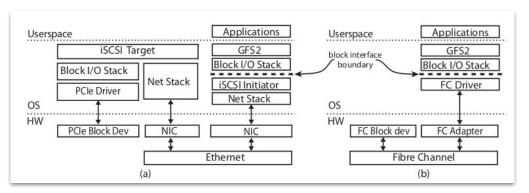
Software Time

Network Time

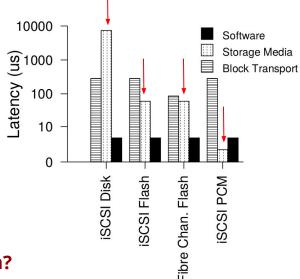
Storage Media Time

Total operation latency. (Often) Mostly dominated by the storage media access time,

that was HDD performance



As storage media access time improved, <u>software</u> and <u>network</u> time became the new bottlenecks - what can we do about them?



# **Understanding iSCSI with Disaggregated Flash**

#### Flash Storage Disaggregation

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#### Abstract

PCIe-based Flash is commonly deployed to provide datacenter applications with high IO rates. However, its capacity and bandwidth are often underutilized as it is difficult to design servers with the right balance of CPU, memory and Flash resources over time and for multiple applications. This work examines Flash disaggregation as a way to deal with Flash overprovisioning. We tune remote access to Flash over commodity networks and analyze its impact on workloads sampled from real datacenter applications. We show that, while remote Flash access introduces a 20% throughput drop at the application level, disaggregation allows us to make up for these overheads through resource-efficient scale-out. Hence, we show that Flash disaggregation allows scaling CPU and Flash resources independently in a cost effective manner. We use our analysis to draw conclusions about data and control plane issues in remote storage.

Categories and Subject Descriptors H.3.4 [Systems and Software]: Performance Evaluation

General Terms Performance, Measurement

Keywords Network storage, Flash, Datacenter

#### 1. Introduction

Flash is increasingly popular in datacenters of all scales as it provides high throughput, low latency, non-volatile storage. Specifically, PCIe-based Flash devices offer 100,000s of IO

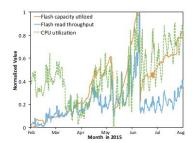
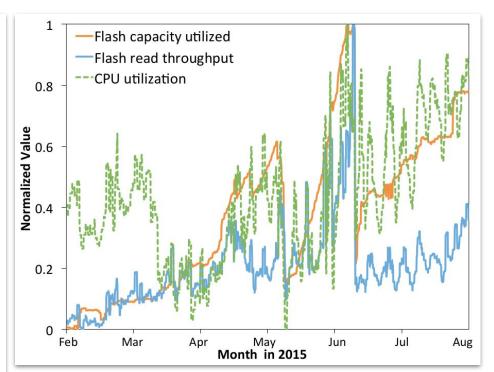


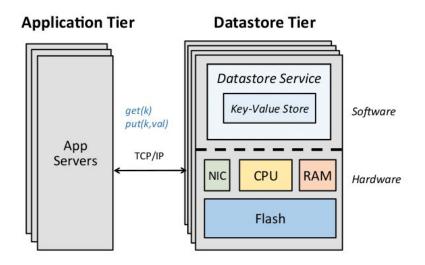
Figure 1: Sample resource utilization on servers hosting a Flashbased key-value store service at Facebook, normalized over a 6 month period. Flash and CPU utilization vary over time and scale according to separate trends.

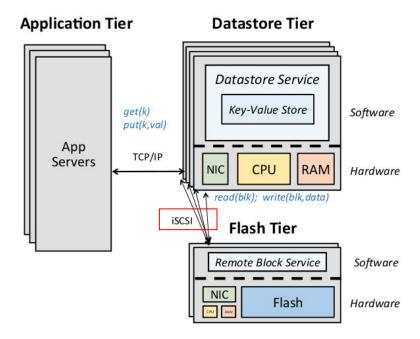
that generate web-page content use PCIe Flash. Similarly, Linkedln reports using PCIe SSDs to scale its distributed key-value database, Project Voldemort [45], to process over 120 billion relationships per day [28].

Designing server machines with the right balance of



# **Deployment Setup with Disaggregated Flash**

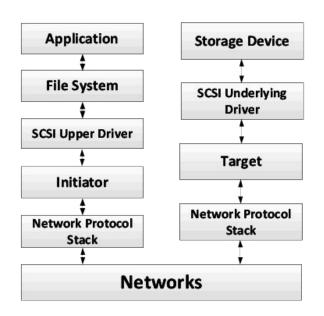




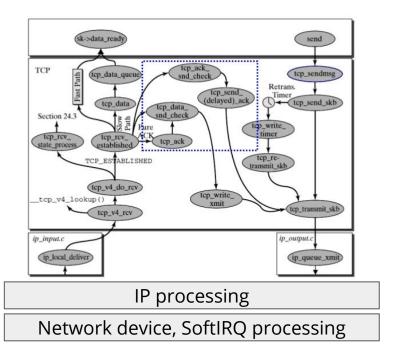
(a) Direct-attached (local) Flash

(b) Disaggregated (remote) Flash

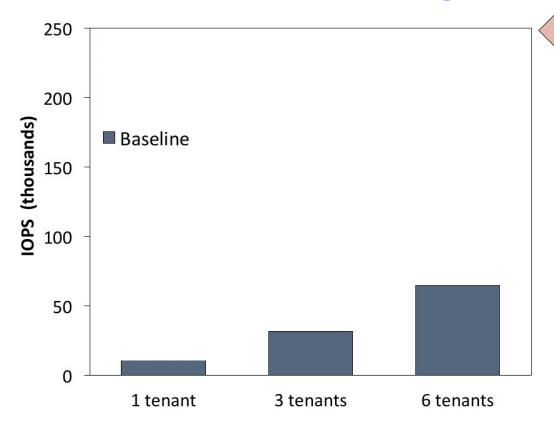
# iSCSI Processing (+Networking) in Linux



- Initiator and Target iSCSI terminology
- iSCSI become a high-level protocol on top of conventional TCP/IP processing



For more details, see Advanced Network Programming, <a href="https://canvas.vu.nl/courses/49125">https://canvas.vu.nl/courses/49125</a>

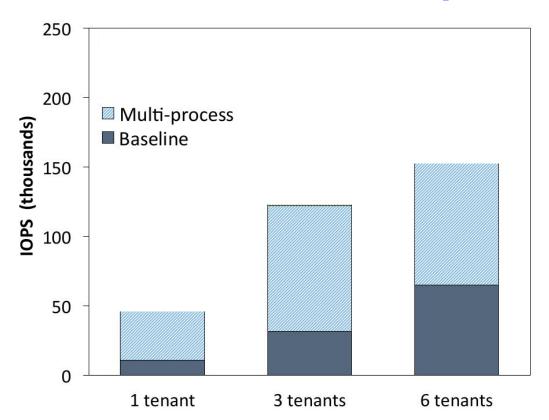


Flash capacity

Server contain Dual socket Xeon processors, 64GB RAM, and network connection of 10 Gbps between tenants (i.e., initiators, datastores) and the iSCSI target

Local performance of flash is at 250K IOPS (random 4kB IOPS)

At 10.5K IOPS iSCSI single client performance - **bottleneck:** CPU performance at the target



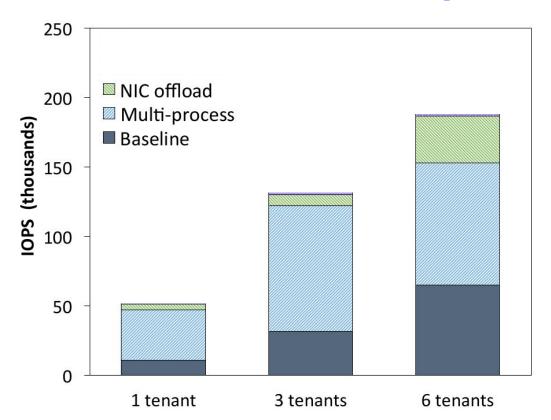
# Optimize network processing scalability

By default iSCSI uses 1 request processing thread per session

Use multiple threads per session to leverage multicore systems (use 6 out of the 8 cores available, why?)

Almost 4-5x gains

(not shown) With 8 tenants it can do 250K (device bounded)

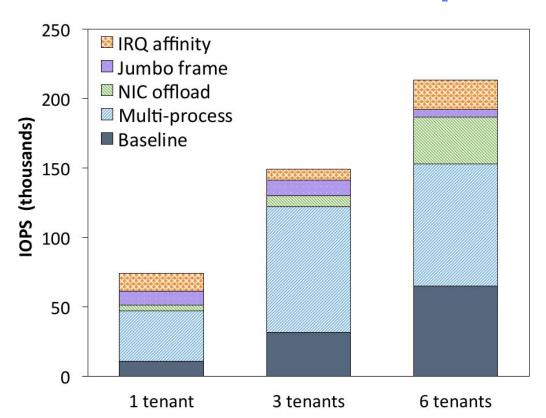


#### **Optimize network offloading**

Enable TSO and LRO offloading

TCP segmentation offloading (TSO) Large receive offloading (LRO)

These network controller features help to reduce per packet overheads by coleasing multiple 1500 bytes packets into a large segments (~64kB)



#### **Optimize network offloading**

Enable jumbo frames and IRQ affinity

Jumbo frames: default Ethernet frames are 1500 bytes, jumbo frames 9000 bytes

→ Help to reduce per packet overheads

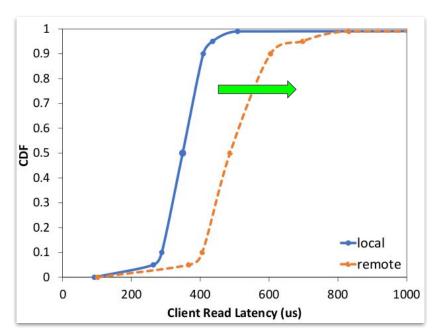
IRQ affinity is used to distributed interrupts from NICs to all cores for scalable processing

### **Application-Level Performance**

Run RocksDB on disaggregated flash devices

Remote flash does increase the 95th percentile latency by <u>260µs</u>

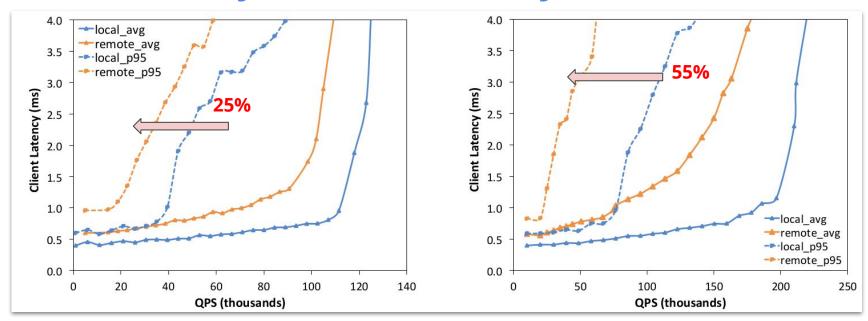
- Is this acceptable? Depends upon the application. If your SLOs are in mseconds then yes
  - o FB's use-cases are in mseconds
- If they are in 100 useconds No



Unloaded latency

What happens when multiple tenants share flash devices over the network?

#### **Multi-Tenancy Loaded Latency**



Comparison points: **local** (when each tenant has its own local flash) vs. **remote** when shared between 2 (left) and 3 (right) tenants

**Observations:** QPS is degraded by ~20%, but tail suffers significantly as we increase multi-tenancy

# When does Disaggregation Make Sense?

Let's do a first order approximation for the benefits of disaggregation

$$C_{direct} = \max \left( \frac{GB_t}{GB_s}, \frac{IOPS_t}{IOPS_s}, \frac{QPS_t}{QPS_s} \right) \cdot \left( f + c \right) \qquad C_{disagg} = \max \left( \frac{GB_t}{GB_s}, \frac{IOPS_t}{IOPS_s} \right) \cdot \left( f + \delta \right) + \left( \frac{QPS_t}{QPS_s} \right) c$$

Maximum capacity required

Maximum capacity per machine/

Sum of flash + compute capacity in a single server

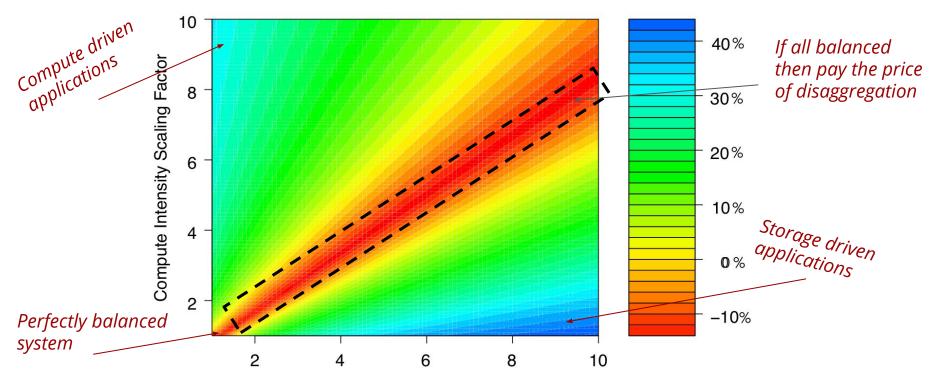
What are the minimum number of servers needed to support an application?

$$C_{disagg} = \max\left(\frac{GB_t}{GB_s}, \frac{IOPS_t}{IOPS_s}\right) \cdot (f + \delta) + \left(\frac{QPS_t}{QPS_s}\right) \cdot (f + \delta)$$

*Only flash requirements Multiplied with the cost of flash* + disaggregation overheads (20%)

> Completely separate scaling of compute/memory requirements

#### When does Disaggregation Make Sense



**When does disaggregation makes sense:** when compute and storage demands scale at a different rate (which in real world happens often)

#### What are the Challenges with Storage Disaggregation

- 1. Come up with a better protocol than iSCSI? (hint: we did already for locally connected flash)
- 2. What can we do to improve multi-tenancy for disaggregated flash?
- 3. What kind of joint network and storage optimizations we can do to decrease the software cost of accessing remote storage?
- 4. Come up with a better remote data access API than just simple block, files, or objects?

And many other variants of these themes, let's start with a better protocol

### **Faster Storage Needs a Faster Network**

We are seeing networking performance improve from **100 Gbps to 200 Gbps and 400 Gbps** 

They can deliver < 1-10 usec latencies to access remote DRAM buffers

New ways of doing network operations like RDMA enabled networks like InfiniBand, iWARP, RoCE



Allows network processing inside the network controller (not the CPU)

How do we leverage the performance of these networks with storage?

#### **NVM Express over Fabrics (NVMe-oF)**

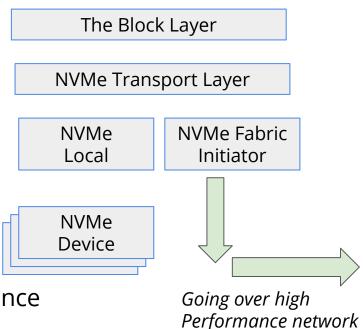
#### **NVM Express**

- Command and completion queues
- PCIe directly mapped queues
- Light-weight protocol

NVMe over Fabrics is a networked extension of this idea

What is "<u>Fabrics</u>" here?

It is an umbrella term to cover high-performance networks like RDMA networks



#### Remote Direct Memory Access (RDMA)

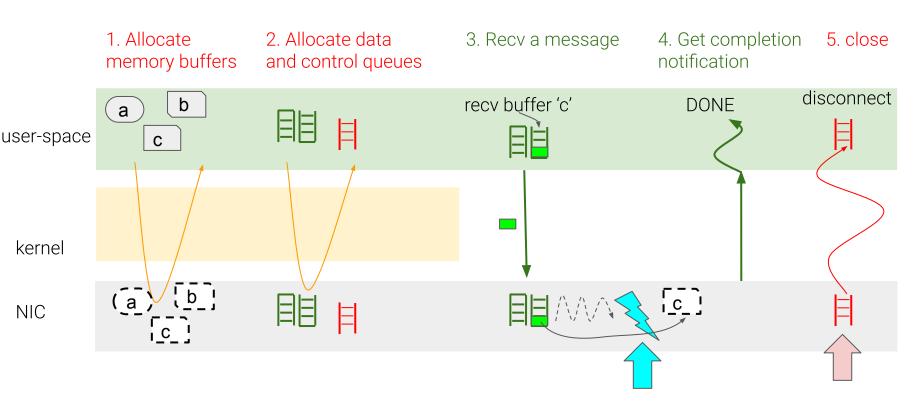
A Userspace networking technology, applications have

- Directly mapped RX/TX queues in the userspace
- Can execute send/recv commands
- Can execute remote memory read/write operations
- Poll or interrupt driven completion notifications
- All networking processing is offloaded to the hardware (Network controller)

The interesting thing for us here is that RDMA is also (i) a queue-based; (ii) post commands; (iii) poll for completion - type network operation

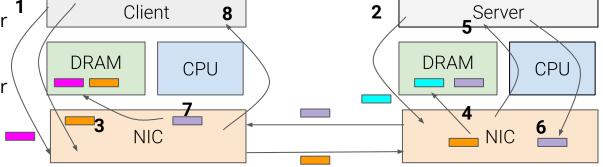
Animesh Trivedi, Patrick Stuedi, Bernard Metzler, Roman Pletka, Blake G. Fitch, and Thomas R. Gross. 2013. Unified high-performance I/O: one stack to rule them all. In Proceedings of the 14th USENIX conference on Hot Topics in Operating Systems (HotOS'13). USENIX Association, USA, 4.

### **RDMA Operations**



#### **RDMA: Two-Sided Send Recv Operations**

- Client posts a receive buffer (pink)
- Server posts a receive buffer (cyan)
- 3. Client sends a buffer to the server (orange)
- Server's NIC receives the buffer and deposit it into the cyan buffer
- 5. NIC notifies the server
- 6. Server prepares a response and send back the purple buffer
- 7. Client NIC receives the purple buffer and deposit it into the pink buffer
- 8. NIC notifies the client



#### **RDMA: One-sided Read Operation**

Hey! Your content is stored in the buffer at 'raddr' (+ a tag, called steering tag or Stag)

Client

Server

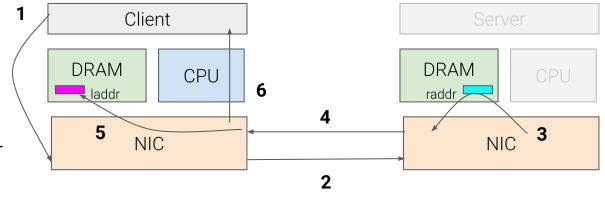
DRAM
CPU

DRAM
raddr
ROPU

NIC

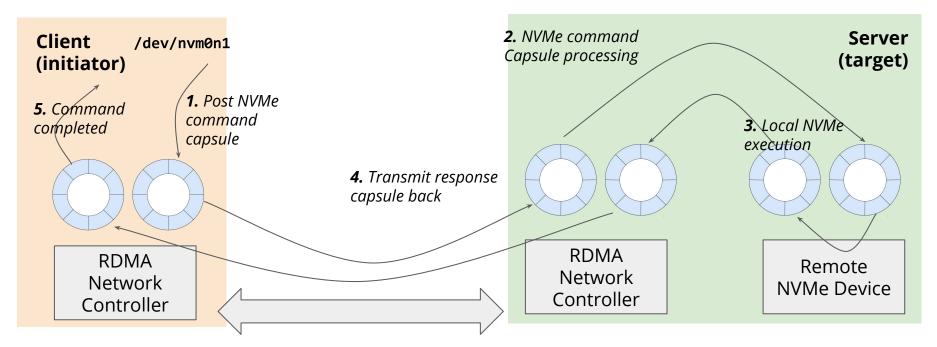
### **RDMA: One-sided Read Operation**

- 1. Client: READ remote memory address (raddr) to local address (laddr)
- 2. Client: posts READ request
- 3. Server: read local (raddr) local DMA operation
- 4. Server: TX data back to client NIC
- 5. Client: local DMA to (laddr) buffer in DRAM
- 6. Client: interrupt the local CPU/OS to notify completion about the client's READ operation



RDMA operations are like remote "DMA" - defined for specific remote memory locations

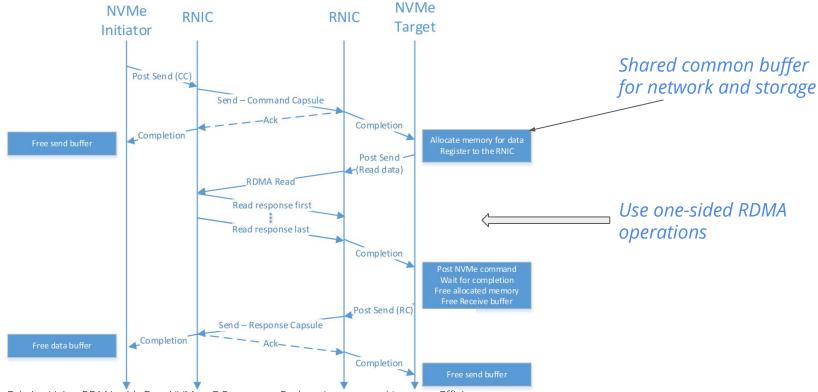
### **NVMe-oF = RDMA + NVM Express**



High-performance Network, 100 Gbps Ethernet

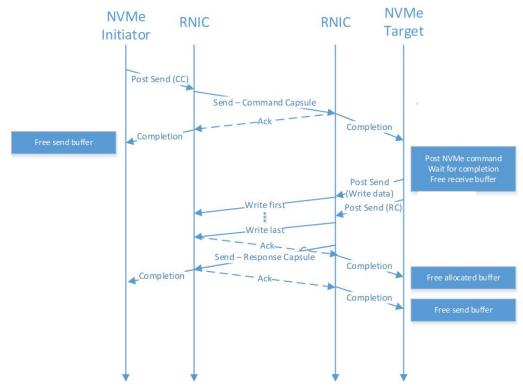
At no point in time we have to use any legacy protocols like SCSI, or socket/TCP network transfers

#### **NVMe-oF Write**

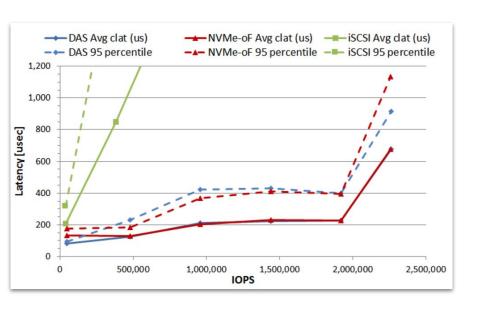


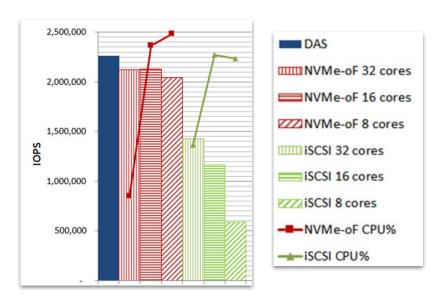
Ethernet Storage Fabrics Using RDMA with Fast NVMe-oF Storage to Reduce Latency and Improve Efficiency, <a href="https://www.snia.org/educational-library/ethernet-storage-fabrics-using-rdma-fast-nyme-storage-reduce-latency-and-improve">https://www.snia.org/educational-library/ethernet-storage-fabrics-using-rdma-fast-nyme-storage-reduce-latency-and-improve</a>

### **NVMe-oF Read**



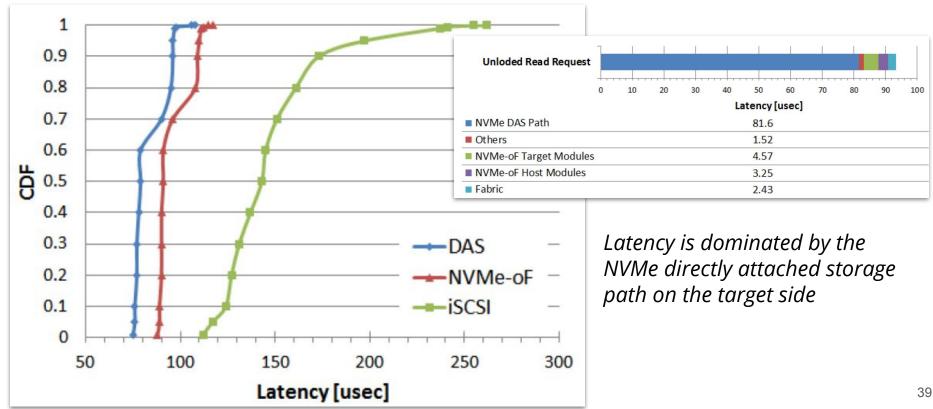
### **NVMe-oF Performance**





In comparison to iSCSI, NVMoF provides performance very close to a locally attached storage

## **NVMe-oF Latency Performance**



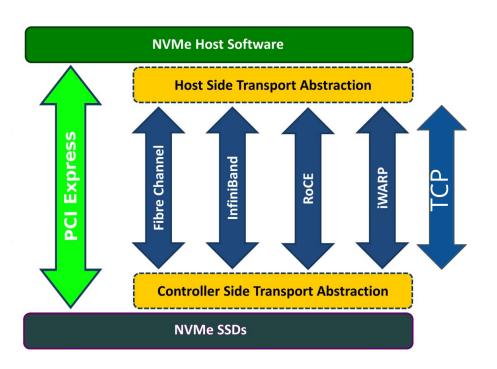
### **NVM over Fabrics**

Is the dominant and standard way to deploy networked flash

Supports various high-performance Networks like RDMA

 New specification on TCP is now also available (not offloaded)

Is constantly being updated to accommodate new changes



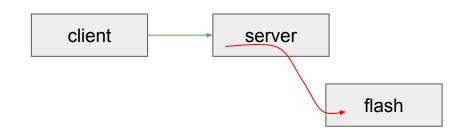
# Thinking outside the Box

NVMe-oF is equivalent to iSCSI (hence, a SAN solution)

RDMA allows to read/write remote memories directly

Quite popular inside data center due to its performance to build

- Key-value stores and caches
- Transaction systems
- File systems
- Distributed data structures
- Consensus and ordering



Can we think of extending RDMA operations to directly access Flash location?

### FlashNet: Building a Unified Network/Storage Stack

### FlashNet: Flash/Network Stack Co-Design

ANIMESH TRIVEDI, NIKOLAS IOANNOU, BERNARD METZLER, PATRICK STUEDI, JONAS PFEFFERLE, and KORNILIOS KOURTIS, IBM Research, Zurich, Switzerland IOANNIS KOLTSIDAS, Google

THOMAS R. GROSS, ETH Zurich, Switzerland

During the past decade, network and storage devices have undergone rapid performance improvements, delivering ultra-low latency and several Gbps of bandwidth. Nevertheless, current network and storage stacks fail to deliver this hardware performance to the applications, often due to the loss of I/O efficiency from stalled CPU performance. While many efforts attempt to address this issue solely on either the network or the storage stack, achieving high-performance for networked-storage applications requires a holistic approach that considers both.

In this article, we present FlashNet, a software I/O stack that unifies high-performance network properties with flash storage access and management. FlashNet builds on RDMA principles and abstractions to provide a direct, asynchronous, end-to-end data path between a client and remote flash storage. The key insight behind FlashNet is to co-design the stack's components (an RDMA controller, a flash controller, and a file system) to enable cross-stack optimizations and maximize I/O efficiency. In micro-benchmarks, FlashNet improves 4kB network I/O operations per second (IOPS by 38.6% to 1.22M, decreases access latency by 43.5% to 50.4µs, and prolongs the flash lifetime by 1.6-5.9x for writes. We illustrate the capabilities of FlashNet by building a Key-Value store and porting a distributed data store that uses RDMA on it. The use of FlashNet's RDMA API improves the performance of KV store by 2× and requires minimum changes for the ported data store to access remote flash devices.

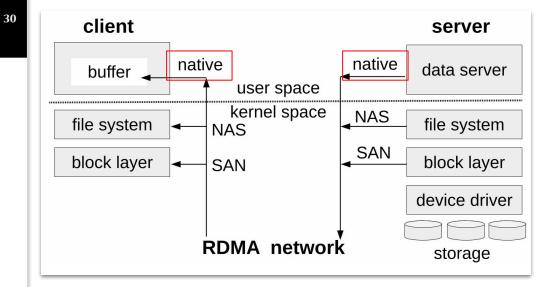
 $\begin{array}{l} {\sf CCS\ Concepts: \bullet Information\ systems \to Storage\ network\ architectures; Flash\ memory; \bullet Networks \\ \to Network\ performance\ evaluation; \bullet\ Software\ and\ its\ engineering\ \to\ Operating\ systems; } \end{array}$ 

Additional Key Words and Phrases: RDMA, flash, network storage, performance, operating systems

#### **ACM Reference format:**

Animesh Trivedi, Nikolas Ioannou, Bernard Metzler, Patrick Stuedi, Jonas Pfefferle, Kornilios Kourtis, Ioannis Koltsidas, and Thomas R. Gross. 2018. FlashNet: Flash/Network Stack Co-Design. ACM Trans. Storage 14, 4, Article 30 (December 2018), 29 pages.

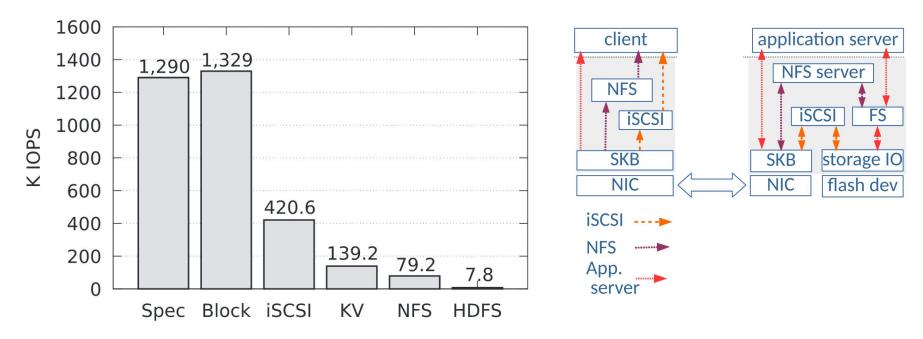
https://doi.org/10.1145/3239562



PhD Thesis, A. Trivedi, End-to-End Considerations in the Unification of High-Performance I/O, <a href="https://doi.org/10.3929/ethz-a-010651949">https://doi.org/10.3929/ethz-a-010651949</a>

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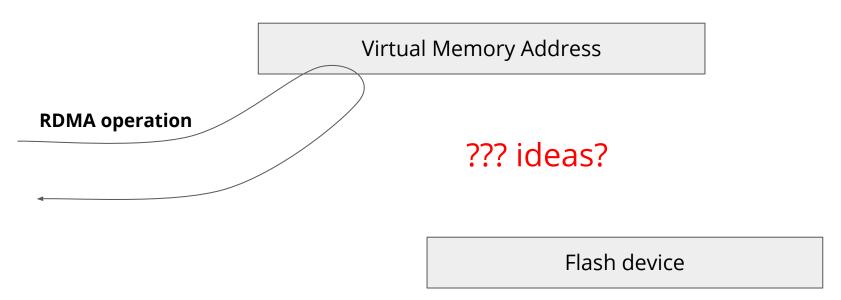
## **Number of Steps to Access Remote Data**



Going over block protocols (iSCSI), application (KV), file system (NFS), or cloud-FS (HDFS) costs performance (mix of network and storage overheads)  $\rightarrow$  can we do something better?

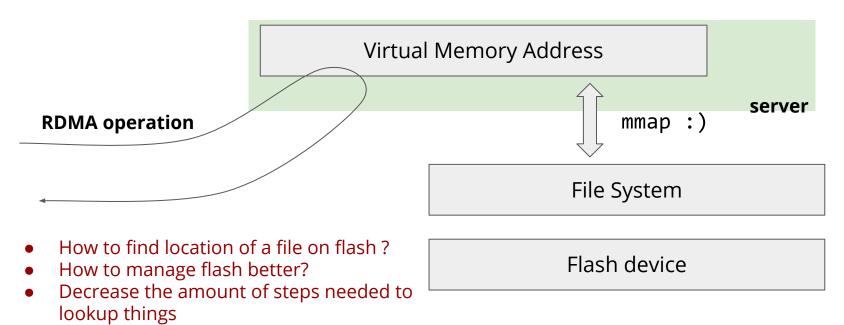
### FlashNet: Basic Challenge

RDMA operations are defined for a memory location, how do we get a memory location for a flash?



### FlashNet: Basic Challenge

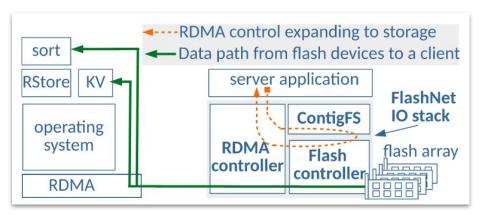
RDMA operations are defined for a memory location, how do we get a memory location for a flash?



### FlashNet Stack

### <u>Co-development</u> of a software:

- Flash Controller
- 2. File system
- 3. RDMA Controller



RDMA controllers helps to on-demand fetch pages from the file system

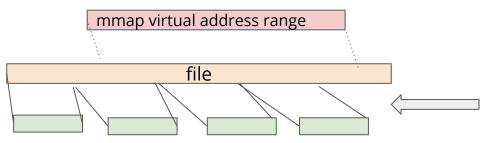
File system is like DFS, hence, large contiguous files (easy offset calculation)

Flash controller manages devices and uses RDMA access statistics for flash device management and page sharing between I/O operations

Put together they help to translate quickly between a network request and flash address

### **Abstraction Translation**

RDMA identifies a memory location using a tag



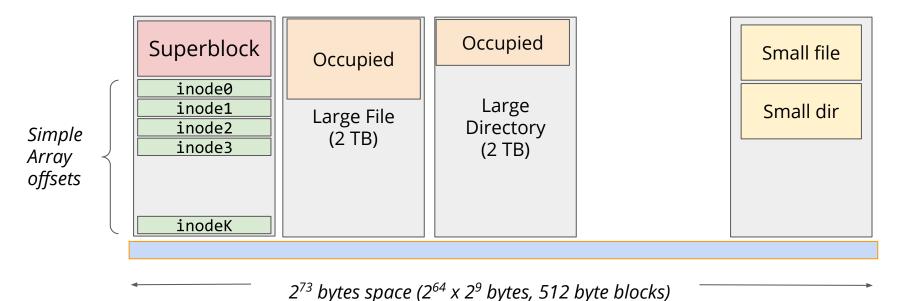
Flash logical address locations (FS does this translation)

This file offset to a local on flash LBA is done by the file system (ext4, F2FS)

So for any random file offset you need to ask from the file system where is the data stored

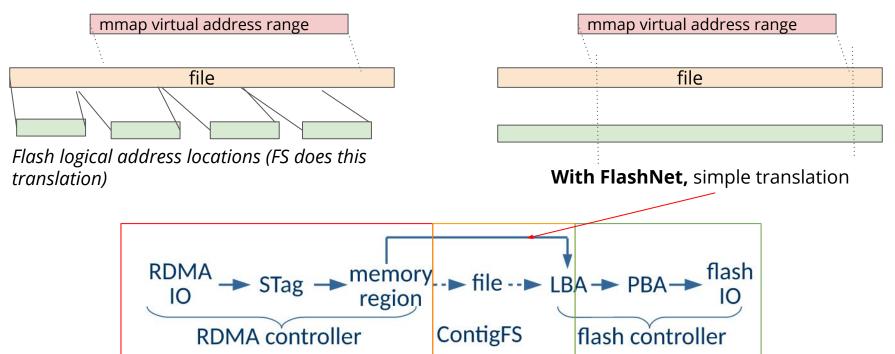
# **ContigFS**

- Builds on the same idea as DFS (lecture 4) on virtualized flash devices
- All files are contiguously allocated (logically)

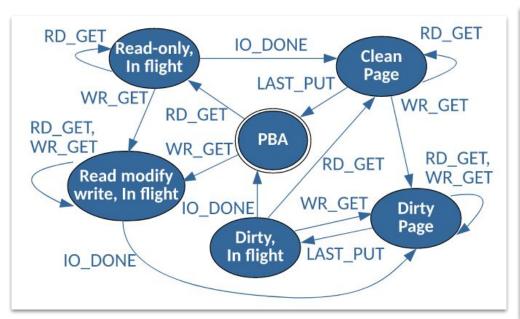


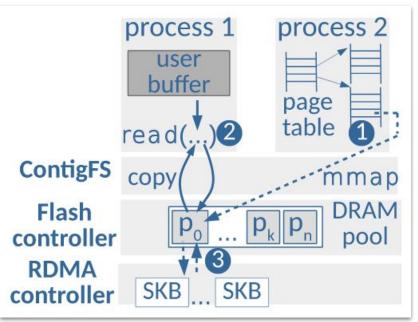
### **Abstraction Translation**

RDMA identifies a memory location using a tag



# Flash Page Management

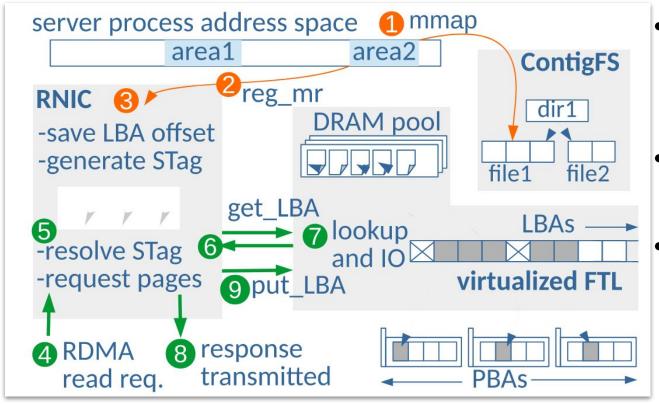




Flash pages in the host go through this state machine when in use

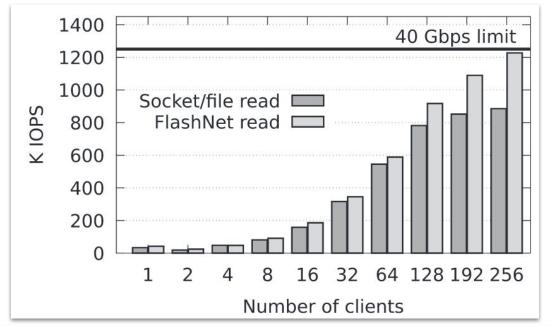
A simple shared DRAM page pool where all I/O happens

## **A Complete Operation**



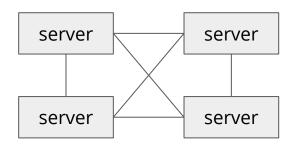
- DRAM DMA pool is shared between
  - o RDMA
  - Mmap
  - Local read/write
- get/put LBAs counters help with identifying hot and cold flash pages
- An RDMA controller can easily do an offset calculation from a virtual address to a flash LBA address (hence, no need to involved the file system)

# **Application-Driven Remote Storage Access**



	network	storage	I/O drivers	scheduling	kernel	app-logic	misc.
Socket/file	19.3%	7.3%	6.7%	15.8%	40.1%	4.7%	6.1%
FlashNet	20.6%	0.8%	6.4%	8.4%	46.7%	11.7%	5.4%

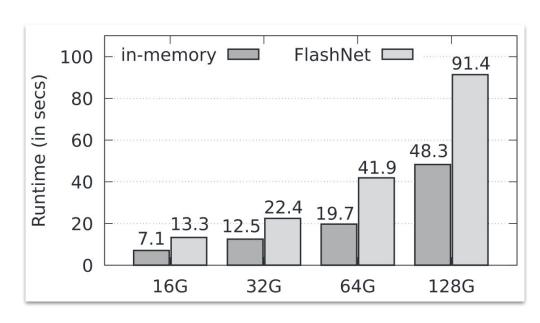
# **Application Performance**



Doing a distributed sorting over 4 machines

In-memory all data is stored in memory, all network traffic is using RDMA

FlashNet, all data is in Flash, and accessed using the "same" RDMA operations



The performance gap is purely from flash I/O performance

# What you should know from this lecture

- What is Storage Disaggregation and why is it useful
- 2. What are the options to access data stored on a remote server
  - a. Storage Area Network: iSCSI (block)
  - b. Network Attached Storage: NFS (files)
  - c. Object/Key-Value stores: like S3, redis (application-driven protocols)
- 3. What is NVMe-oF and how does it relate to RDMA networking
- 4. Why was NVMe-oF invented
- 5. What is FlashNet and what does it tries to optimize
- 6. How does FlashNet (an application-level RDMA operation) related to NVMe-oF (a block-level protocol)

# **Further Reading**

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- Animesh Trivedi, Nikolas Ioannou, Bernard Metzler, Patrick Stuedi, Jonas Pfefferle, Kornilios Kourtis, Ioannis Koltsidas, and Thomas R. Gross. 2018. FlashNet: Flash/Network Stack Co-Design. ACM Trans. Storage, 2018.
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   Unified high-performance I/O: one stack to rule them all. In ACM HotOS 2013.
- Brendan Cully, Jake Wires, Dutch Meyer, Kevin Jamieson, Keir Fraser, Tim Deegan, Daniel Stodden, Geoffrey Lefebvre, Daniel Ferstay, and Andrew Warfield. 2014. Strata: scalable high-performance storage on virtualized non-volatile memory. In Proceedings of the 12th USENIX FAST, 2014.