

A

B

C

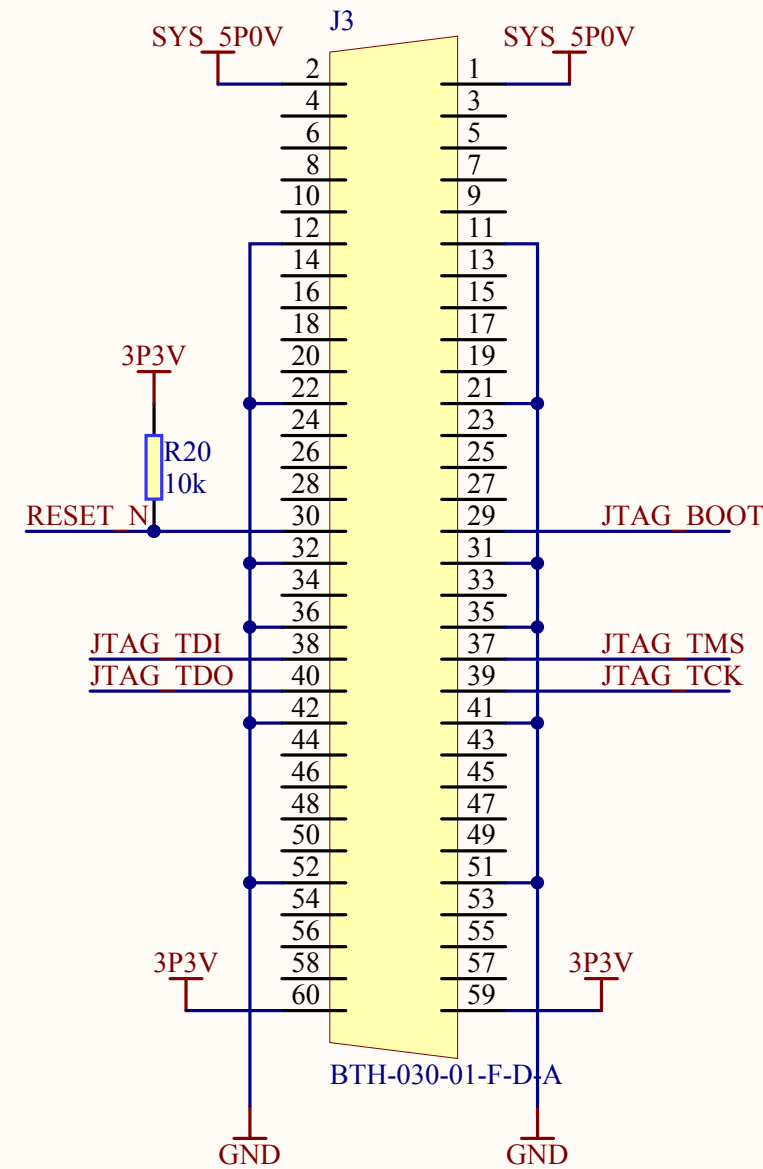
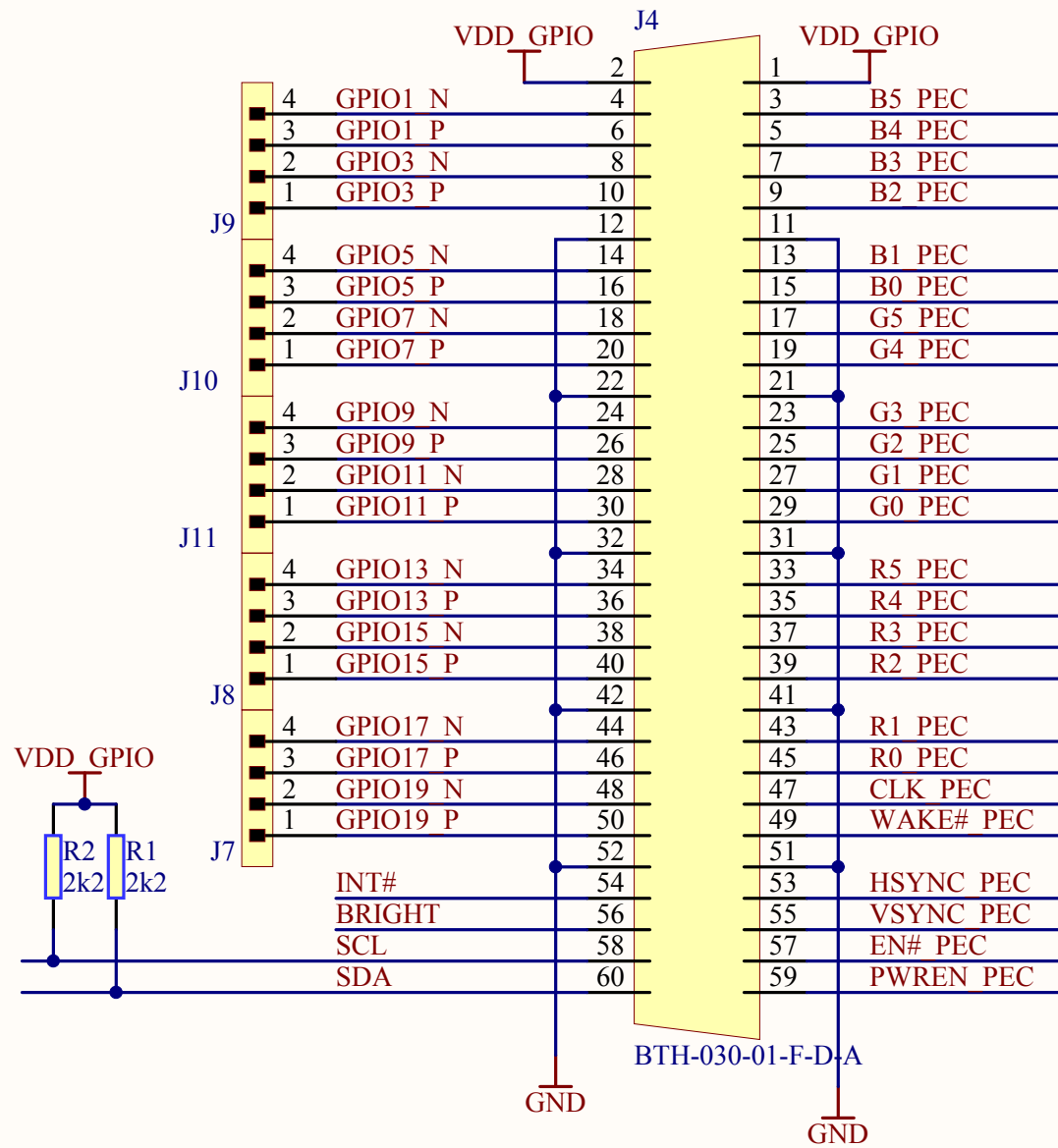
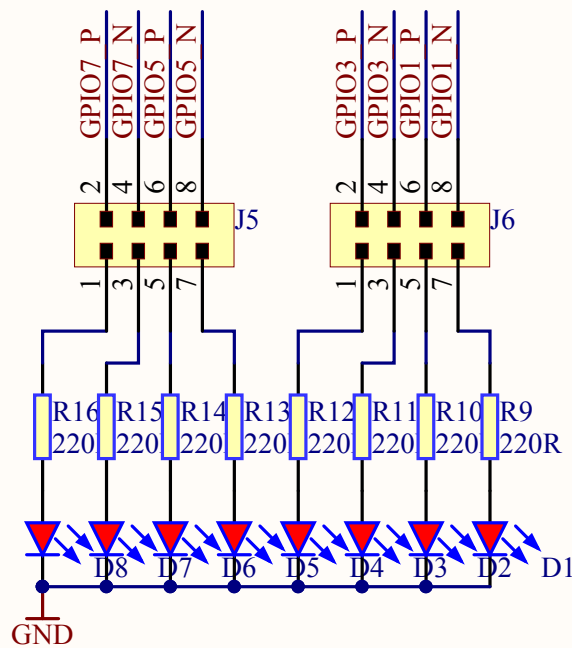
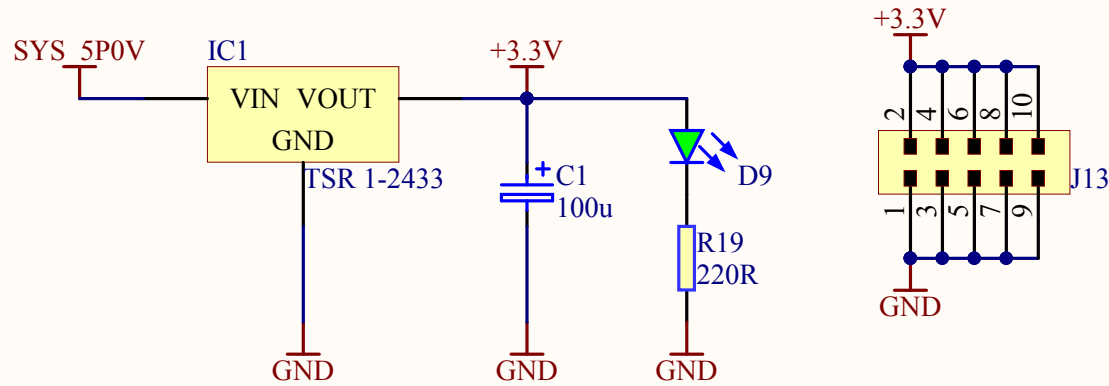
D

A

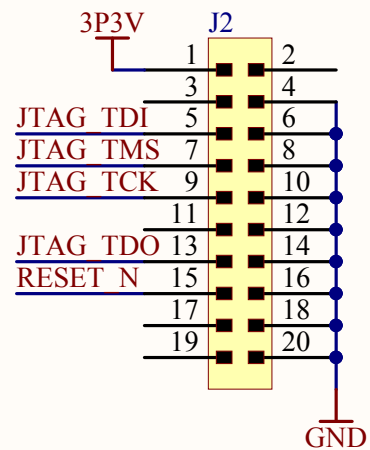
B

C

D



ARM 20-pin JTAG



Xilinx JTAG

