

Diagram of the connector pinout for the connector on the left. The connector is labeled J0 and CONNECTOR\_LEFT. The pins are numbered 1 to 10. The pin labels are: MPU\_INT (1), VDD (2), QMC\_SDA (3), QMC\_SCL (4), DRV\_IN2 (5), DRV\_IN1 (6), DRV\_OUT2 (7), DRV\_OUT1 (8), GND (9), and VMOT (10). Test points TP1, TP2, TP3, and TP4 are indicated on pins 2, 3, 4, and 9 respectively.

[illegible]

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