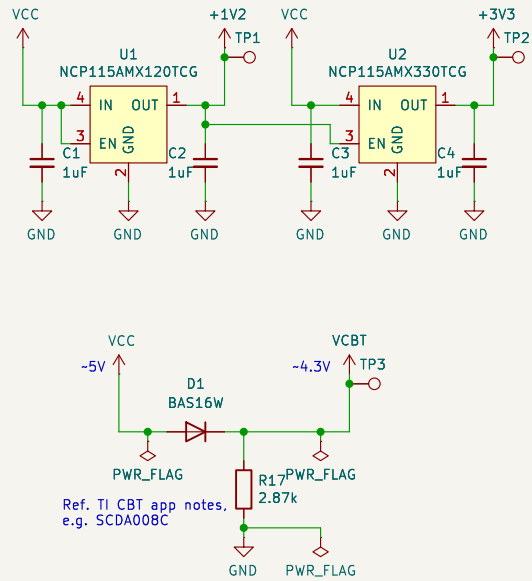
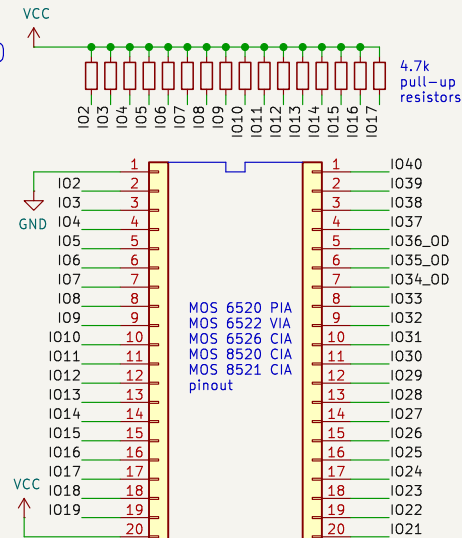


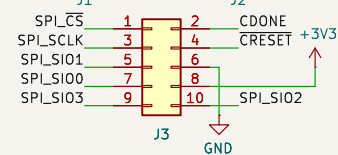
Power



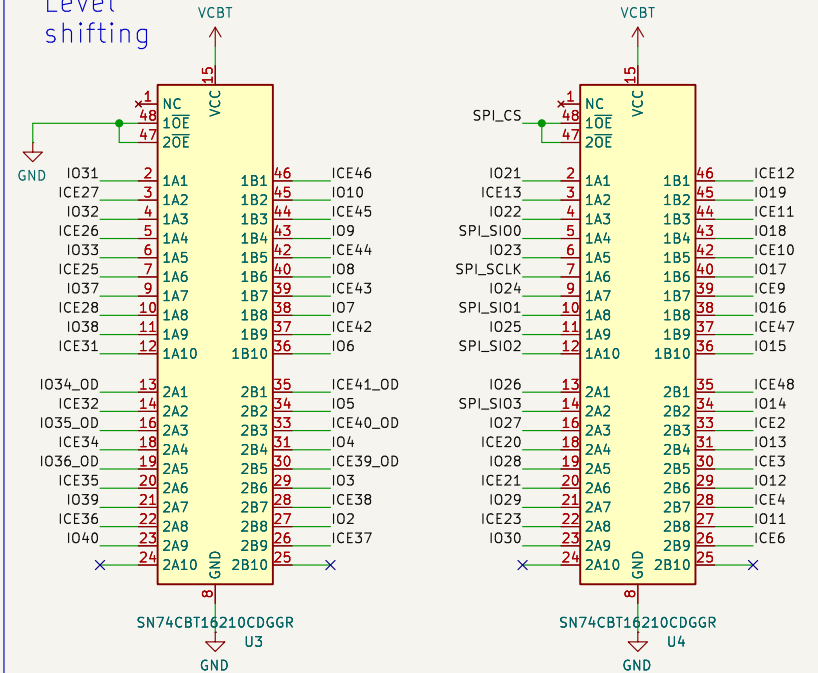
I/O



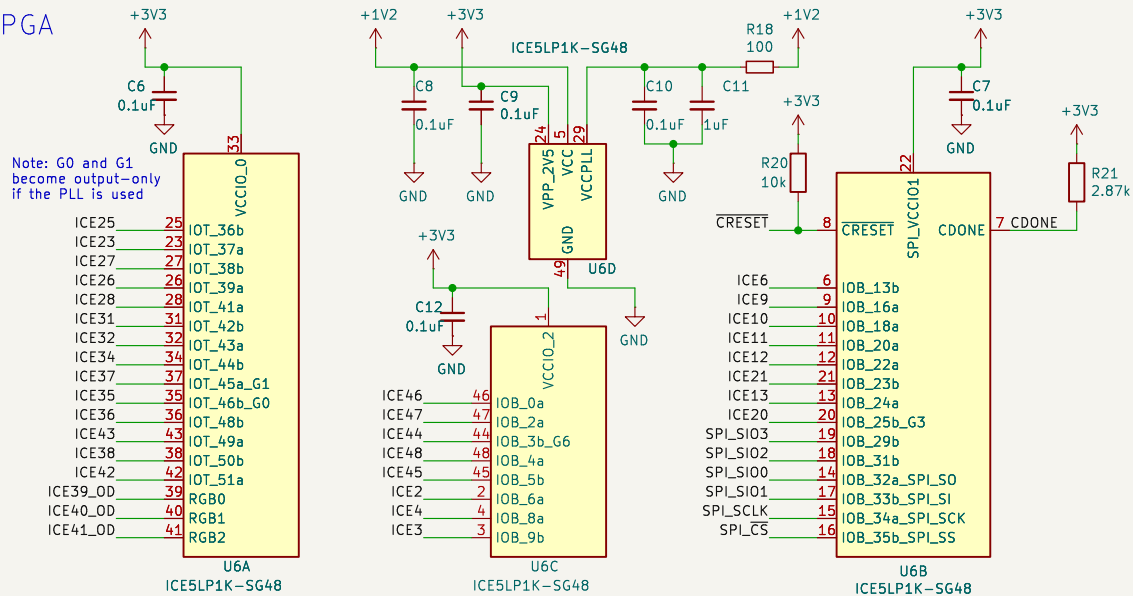
PRG



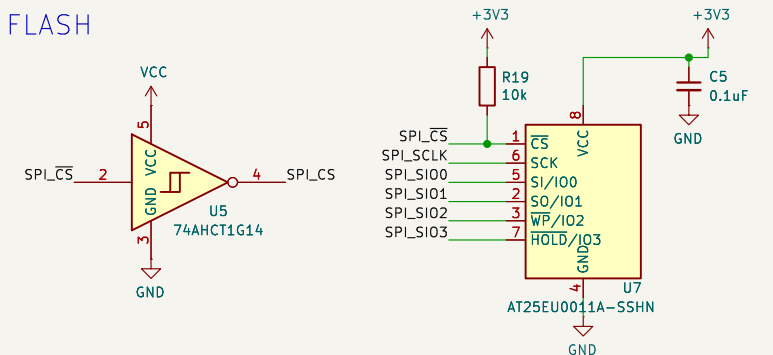
Level shifting



FPGA



FLASH



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