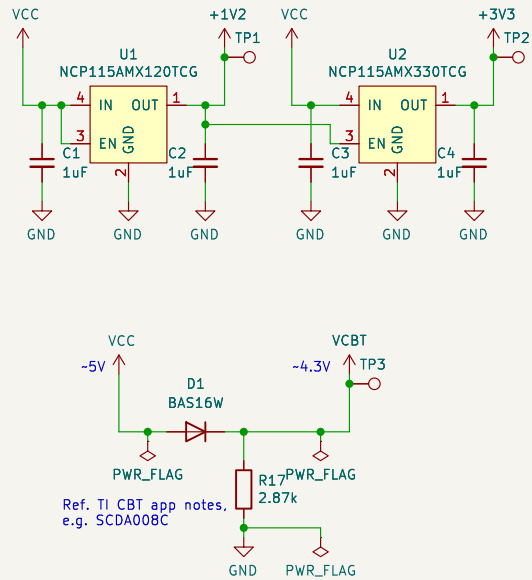
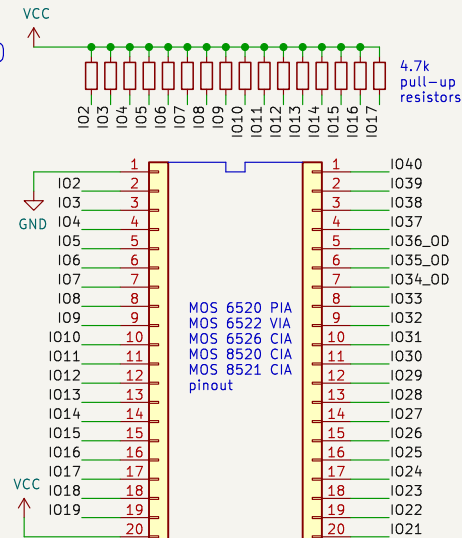


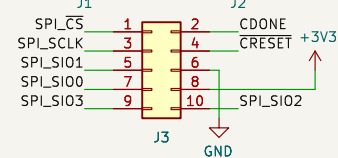
Power



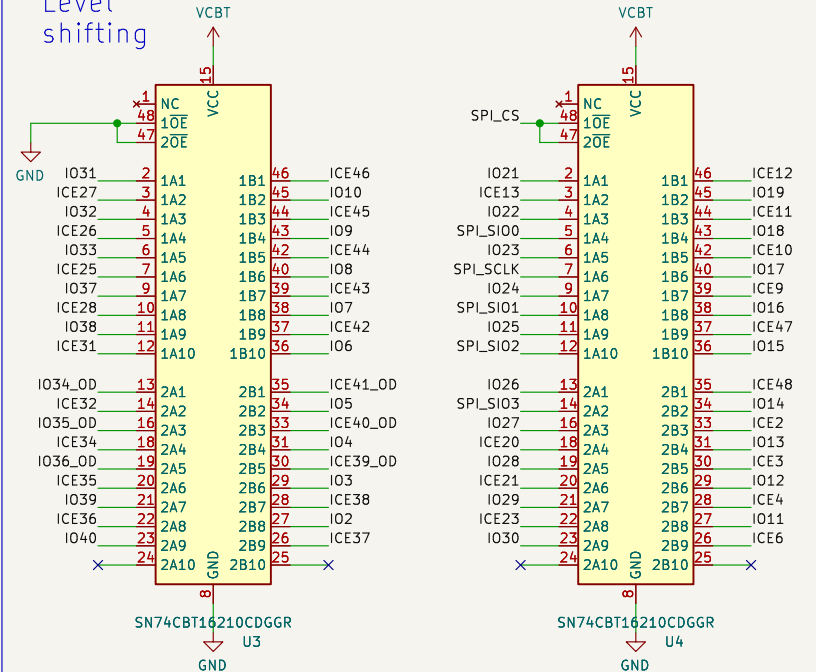
I/O



PRG



Level shifting



FPGA

