



# RISC-V based MYTH

*MYTH- Microprocessor for You in Thirty Hours*

## Workshop Certificate

*{26<sup>th</sup>-30<sup>th</sup> August 2020}*

**Rohit Kankal**

*has successfully completed  
an online cloud based workshop conducted on **VSD-IAT***

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|--|-------|
| <b>Skills learned</b> <ul style="list-style-type: none"><li>• RISC-V ISA Overview</li><li>• RISC-V GNU Tool-chain</li><li>• Spike debugging</li><li>• Digital Logic</li><li>• Pipelined CPU Design</li><li>• TL-Verilog</li><li>• Markerchip IDE</li></ul> | 17/17 |
| <b>Lab Skills Activities</b>   | 82/82 |
| <b>% of RISC-V Core<br/>Designed</b>   | 100%  |

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**Kunal Ghosh**  
*(Design & Sign-off Expertise, Founder VLSI System Design)*