

Digital Communication Lab

Laboratory report submitted for the partial fulfillment
of the requirements for the degree of

Bachelor of Technology
in
Electronics and Communication Engineering

by

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21UEC072

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Chapter 1

Experiment - 1

1.1 AIM

1. To generate Delta modulated and Demodulated signal.
2. Determine the conditions for slope overloading and step size.

1.2 Apparatus Used

- | | | | |
|-----------------------|-----------------------|---------------------------------|-------------------|
| 1. LM393 (Comparator) | 2. LM741 | 3. 74LS74 (D Flip Flop) | 4. Resistance |
| 5. Capacitor | 6.Connecting wires | 7.Breadboard | 8.DC Power Supply |
| 9.DSO Probe | 10.Function Generator | 11.Digital Storage Oscilloscope | |

1.3 Theory

Delta Modulation (DM) is a simplified PCM. In some type of signals, the neighboring samples are closely correlated with each other. Therefore, once a sample value is known this enables the determination of the following sample values most probably. Thus, instead of sending the real value of each sample at each time, differences (variances) between adjacent samples are sent in DM.

In DM, two-level quantizer and one-bit coding is used. Transmitted code pulses do not carry the data related to the message signal itself; instead they carry data regarding the differentials of the message function. The output of a delta modulator is a bit stream of samples at a relatively high rate, the value of each bit being determined according to whether the input message sample amplitude has increased or decreased relative to the previous sample.

1.3.1 Block Diagram of Delta Modulation and Demodulation

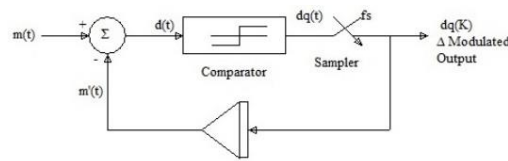


Figure: Delta Modulator

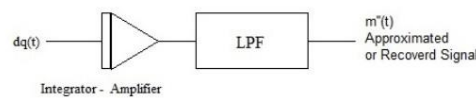


Figure: Delta Demodulator

Condition to avoid Slope overloading

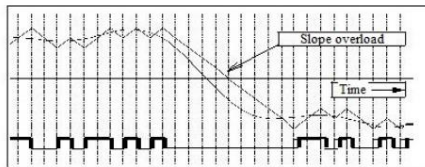


Figure: (a) Slope overload

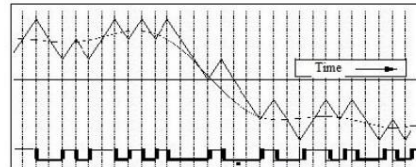


Figure: (b) Increased step size to reduce slope overload

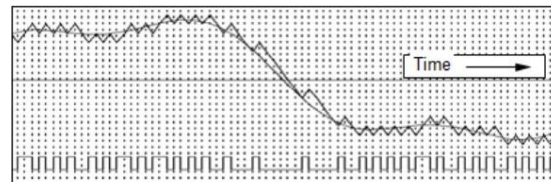


Figure: (c) increased sampling rate to reduce slope overload

This occurs when the sawtooth approximation cannot keep up with the rate-of-change of the input signal in the regions of greatest slope. The step size is reasonable for those sections of the sampled waveform of small slope, but the approximation is poor elsewhere. This is 'slope overload', due to too small a step. Slope overload is illustrated in Figure (a).

- To reduce the possibility of slope overload the step size can be increased (for the same sampling rate). This is illustrated in Figure (b). The sawtooth is better able to match the message in the regions of steep slope.
- An alternative method of slope overload reduction is to increase the sampling rate. This is illustrated in Figure (c), where the rate has been increased by a factor of 2.4 times, but the step is the same size as in Figure (a).

Figure/Connection Diagram:

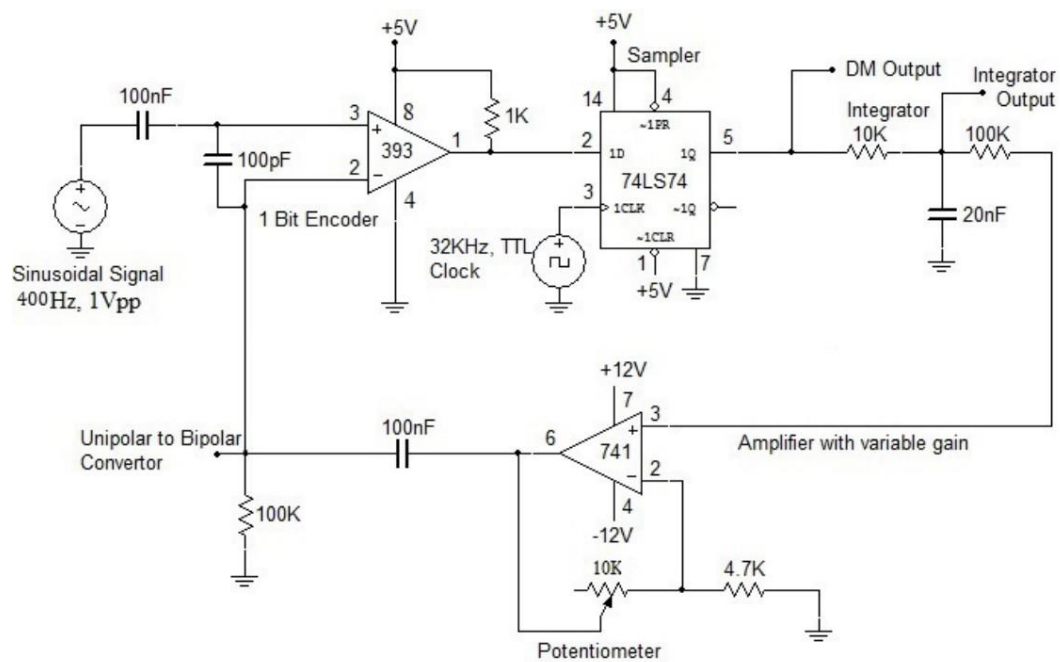


Figure 1.1: Delta Modulation

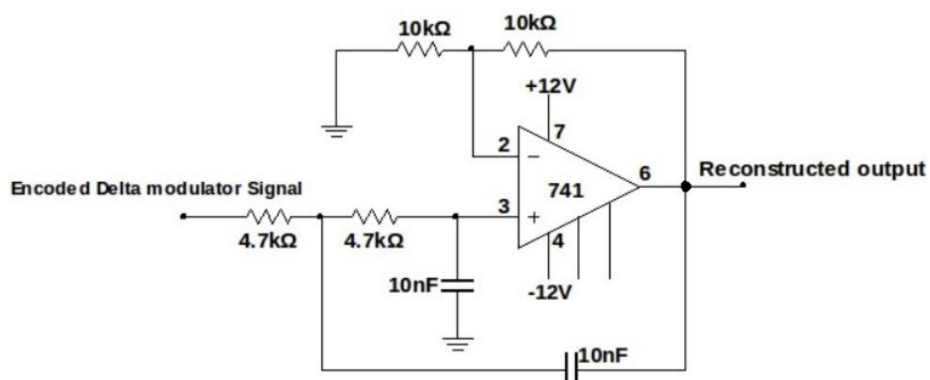


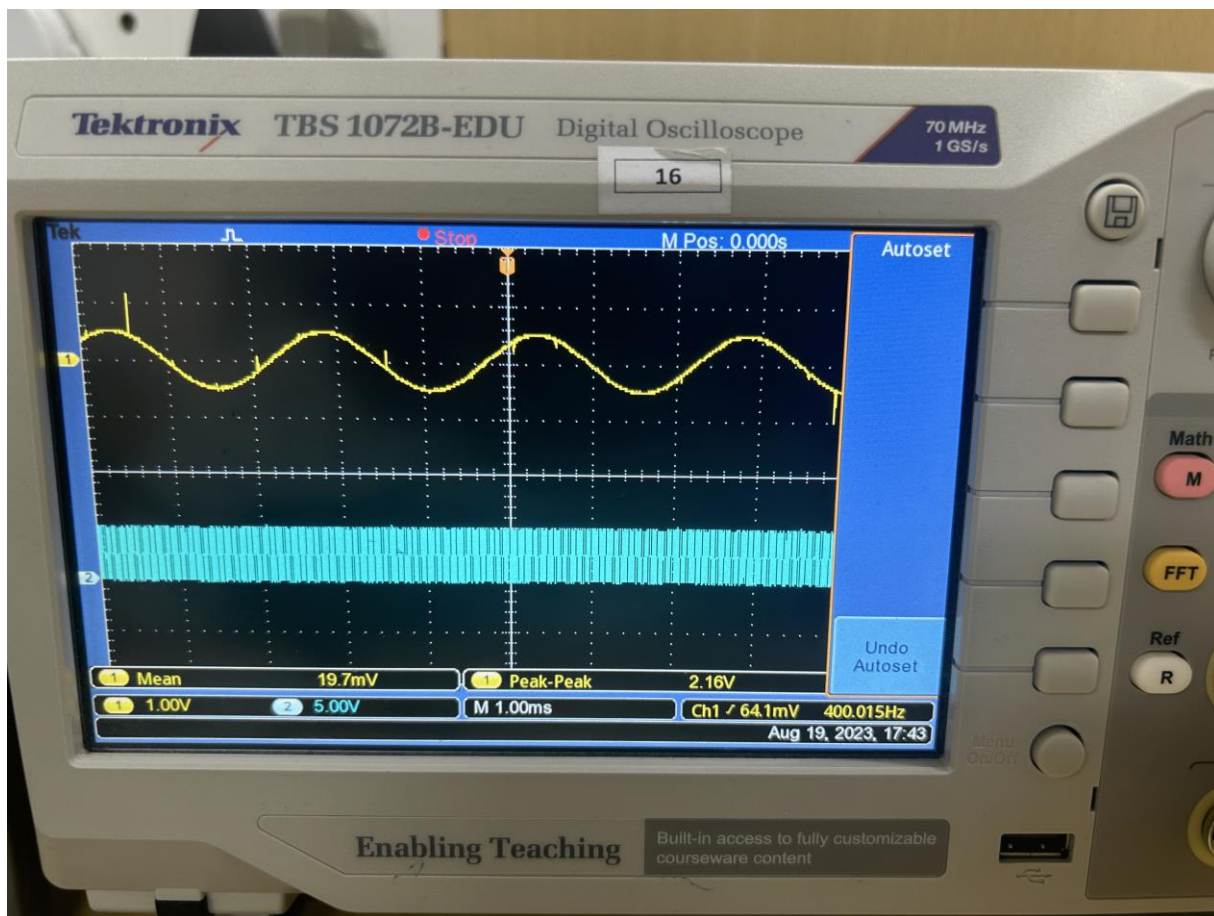
Figure 2: Delta Demodulation

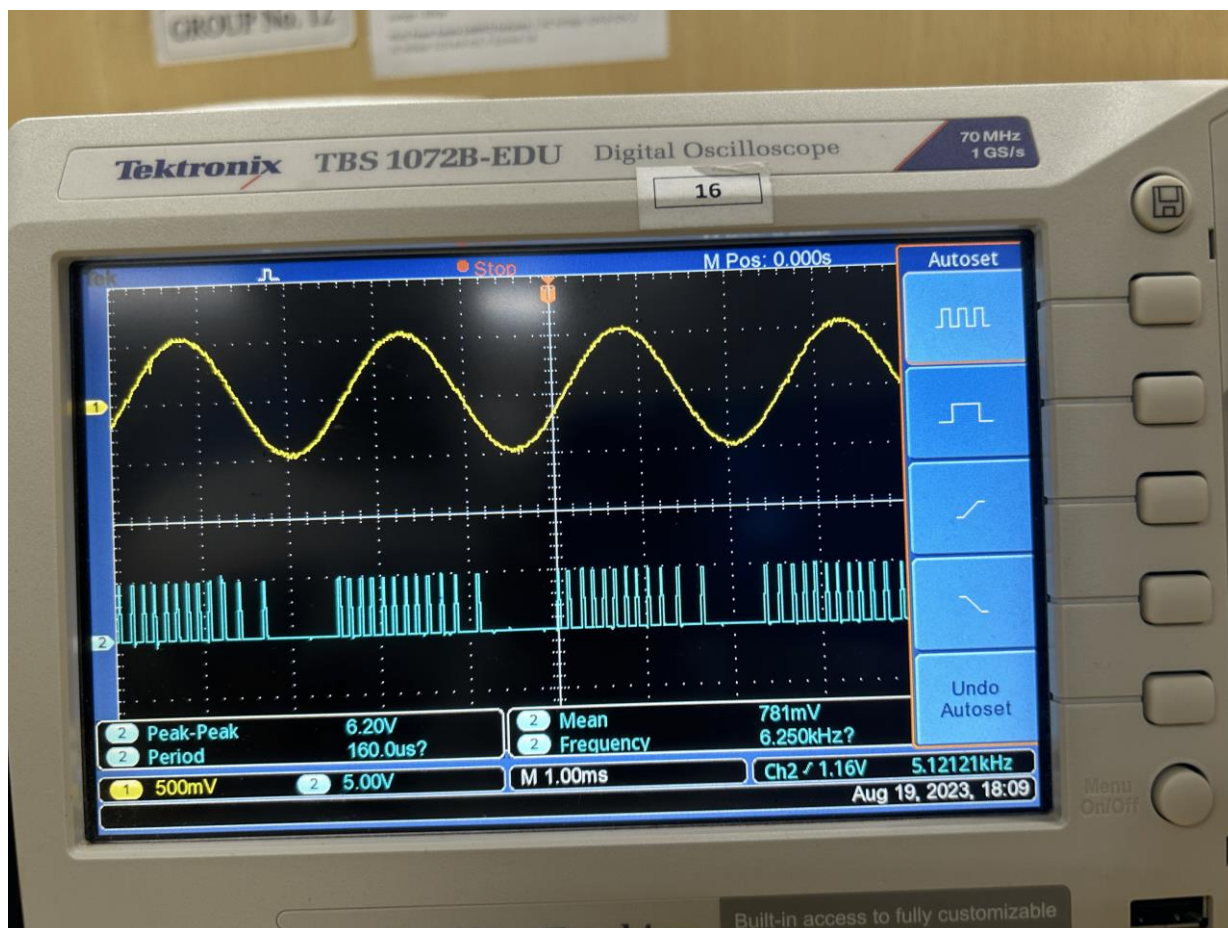
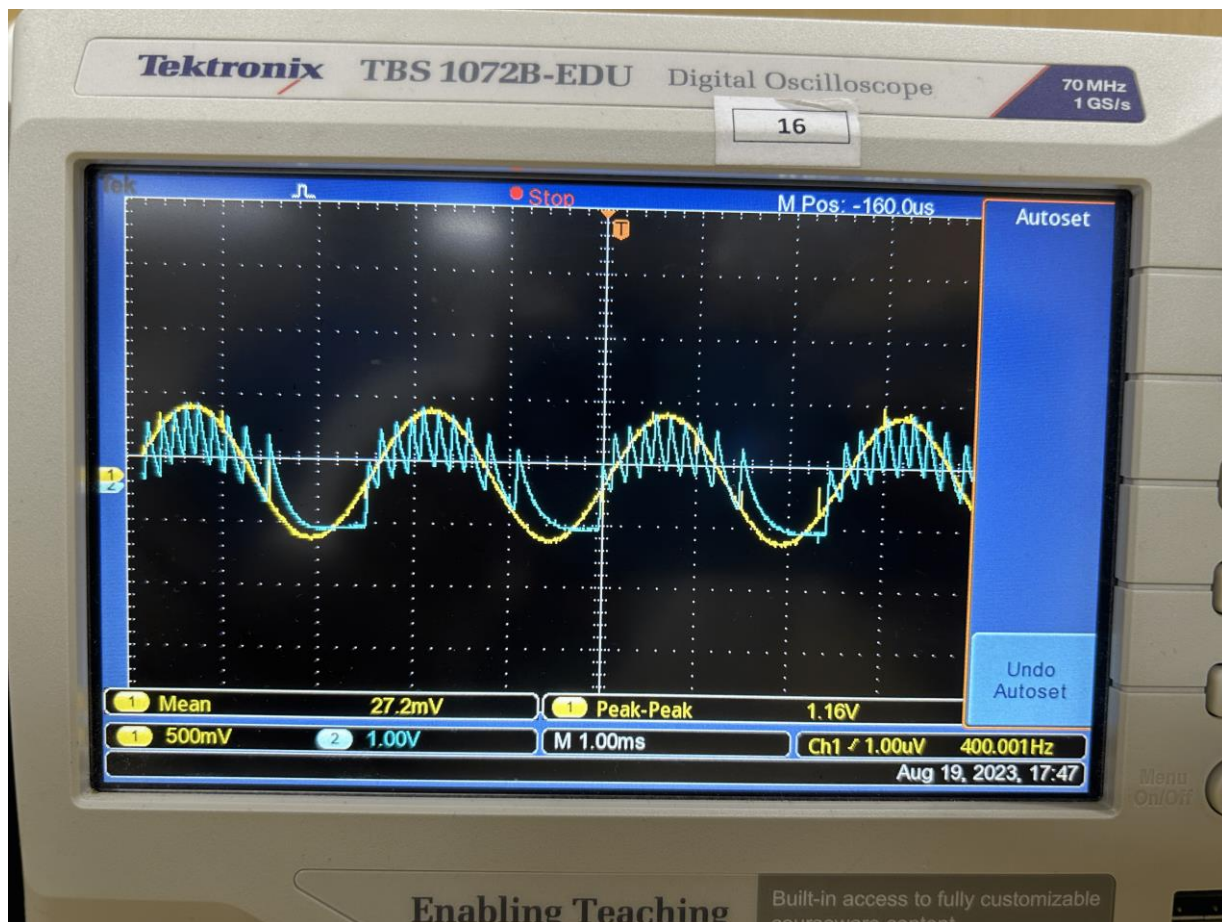
Figure 1.2: Delta Demodulation

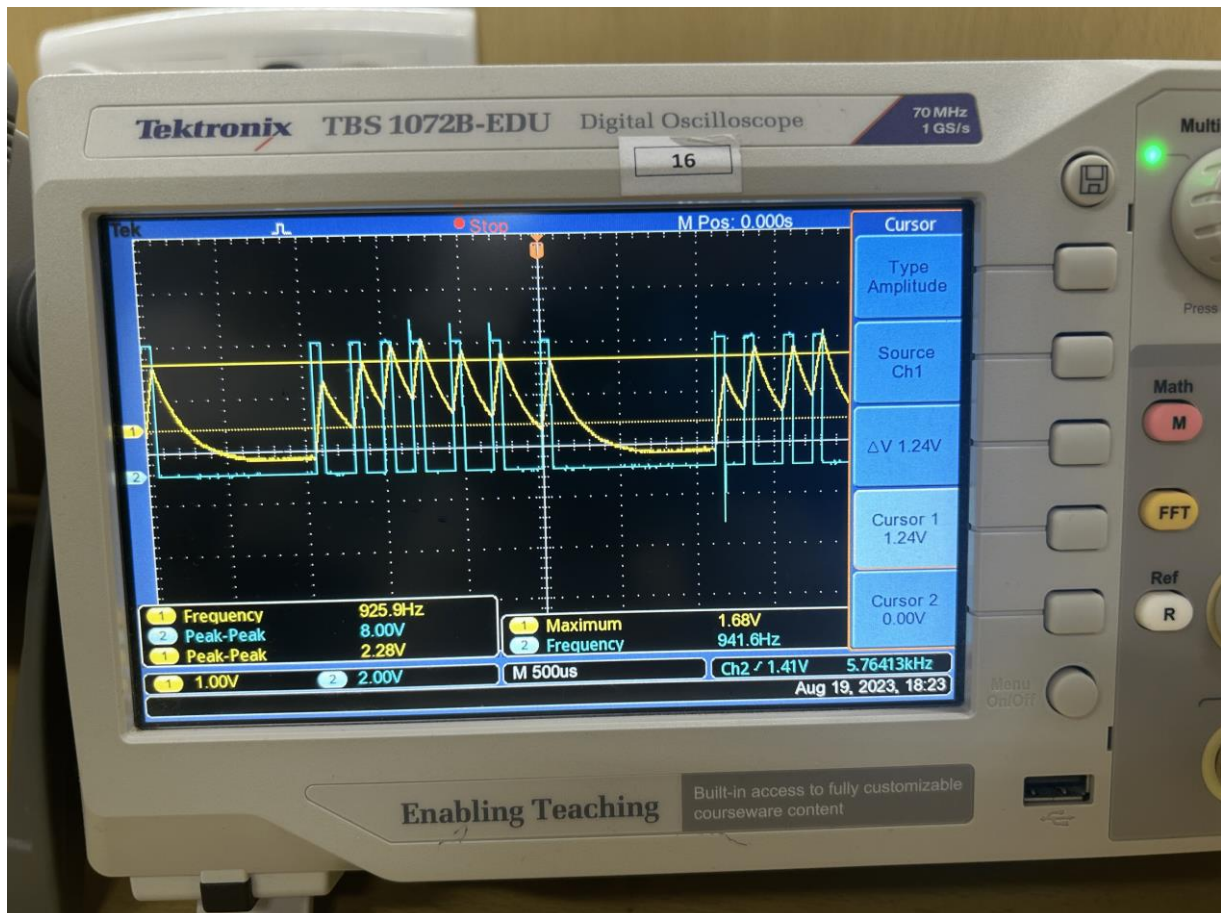
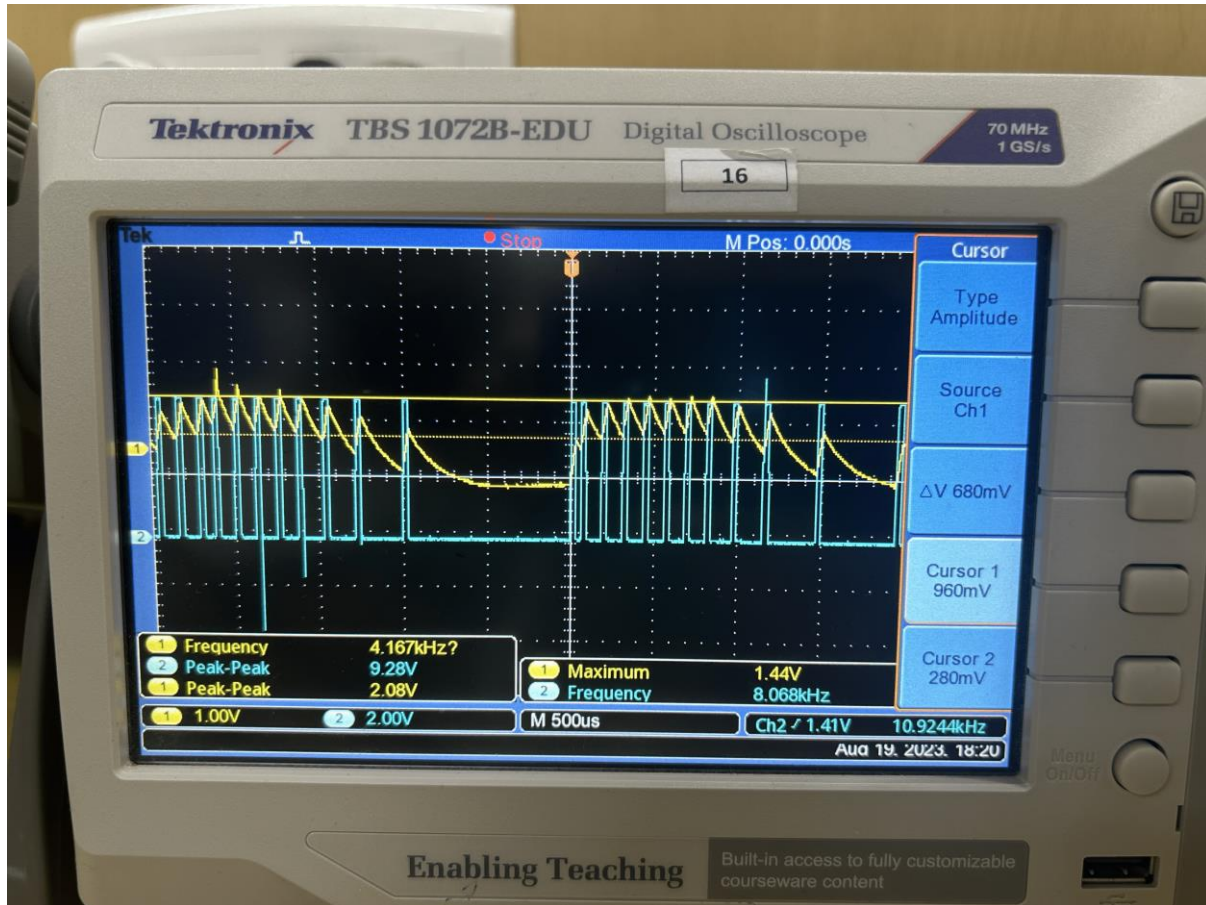
1.3.2 Observation Table

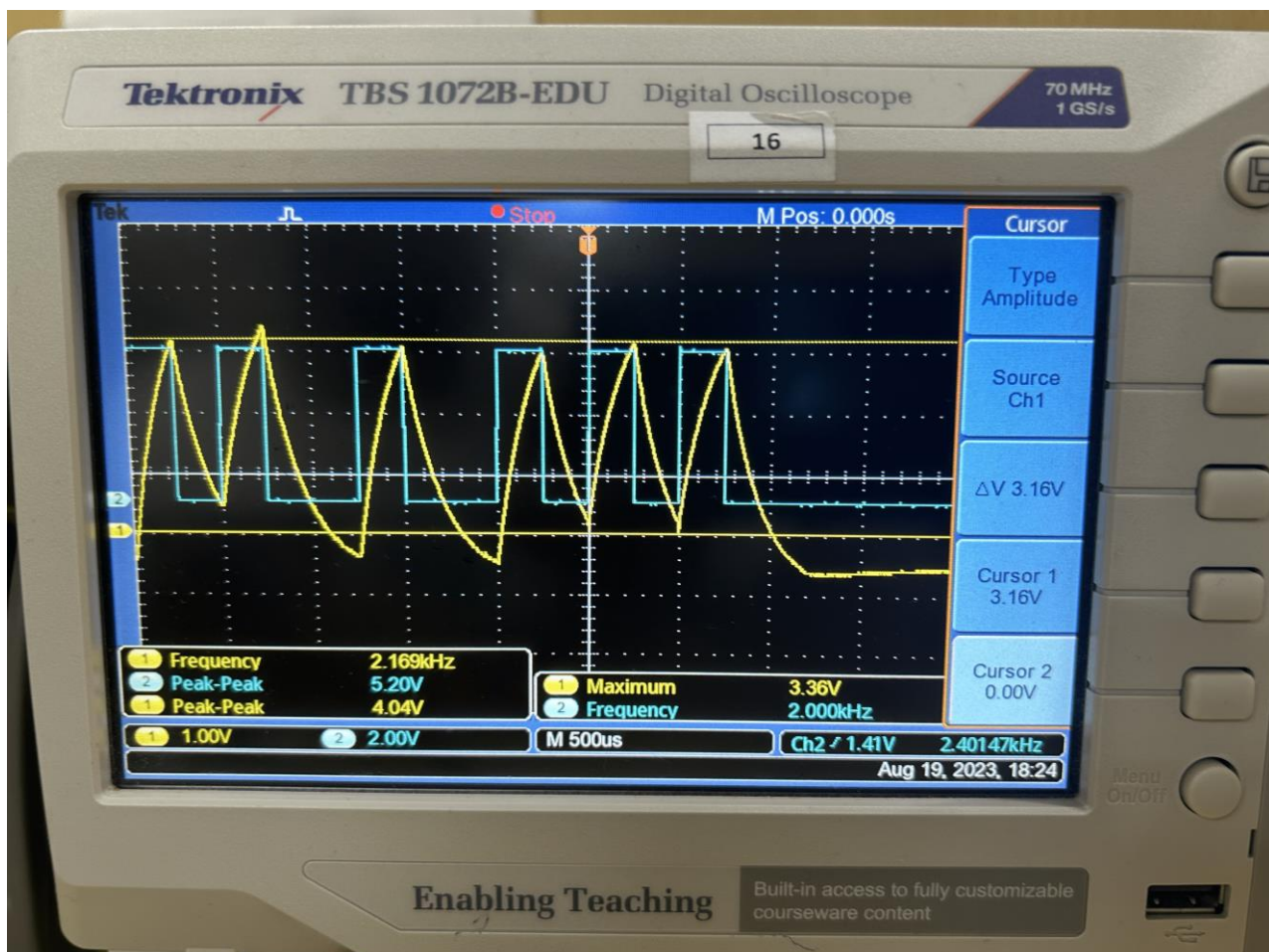
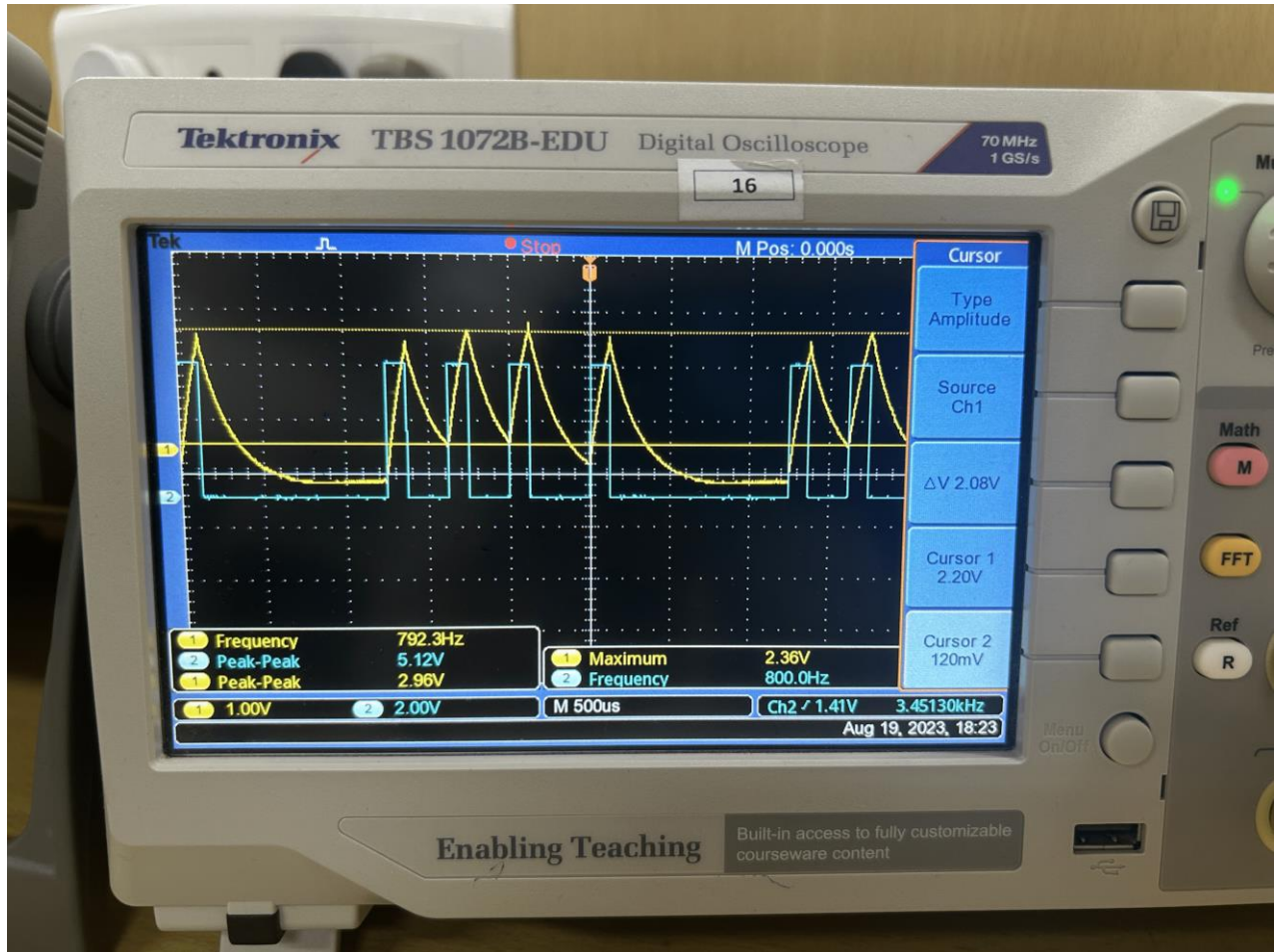
S. No.	Sampling Frequency)	Step Size(Volt)
1	32 KHz	0.68
2	16 KHz	1.24
3	8 KHz	2.08
4	4 KHz	3.16

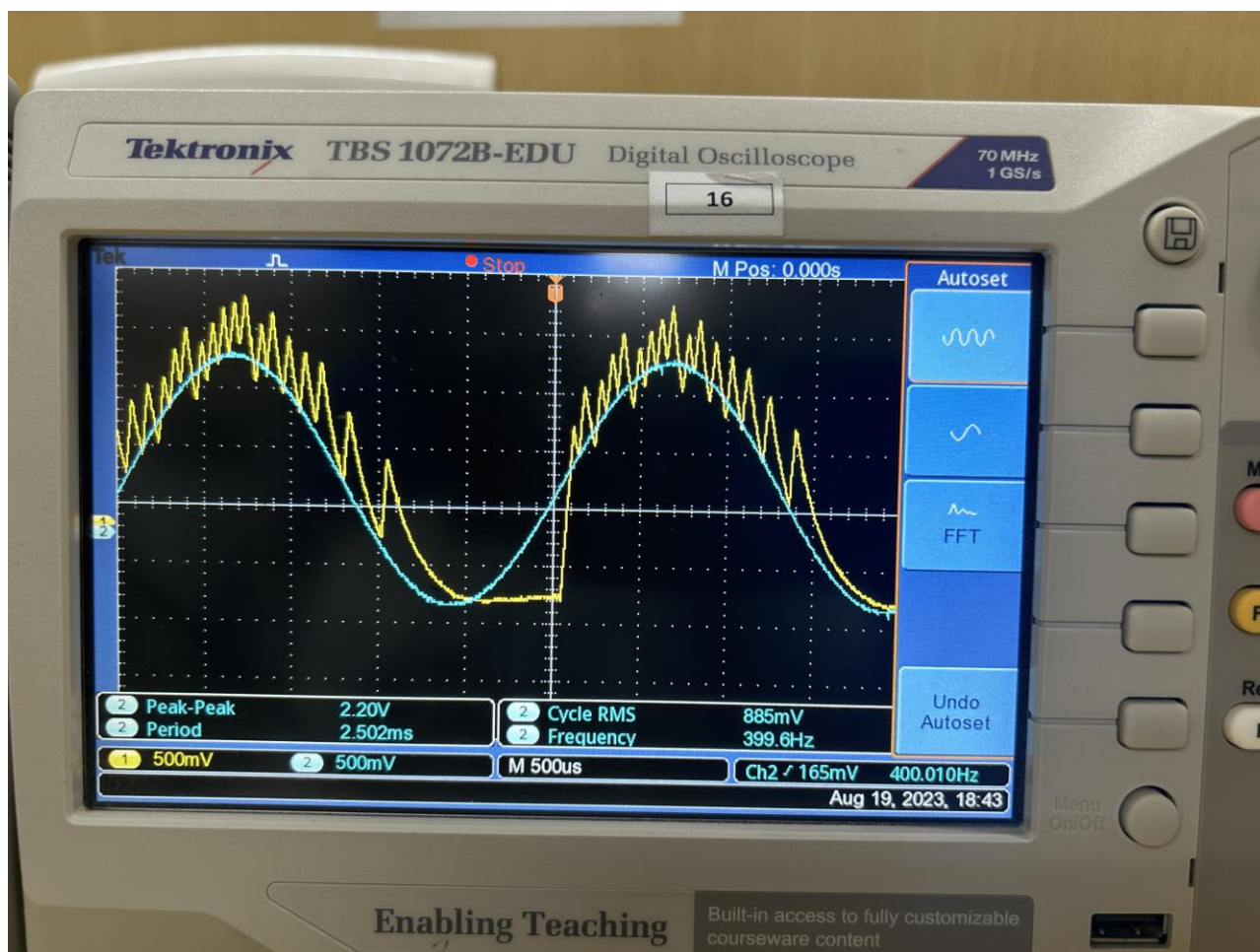
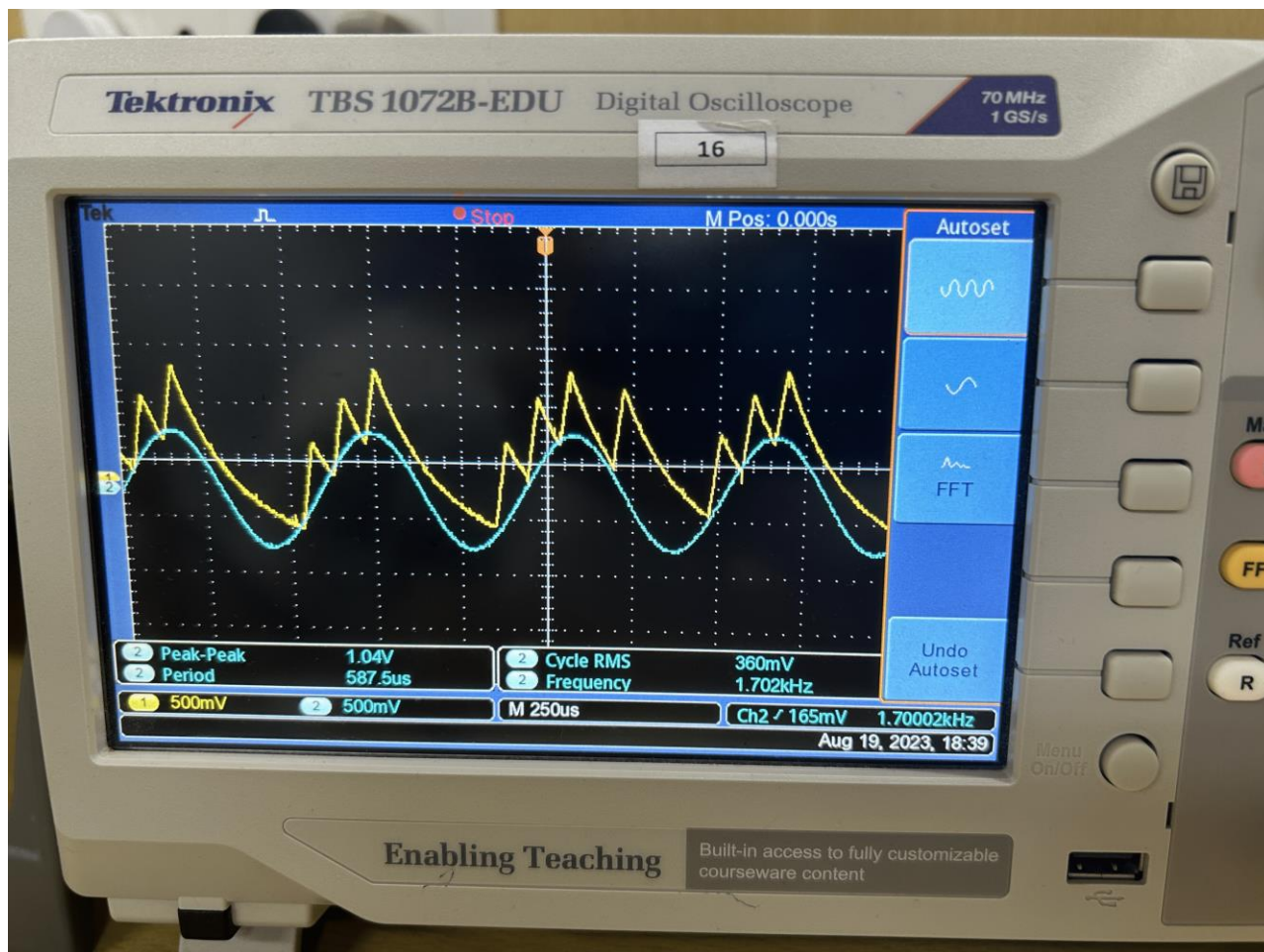
1.4 Result Screenshots

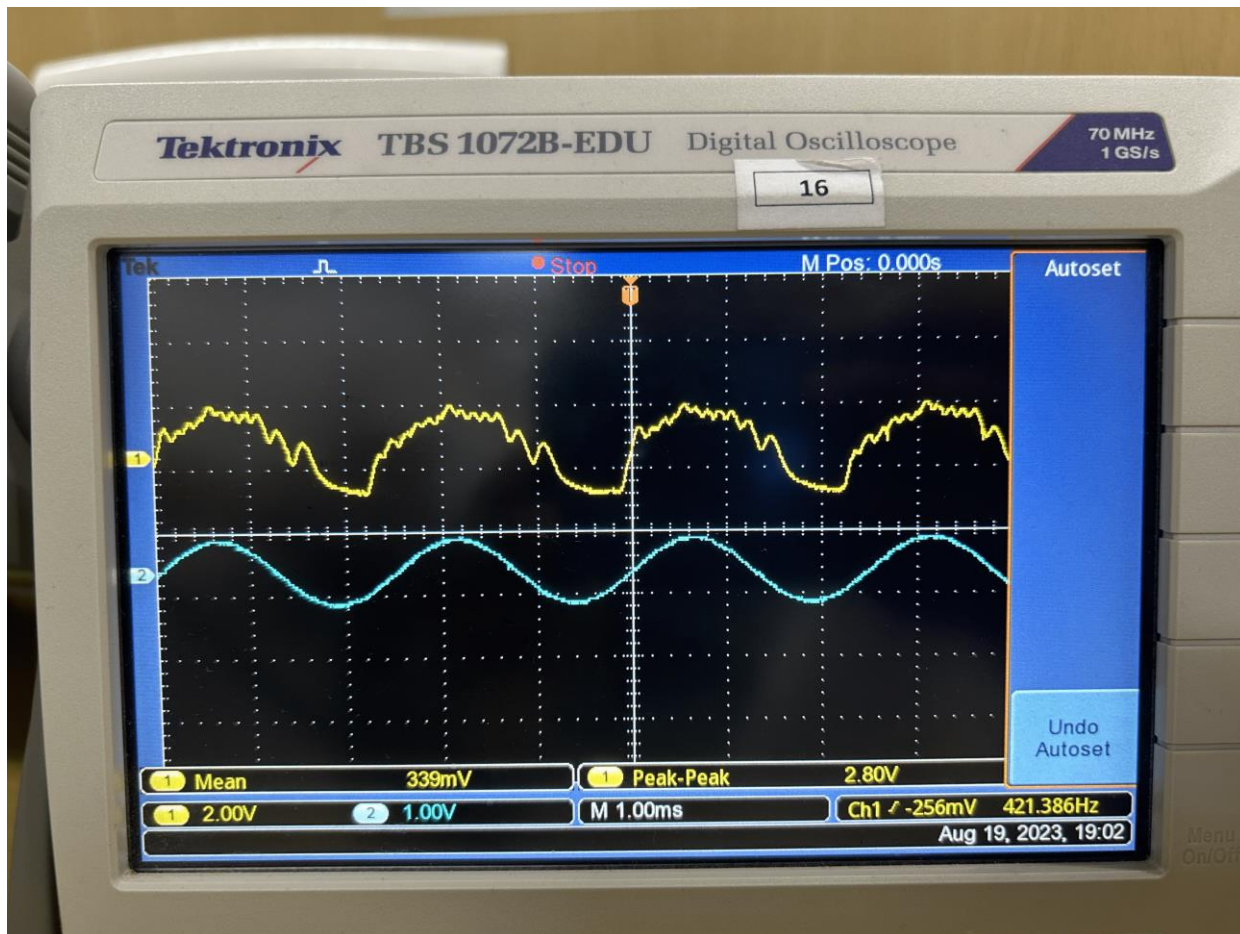




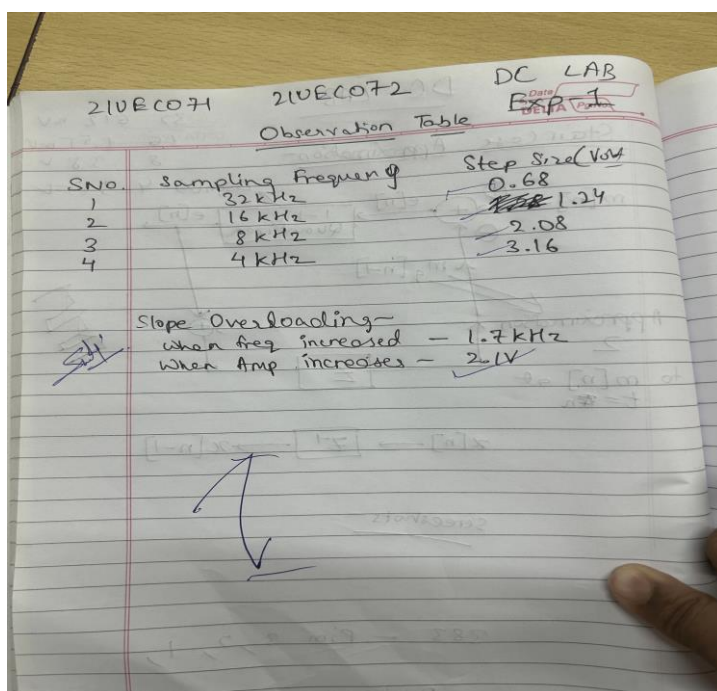








1.5 Conclusions



1.6 Precautions

1. Check the connections before switching on the kit.
2. Connections should be done properly.
3. Observation should be taken properly.