

Digital Communication Lab

Laboratory report submitted for the partial fulfillment
of the requirements for the degree of

Bachelor of Technology
in
Electronics and Communication Engineering

by

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August 2023

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Chapter 1

Experiment -3

1.1 Aim

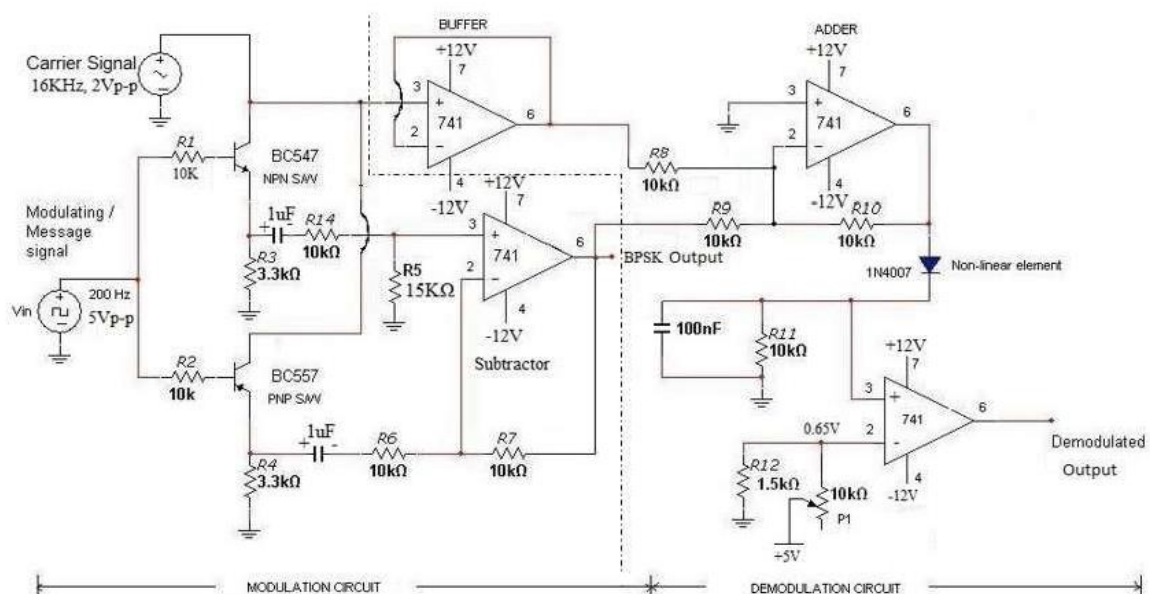
1. Implement BPSK Modulation and Demodulation.

1.2 Apparatus Required

- | | | |
|--------------------------------------|---------------------------------|-----------------------|
| 1. IC: LM741 (Operational Amplifier) | S. Digital Storage Oscilloscope | 9. BJT [BC547, BC557] |
| 2. Diode | 6. Resistance | 10. Capacitor |
| 3. DC Power Supply | 7. Connecting Wires | 11. DSO Probes |
| 4. Breadboard | 8. Function Generator | |

•1.3 Theory

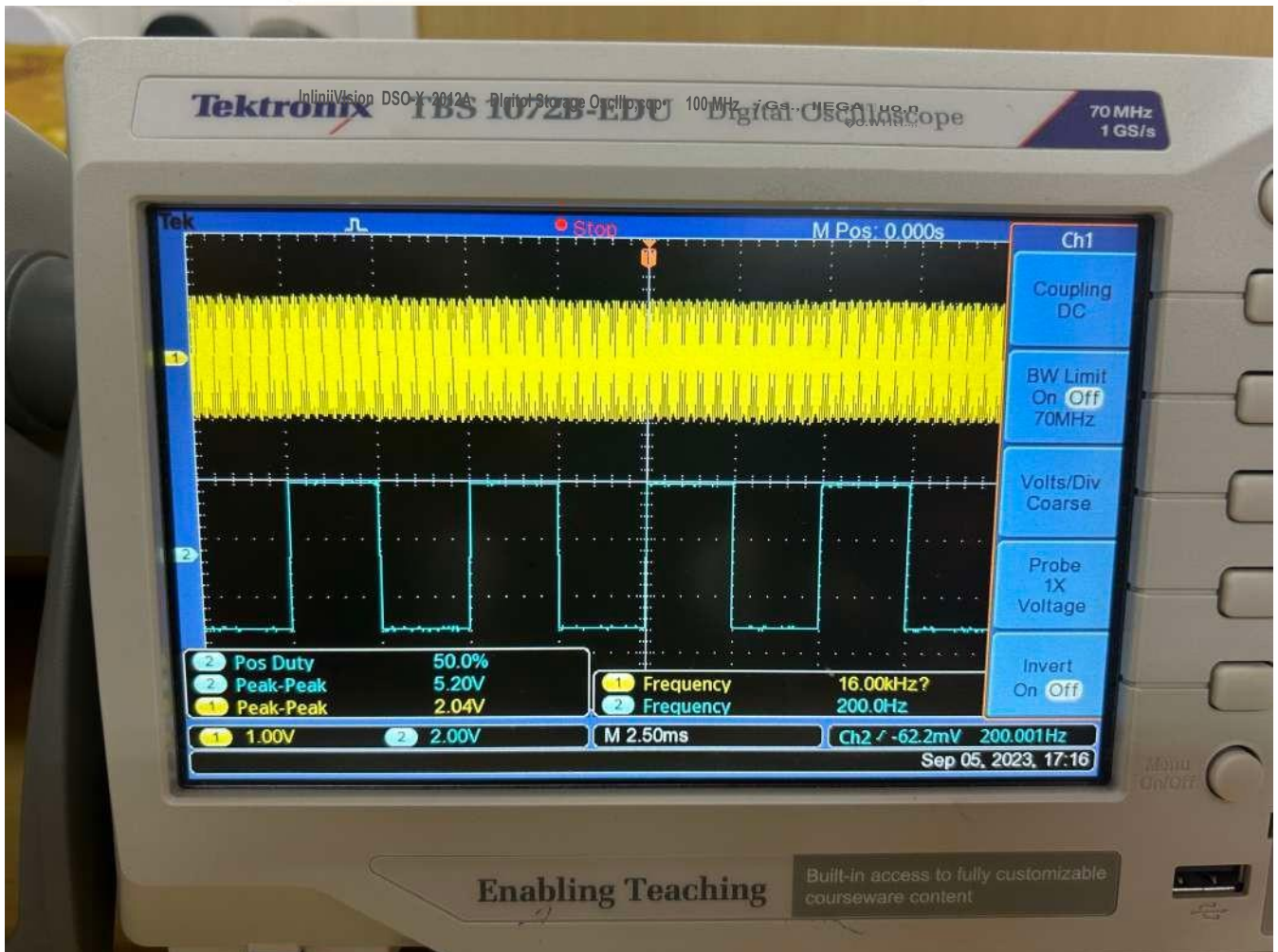
1.3.1 Connection Diagram



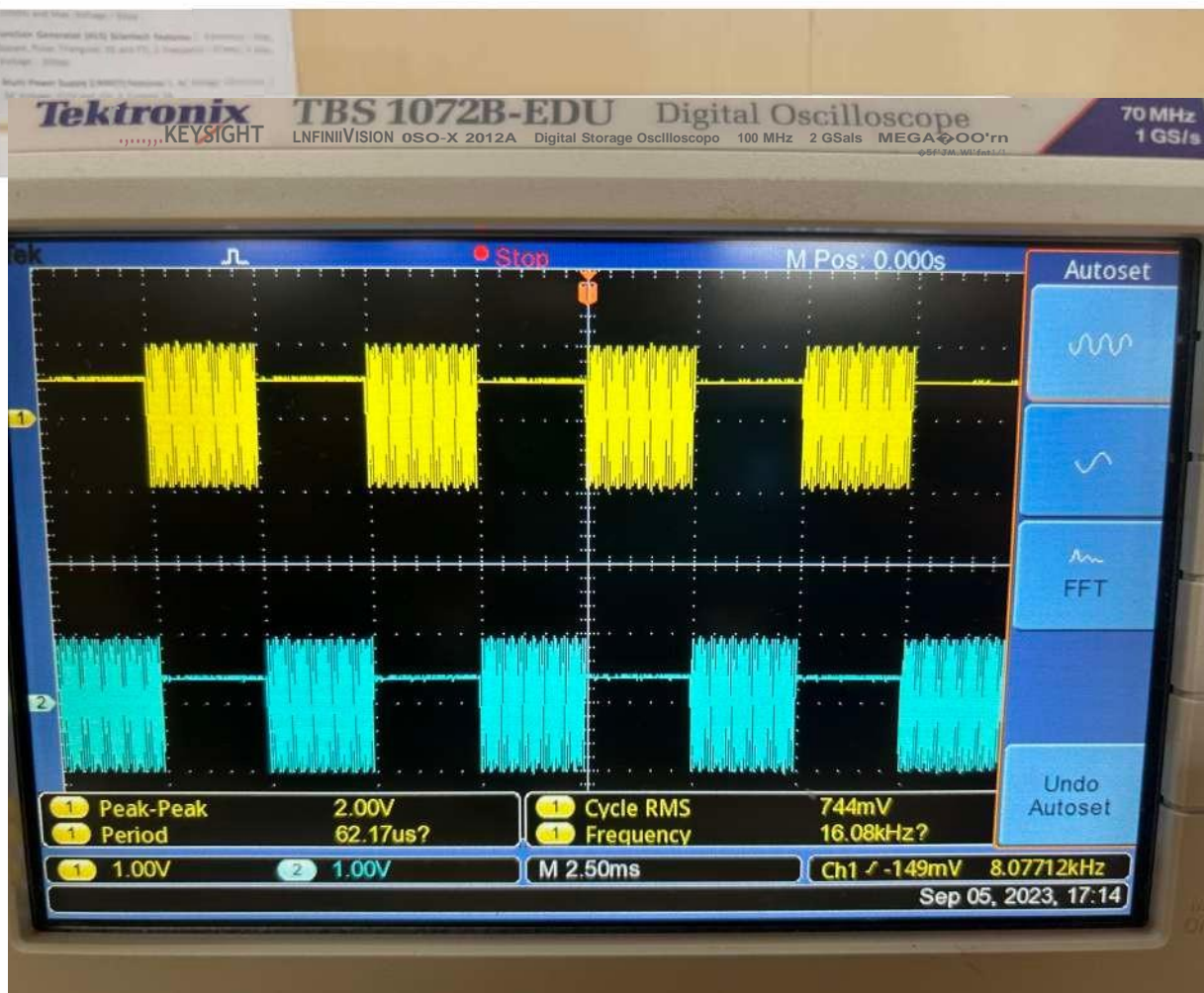
1.4 Observations

- A) Carrier Side Band Power - -35.70dbv
USB - 16.10kHz
LSB - 15.70kHz

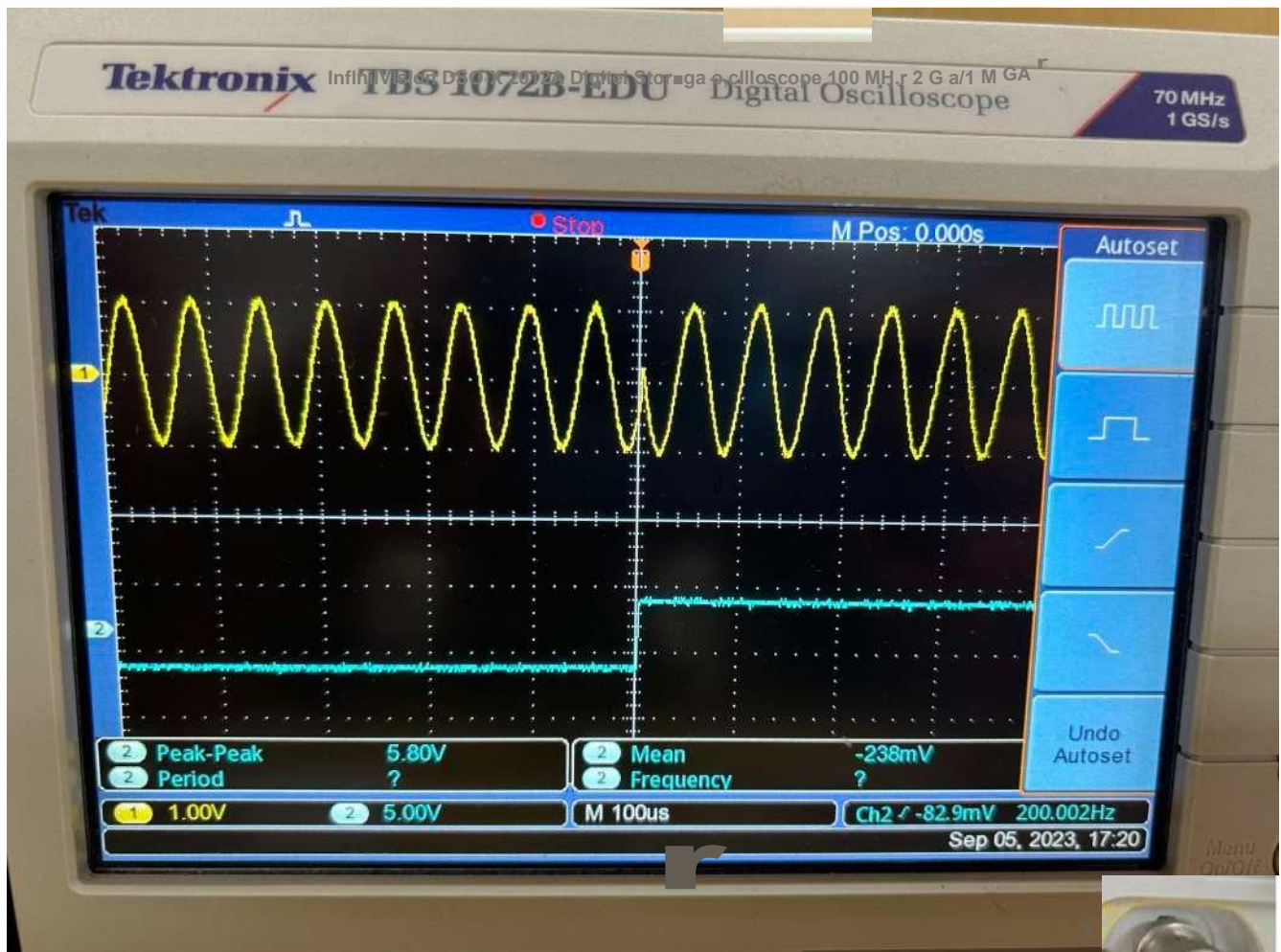
1.5 Results



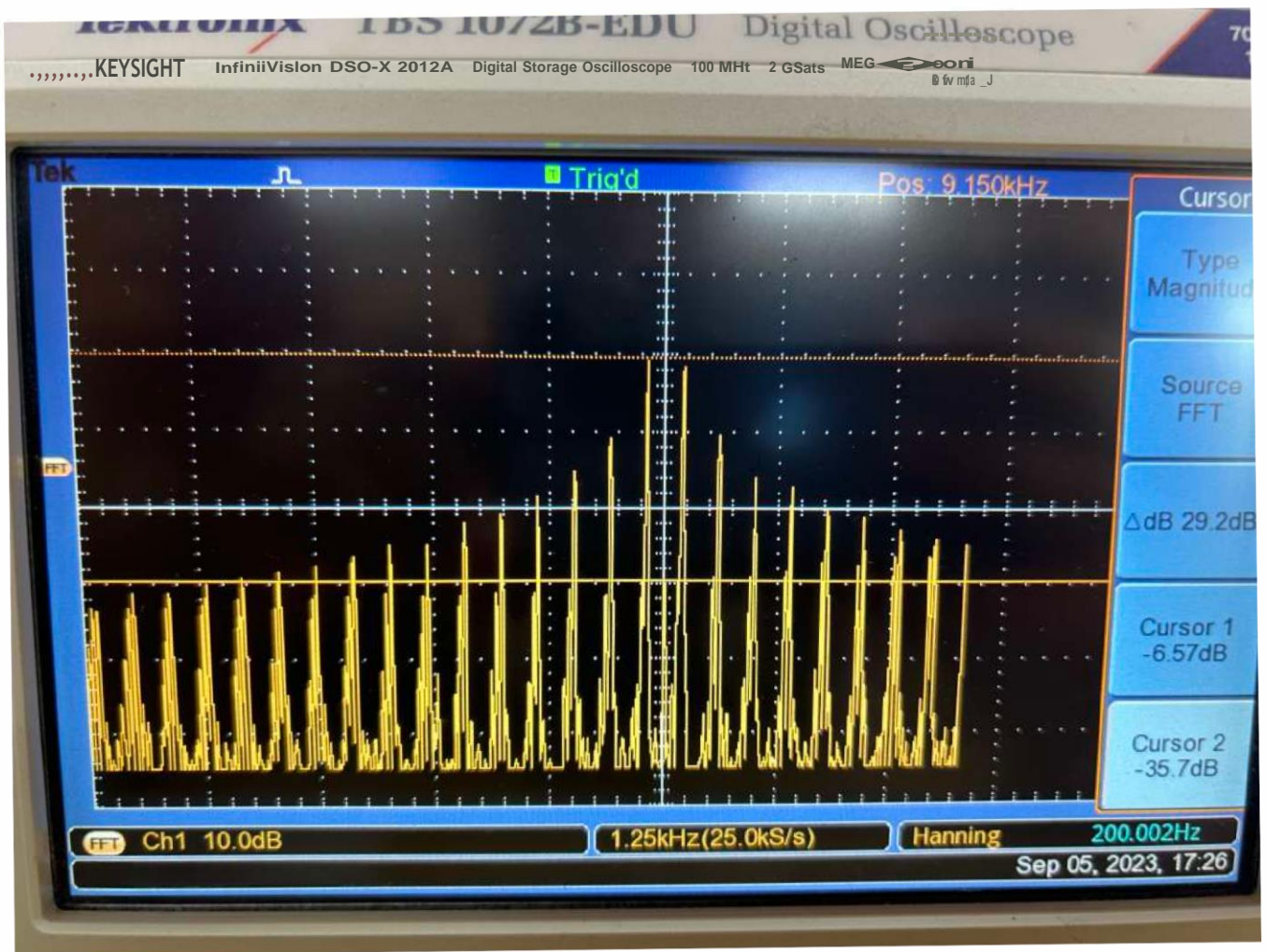
Carrier Signal + Message Signal



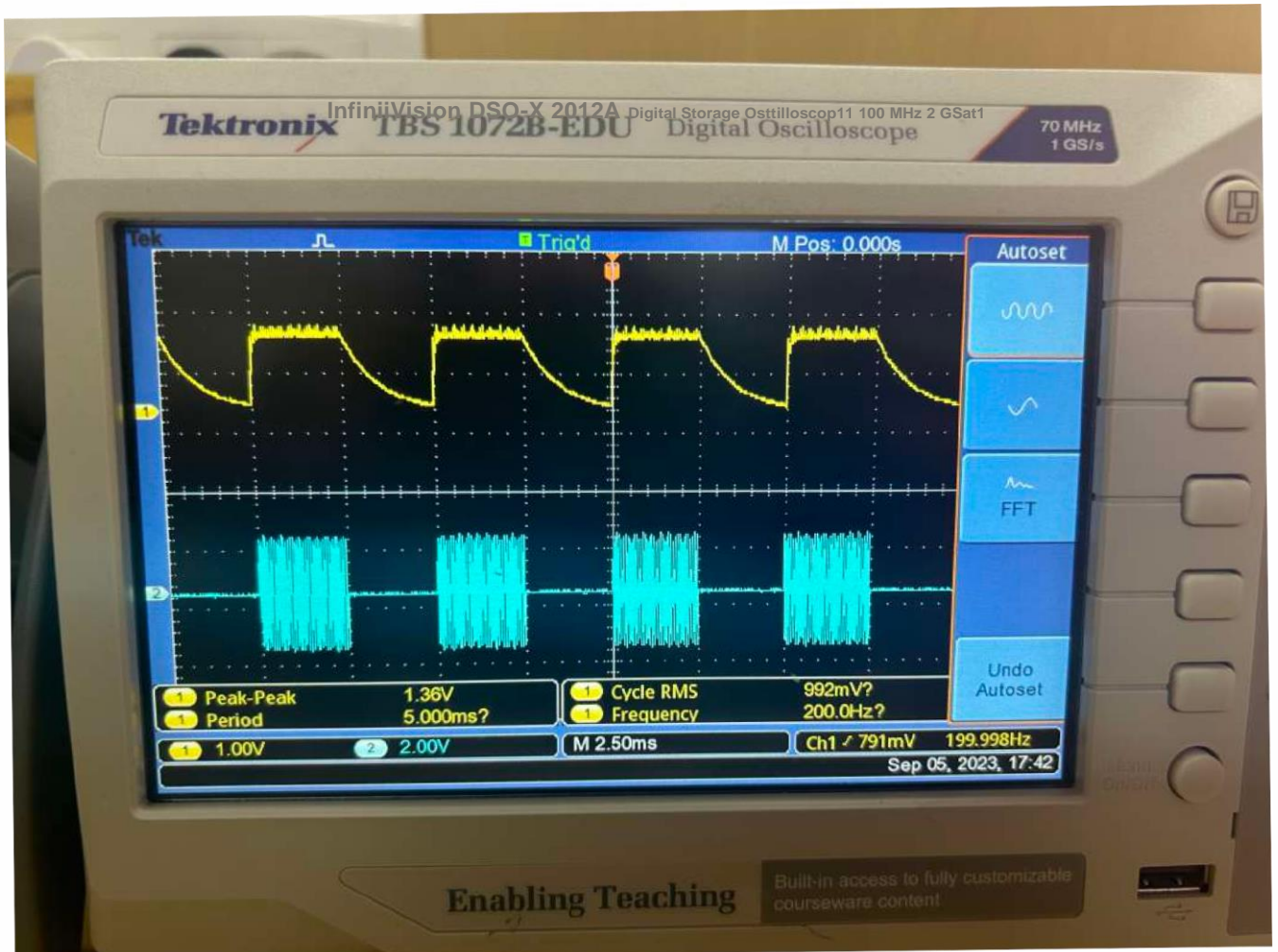
Output of Both Transistor at Emmitter



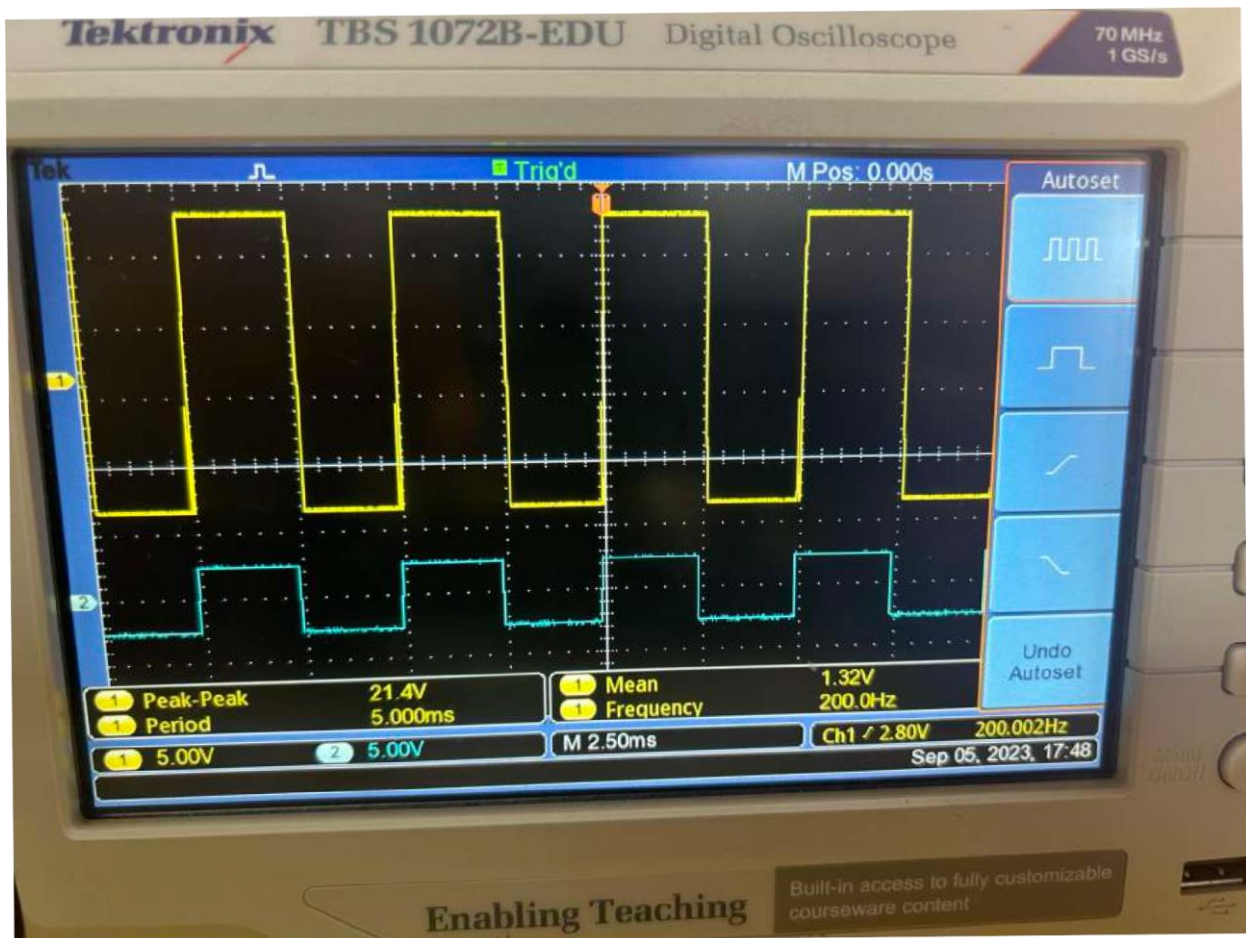
BPSK Output with Message Signal



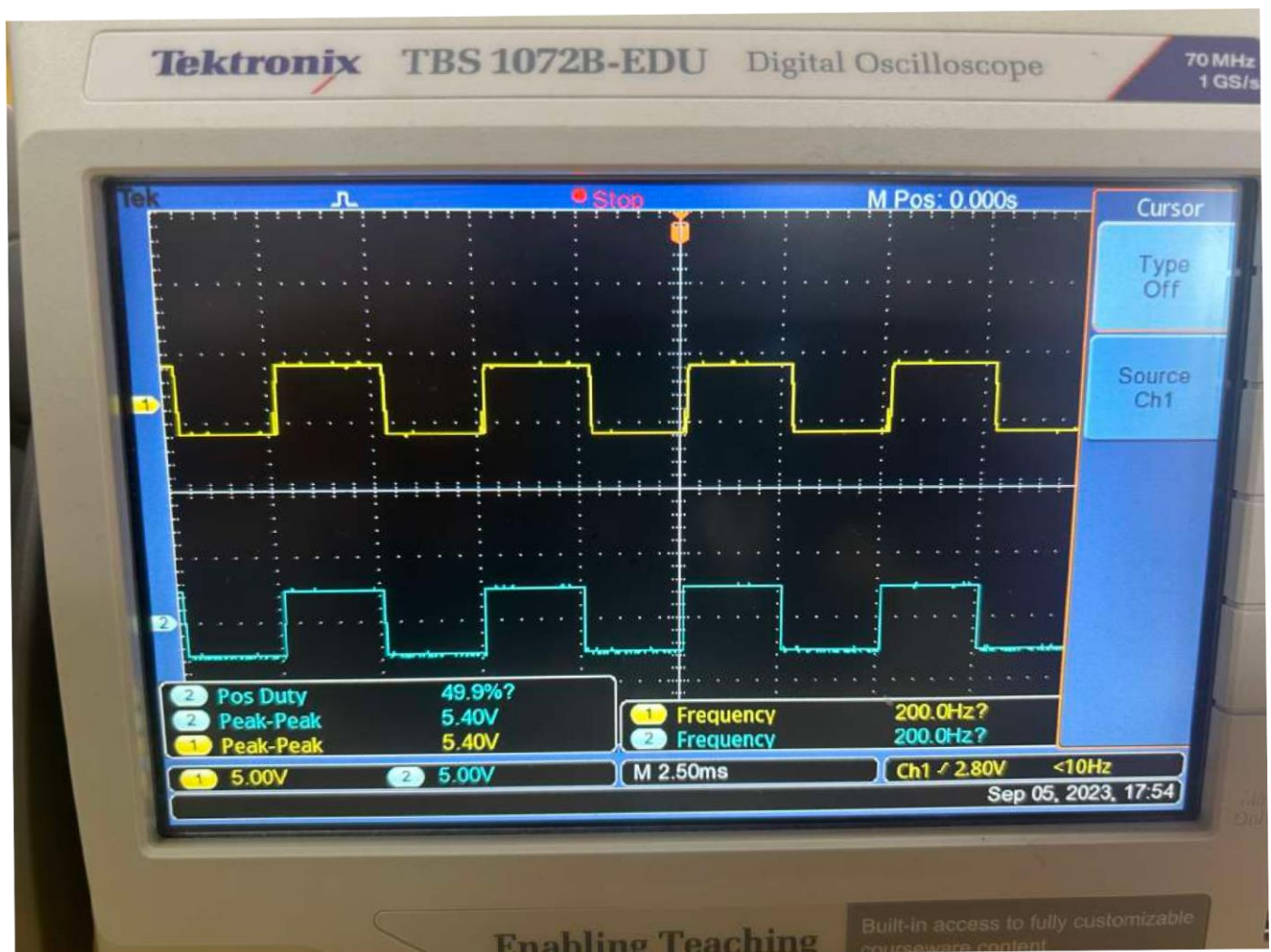
BPSK Output (Freq Domain)



Output after adder and Envelope Detector



Demodulated output with Message Signal



Output after Voltage Divider Circuit

DC - Lab Exp 3

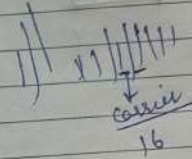
DL: Delta
Pg:

Power
SB ~~Power~~ → lower side = -6.5 dB
→ Upper side = -8.17 dB

Freq
SB ~~Power~~ → LSB = ~~8.95 kHz~~ = 15.7 kHz
→ USB = ~~9.35 kHz~~ = 16.1 kHz

Carrier Power → -35.7 dB

Voltage Divider →

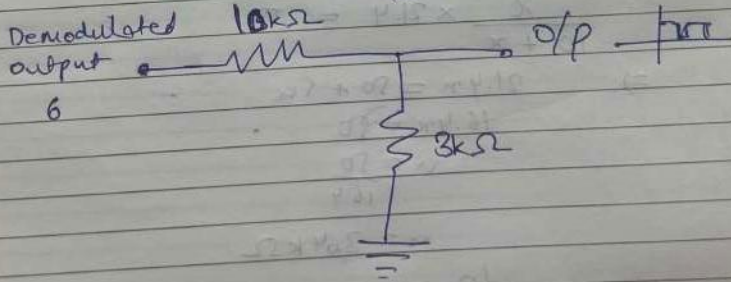


$$\frac{x}{x+10} \times 21.4 = 5$$

$$21.4x = 5x + 50$$

$$16.4x = 50$$

$$x = \frac{50}{16.4} \approx 3 \text{ k}\Omega$$



Calculations-Written

1.6 Conclusions

Implemented BPSK Modulation and Demodulation.

1.7 Precautions

1. Check the connections before switching on the kit.
2. Connections should be done properly.
3. Observation should be taken properly.