

Beaglebone Intro

Login/Access Test Functions

- Connect to BeagleBone-8EC2
- Wifi password: BeagleBone
- Re-upload code

```
sudo rm -R blood-smear  
git clone https://github.com/mucolon/blood-smear.git
```

- Run basic smear code

```
sudo python3 Desktop/blood-smear/test/basic_smear.py
```

- Edit code

```
sudo micro Desktop/blood-smear/test/basic_smear.py
```

- Shutdown

```
sudo shutdown -h now
```

Setup Steps

1. Flash Debian 9.5 LXQT Version
2. Connect to wifi [Robot Control Library: Networking WiFi](#)
3. Repartition to use all of the microSD's memory

```
sudo /opt/scripts/tools/grow_partition.sh
```

Reboot after

```
reboot
```

4. Download Micro text editor (personal preference)

```
curl https://getmic.ro | bash
```

Move to bin directory

```
sudo mv ~/micro /bin/micro
```

5. Update packages

```
sudo apt update  
sudo apt upgrade -y  
sudo apt install python3 python3-pip python3-numpy build-essential python3-dev  
autoconf-archive python3-scipy python3-tk -y  
sudo pip3 install rcpy Adafruit_BBIO  
sudo pip3 install --upgrade Adafruit_BBIO
```

6. Disable Bluetooth (speeds up boot time)

Open this file `sudo micro /etc/rc.local`

File may be blank. Add this below

```
rfkill block bluetooth  
exit 0
```

8. Clone GitHub repository with blood smearing device code

```
git clone https://github.com/mucolon/blood-smear.git
```

Linux Commands

Underlined text is your input (ie. _yourLogin_)

GitHub

- Establish GitHub connection

```
git remote set-url origin git@github.com:mucolon/blood-smear.git
```

- Commands to push latest code updates to GitHub

```
git add -A  
git commit -am "my update msg"  
git push
```

Account Management

- Add new user account

```
sudo useradd -m _yourLogin_
```

- Add user password (You will be prompted to enter your new password)

```
sudo passwd _yourLogin_
```

- Add user to the sudo group

```
sudo usermod -aG sudo _yourLogin_
```

- Edit the `/etc/passwd` to set terminal to bash instead of dash

```
sudo nano /etc/passwd
```

Add `/bin/bash` at the very end of the file after the last `:` of your added username

General

- Shutdown
 - `sudo shutdown -h now`
- Logout
 - `logout` or `sudo logout`
- List contents of current directory
 - `ls`

- Show current directory
 - `pwd`
- Change directory (folder)

```
cd _directoryName_
cd / (Beaglebone root directory)
cd ~ (User home directory)
cd .. (Previous directory)
```

- Make a new directory (folder)

```
mkdir _directoryName_
```

- Create/Open a text file

```
nano _fileName_ (basic text editor)
micro _fileName_ (better text editor) (See below for download instructions)
```

- Create/Open a text file (show line numbers)

```
nano -c _fileName_
```

- Rename/Move file

```
mv _oldFileName_ _newFileName_
```

- Remove directory or file

```
rm -R _fileName_
```

- Superuser login/logout

```
sudo su (login)
```

```
sudo -s (login)
```

```
exit (logout)
```

Troubleshooting

- Resetting remote host ID
 - `ssh-keygen -R 192.168.8.1`
- Check Debian version
 - `cat /etc/debian_version`
 - `cat /etc/dogtag`
- Disabling Bluetooth (speeds up boot time)

Open this file `sudo micro /etc/rc.local`

File may be blank. Add this below

```
rfkill block bluetooth  
exit 0
```

- Update eMMC uBoot for GPIO pin access

```
sudo dd if=/dev/zero of=/dev/mmcblk1 bs=1M count=100
```

Networking

[Robot Control Library: Networking WiFi](#)

Upgrading Packages (RobotControlLib)

- Update the list of available packages

```
sudo apt update
```

- Upgrade important package

```
sudo apt upgrade apt
```

- Upgrade to the latest version of the RobotControlLib (may be out of date)

```
sudo apt upgrade RobotControlLib
```

- Test if everything is working correctly

```
rc_test_drivers
```

- Making a Program Run on Boot

[Robot Control Library: Making a Program Run on Boot](#)

- Background Services

[Robot Control Library: Background Services](#)

Micro Text Editor

- Better text editor than nano that allows for easy color customization and mouse actions over ssh

```
curl https://getmic.ro | bash (Download link)
```

```
mv /home/mae156/micro /bin/micro (Add micro to bin for use in any directory)
```

Hardware

- 5V pins

VDD: input pins to power BeagleBone

SYS: output pins to power external components

Running Programs

- Python

```
sudo python3 _programName.py_
```

Python Libraries

```
sudo apt update  
sudo apt install python3 python3-pip python3-numpy  
sudo apt install build-essential python-dev python-pip -y  
sudo pip3 install Adafruit_BBIO  
sudo pip3 install --upgrade Adafruit_BBIO  
sudo apt install autoconf-archive python3-scipy  
sudo pip3 install rcpy
```

[GitHub - mucolon/adafruit-beaglebone-io-python: Adafruit's BeagleBone IO Python Library](#)

[GitHub - mucolon/rcpy: Python Library for Robotics Cape on Beaglebone Black and Beaglebone Blue](#)

- Interrupts

[Detecting Interrupts and Distinguishing between Rising and Falling · Issue #16 · adafruit/adafruit-beaglebone-io-python · GitHub](#)

UART

[UART | Setting up IO Python Library on BeagleBone Black | Adafruit Learning System](#)
[RoboGoby - ROV/AUV Submersible: April 2014](#)

Extras

- Kill a user's session

```
sudo pkill -9 -u _userName_
```

- Installing Java

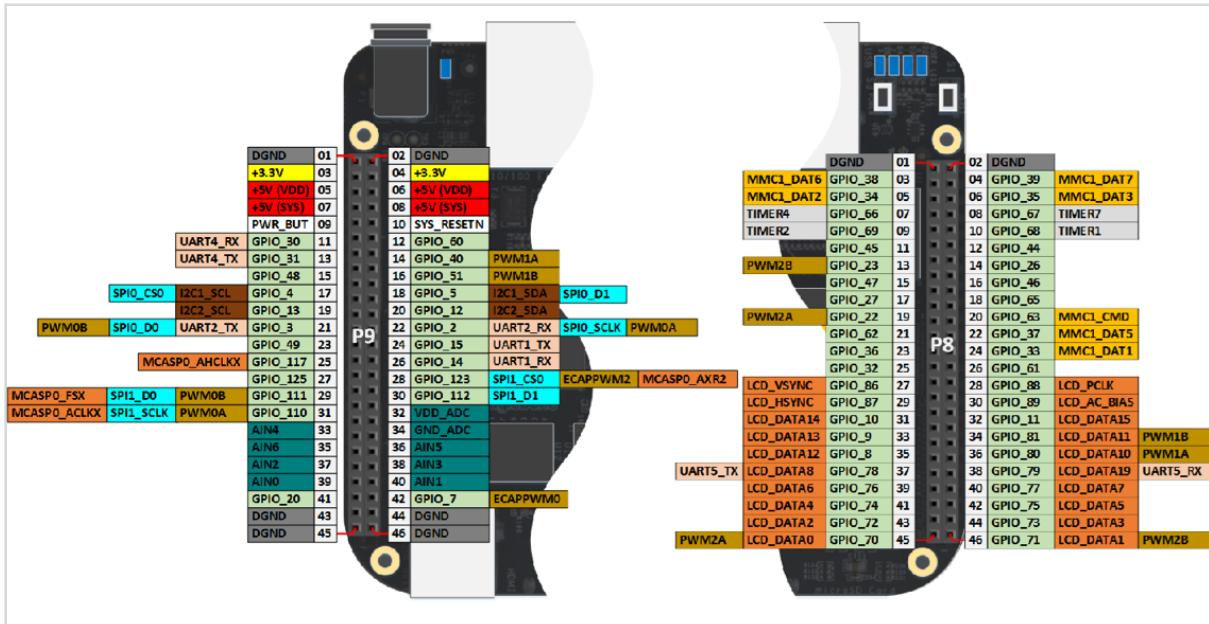
[Setting up Oracle Java 8, Gradle, and Maven On Beaglebone Green | AVR | Hackaday.io](#)

apt command	the command it replaces	function of the command
apt install	apt-get install	Installs a package
apt remove	apt-get remove	Removes a package
apt purge	apt-get purge	Removes package with configuration
apt update	apt-get update	Refreshes repository index
apt upgrade	apt-get upgrade	Upgrades all upgradable packages
apt autoremove	apt-get autoremove	Removes unwanted packages
apt full-upgrade	apt-get dist-upgrade	Upgrades packages with auto-handling of dependencies
apt search	apt-cache search	Searches for the program
apt show	apt-cache show	Shows package details

new apt command	function of the command
apt list	Lists packages with criteria (installed, upgradable etc)
apt edit-sources	Edits sources list

Resources

- Pin Setup



https://www.mathworks.com/help/supportpkg/beagleboneio/ug/beaglebone_black_pinmap.png

beaglebone_black_pinmap.png

P8				P9				
	1	2		1	2			
	GND	 	GND		GND	 	GND	
GPIO1_6	P8.3	 	P8.4	GPIO1_7		VDD 3.3V	 	VDD 3.3V
GPIO1_2	P8.5	 	P8.6	GPIO1_3		VDD 5V	 	VDD 5V
GPIO2_2	P8.7	 	P8.8	GPIO2_3		SYS 5V	 	SYS 5V
GPIO2_5	P8.9	 	P8.10	GPIO2_4		PWR_BUT	 	SYS_RESETn
GPIO1_13	P8.11	 	P8.12	GPIO1_12	GPIO0_30	P9.11	 	P9.12 GPIO1_28
GPIO0_23	P8.13	 	P8.14	GPIO0_26	GPIO0_31	P9.13	 	P9.14 GPIO1_18
GPIO1_15	P8.15	 	P8.16	GPIO1_14	GPIO1_16	P9.15	 	P9.16 GPIO1_19
GPIO0_27	P8.17	 	P8.18	GPIO2_1	GPIO0_5	P9.17	 	P9.18 GPIO0_4
GPIO0_22	P8.19	 	P8.20	GPIO1_31	GPIO0_13	P9.19	 	P9.20 GPIO0_12
GPIO1_30	P8.21	 	P8.22	GPIO1_5	GPIO0_3	P9.21	 	P9.22 GPIO0_2
GPIO1_4	P8.23	 	P8.24	GPIO1_1	GPIO1_17	P9.23	 	P9.24 GPIO0_15
GPIO1_0	P8.25	 	P8.26	GPIO1_29	GPIO3_21	P9.25	 	P9.26 GPIO0_14
GPIO2_22	P8.27	 	P8.28	GPIO2_24	GPIO3_19	P9.27	 	P9.28 GPIO3_17
GPIO2_23	P8.29	 	P8.30	GPIO2_25	GPIO3_15	P9.29	 	P9.30 GPIO3_16
GPIO0_10	P8.31	 	P8.32	GPIO0_11	GPIO3_14	P9.31	 	VDD_ADC(1.8V)
GPIO0_9	P8.33	 	P8.34	GPIO2_17	AIN4	P9.33	 	GNDA_ADC
GPIO0_8	P8.35	 	P8.36	GPIO2_16	AIN6	P9.35	 	P9.36 AIN5
GPIO2_14	P8.37	 	P8.38	GPIO2_15	AIN2	P9.37	 	P9.38 AIN3
GPIO2_12	P8.39	 	P8.40	GPIO2_13	AIN0	P9.39	 	P9.40 AIN1
GPIO2_10	P8.41	 	P8.42	GPIO2_11	GPIO0_20	P9.41	 	P9.42 GPIO0_7
GPIO2_8	P8.43	 	P8.44	GPIO2_9		GND	 	GND
GPIO2_6	P8.45	 	P8.46	GPIO2_7		GND	 	GND
	45	46			45	46		

<http://mkaczanowski.com/wp-content/uploads/2014/03/resizedimage600667-hwio-beaglebone-ports2.png>

Beaglebone Black P8 Header

Head_pin	SPINS	ADDR/OFFSET	GPIO NO.	Name	Mode7	Mode6	Mode5	Mode4	Mode3	Mode2	Mode1	Mode0	PIN	Notes
					D/GND									Ground
P0_03	6	0x818/018	38	GPIO0_16	gpio[1 6]					mmc1_dat6	gpmc_a6	R9	Used on Board (Group: pinnx_emmc2_pins)	
P0_04	7	0x818/01c	39	GPIO0_17	gpio[1 7]					mmc1_dat7	gpmc_a7	T9	Used on Board (Group: pinnx_emmc2_pins)	
P0_05	2	0x808/008	34	GPIO0_12	gpio[1 2]					mmc1_dat2	gpmc_a2	R8	Used on Board (Group: pinnx_emmc2_pins)	
P0_06	3	0x808/00c	35	GPIO0_13	gpio[1 13]					mmc1_dat3	gpmc_a3	T8	Used on Board (Group: pinnx_emmc2_pins)	
P0_07	36	0x808/000	60	TIIMER8	gpio[1 0]					timer4	gpmc_ae8	R9	Used on Board (Group: pinnx_emmc2_pins)	
P0_08	37	0x894/074	67	TIIMER7	gpio[1 3]					timer5	gpmc_ae9	T7	Used on Board (Group: pinnx_emmc2_pins)	
P0_09	39	0x898/095	69	TIMERS	gpio[1 5]					timer6	gpmc_wen	U6		
P0_10	38	0x898/098	68	TIMERS	gpio[1 4]									
P0_11	13	0x834/034	45	GPIO0_13	gpio[1 12]	pr1_pru0_pru_r30_15	eQEP2B_in	mmc2_dat1	mmc1_dat5	lcd_data18	gpmc_a13	R12		
P0_12	12	0x834/030	44	GPIO0_14	gpio[1 12]	pr1_pru0_pru_r30_14	eQEP2A_in	MMC0_dat4	MMC1_dat4	LCD_data10	GPMC_A012	T12		
P0_13	13	0x834/034	33	EEPROM2B	gpio[1 14]	pr1_pru0_pru_r30_13	mcasp0_rx2	mmc2_dat2	mmc2_dat5	lcd_data22	gpmc_a19	T9		
P0_14	10	0x828/028	26	GPIO0_26	gpio[1 26]	ehprm02_trigpos_in	mmc2_dat6	mmc1_dat2	lcd_data21	gpmc_a10	T11			
P0_15	15	0x838/037	47	GPIO0_15	gpio[1 15]	pr1_pru0_pru_r31_15	eQEP2B_strobe	mmc2_dat3	mmc1_dat7	lcd_data16	gpmc_a35	V11		
P0_16	14	0x838/038	46	GPIO0_14	gpio[1 14]	pr1_pru0_pru_r31_14	eQEP2_index	mmc2_dat2	mmc1_dat6	lcd_data17	gpmc_a14	V13		
P0_17	11	0x828/022	27	GPIO0_27	gpio[1 27]	ehprm02d_sync	mmc2_dat4	mmc1_dat3	lcd_data20	gpmc_a21	V12			
P0_18	9	0x808/008	65	GPIO0_09	gpio[1 9]	mcasp0_fsr	mmc2_dat5	mmc2_dat2	ehprm02d_strobe	gpmc_a20	V13			
P0_19	8	0x808/000	22	EEPROM2A	gpio[1 8]	ehprm02A	mmc2_dat4	mmc1_dat4	lcd_data23	gpmc_a8	U9			
P0_20	33	0x884/084	63	GPIO0_31	gpio[1 31]	pr1_pru0_pru_r31_13	pr1_pru0_pru_r30_13	mmc1_cmd	gpmc_be1n	gpmc_c02	V9	Used on Board (Group: pinnx_emmc2_pins)		
P0_21	32	0x880/080	62	GPIO0_30	gpio[1 30]	pr1_pru0_pru_r31_12	pr1_pru0_pru_r30_12	mmc1_clk	gpmc_be1k	gpmc_c01	U9	Used on Board (Group: pinnx_emmc2_pins)		
P0_22	5	0x814/014	37	GPIO0_05	gpio[1 5]	pr1_pru0_pru_r31_11	mcasp0_rx1	mmc1_dat5	gpmc_a5	V8	Used on Board (Group: pinnx_emmc2_pins)			
P0_23	4	0x810/010	36	GPIO0_04	gpio[1 4]	pr1_pru0_pru_r31_10	mcasp0_rx0	mmc1_dat4	gpmc_a4	U8	Used on Board (Group: pinnx_emmc2_pins)			
P0_24	3	0x808/008	33	GPIO0_03	gpio[1 1]	pr1_pru0_pru_r31_09	mcasp0_rx1	mmc1_dat1	gpmc_a1	V7	Used on Board (Group: pinnx_emmc2_pins)			
P0_25	0	0x800/000	32	GPIO0_00	gpio[1 0]	pr1_pru0_pru_r31_08	mcasp0_rx0	mmc1_dat0	gpmc_a0	U7	Used on Board (Group: pinnx_emmc2_pins)			
P0_26	31	0x87/07c	61	GPIO0_29	gpio[1 29]									
P0_27	56	0x808/060	86	GPIO0_22	gpio[1 22]	pr1_pru0_pru_r31_8	pr1_pru0_pru_r30_8			gpmc_a8	ldc_vsync	U5	Allocated (Group: npx_hdmi_boneli_pins)	
P0_28	58	0x808/068	88	GPIO0_24	gpio[1 24]	pr1_pru0_pru_r31_10	pr1_pru0_pru_r30_10			gpmc_a10	ldc_pdpn	V5	Allocated (Group: npx_hdmi_boneli_pins)	
P0_29	57	0x808/064	87	GPIO0_22	gpio[1 22]	pr1_pru0_pru_r31_9	pr1_pru0_pru_r30_9			gpmc_a10	ldc_pdpn	R5	Allocated (Group: npx_hdmi_boneli_pins)	
P0_30	0	0x808/060	89	GPIO0_25	gpio[1 25]					gpmc_a11	ldc_ar_ban	RE	Allocated (Group: npx_hdmi_boneli_pins)	
P0_31	54	0x808/068	10	UARTS_CTSN	gpio[1 10]	uart5_ctsn	mcasp0_rx1	eQEP1_index	gpmc_a12	ldc_ar_d14	V4	Allocated (Group: npx_hdmi_boneli_pins)		
P0_32	55	0x808/060	11	UARTS_RTSN	gpio[1 11]	uart5_ctsn	mcasp0_tx3	mcasp0_txhcrx	eQEP1_strobe	gpmc_a19	ldc_data15	T5	Allocated (Group: npx_hdmi_boneli_pins)	
P0_33	53	0x804/040	9	UARTS_RTSN	gpio[1 9]	wart2_ctsn	mcasp0_rx3	mcasp0_fsr	eQEP1B_in	gpmc_a17	ldc_data13	V3	Allocated (Group: npx_hdmi_boneli_pins)	
P0_34	51	0x802/040	81	UARTS_RTSN	gpio[1 17]	wart3_ctsn	mcasp0_tx2	mcasp0_txhcrx	ehprm01B	gpmc_a15	ldc_data11	V2	Allocated (Group: npx_hdmi_boneli_pins)	
P0_35	52	0x800/040	80	UARTS_CTSN	gpio[1 18]	wart4_ctsn	mcasp0_tx0	mcasp0_txhcrx	ehprm01A	gpmc_a10	ldc_data12	V2	Allocated (Group: npx_hdmi_boneli_pins)	
P0_36	49	0x808/060	80	UARTS_RTSN	gpio[1 19]	wart5_ctsn	mcasp0_tx0	mcasp0_txhcrx	ehprm01A	gpmc_a14	ldc_data10	U9	Allocated (Group: npx_hdmi_boneli_pins)	
P0_37	48	0x808/060	78	UARTS_RXD	gpio[1 14]	wart2_ctsn	mcasp0_rx2	mcasp0_rxhcrx	ehprm01_trigpos_in	gpmc_a13	ldc_data9	U13	Allocated (Group: npx_hdmi_boneli_pins)	
P0_38	49	0x808/060	79	UARTS_RXD	gpio[1 15]	wart2_ctsn	mcasp0_rx2	mcasp0_rxhcrx	ehprm01_fsync	gpmc_a13	ldc_data9	U2	Allocated (Group: npx_hdmi_boneli_pins)	
P0_39	46	0x808/068	76	GPIO0_12	gpio[1 12]	pr1_pru0_pru_r31_6	pr1_pru0_pru_r30_6	mcasp0_rx0	eQEP2_index	gpmc_a6	ldc_data6	T3	Allocated (Group: npx_hdmi_boneli_pins)	
P0_40	47	0x808/068	77	GPIO0_13	gpio[1 13]	pr1_pru0_pru_r31_7	pr1_pru0_pru_r30_7	mcasp0_rx1	eQEP2_strobe	gpmc_a7	ldc_data7	T4	Allocated (Group: npx_hdmi_boneli_pins)	
P0_41	41	0x808/064	74	GPIO0_11	gpio[1 11]	pr1_pru0_pru_r31_5	pr1_pru0_pru_r30_5	mcasp0_rx3	eQEP2_in	gpmc_a4	ldc_data4	T1	Allocated (Group: npx_hdmi_boneli_pins)	
P0_42	45	0x808/064	75	GPIO0_11	gpio[1 11]	pr1_pru0_pru_r31_5	pr1_pru0_pru_r30_5	mcasp0_rx3	eQEP2_in	gpmc_a5	ldc_data5	T3	Allocated (Group: npx_hdmi_boneli_pins)	
P0_43	42	0x808/068	72	GPIO0_28	gpio[1 8]	pr1_pru0_pru_r31_2	pr1_pru0_pru_r30_2	ehprm02_trigpos_in	gpmc_a2	ldc_data2	R3	Allocated (Group: npx_hdmi_boneli_pins)		
P0_44	43	0x808/068	73	GPIO0_29	gpio[1 9]	pr1_pru0_pru_r31_3	pr1_pru0_pru_r30_3	ehprm02_fsync	gpmc_a3	ldc_data3	R4	Allocated (Group: npx_hdmi_boneli_pins)		
P0_45	40	0x808/060	70	GPIO0_26	gpio[1 6]	pr1_pru0_pru_r31_0	pr1_pru0_pru_r30_0	ehprm02A	gpmc_a0	ldc_data0	R1	Allocated (Group: npx_hdmi_boneli_pins)		
P0_46	41	0x808/064	71	GPIO0_27	gpio[1 7]	pr1_pru0_pru_r31_1	pr1_pru0_pru_r30_1	ehprm02B	gpmc_a1	ldc_data1	R2	Allocated (Group: npx_hdmi_boneli_pins)		

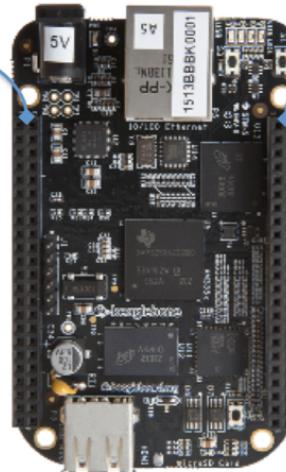
<https://vadl.github.io/images/bbb/P8Header.png>

Beaglebone Black P9 Header

Head pin	SPINS	ADDR/OFFSET	Name	GPIO NO.	Mode7	Mode6	Mode5	Mode4	Mode3	Mode2	Mode1	Mode0	PIN	Notes	
P9_01			GND											Ground	
P9_02			GND											Ground	
P9_03			DC_3.3V											250mA Max Current	
P9_04			DC_5.0V											250mA Max Current	
P9_05			VDD_5V											1A Max Current if DC Jack powered	
P9_06			VDD_5V											1A Max Current only if DC Jack powered	
P9_07			SYS_5V											250mA Max Current	
P9_08			SYS_5V											250mA Max Current	
P9_09			PWRKEY											Has a 5V level pulled up by 1P56521C	
P9_10			SYS_RESETn												
P9_11	28	0x870/070	UART1_RXD	30	gpio[30]	uart4_rxrd_mux2	mmc1_sdcl	rml2_crs_dv	gmcic_csn4	ml2_crs	gmcic_wrt0	A10			
P9_12	30	0x870/070	GP01_128	60	gpio[128]	mcasp0_aclk_rxrd_mux3	gmcic_dir	mmci2_dat3	gmcic_csn6	ml2_col	gmcic_be10	A11			
P9_13	29	0x874/074	UART1_TXD	11	gpio[11]	uart4_txrd_mux2	mmc2_sdcl	rml2_cswerr	gmcic_csn5	ml2_crs	gmcic_wrt1	A12			
P9_14	18	0x874/074	EHRPWM16	50	gpio[50]	ehrpwm16_mux1	mmc1_sdcl	rml2_tx1	gmcic_csn1	ml2_tx1	gmcic_a1	A13			
P9_15	16	0x840/040	GP01_16	48	gpio[16]	ehrpwm16_trigzone_input	mmc1_sdcl	rml2_txem	ml2_tx1	gmcic_a0	A13				
P9_16	19	0x84c/04c	EHRPWM18	51	gpio[19]	ehrpwm18_mux1	gmcic_a19	mmci2_dat2	rml2_tdi	ml2_tx2	gmcic_a3	A14			
P9_17	87	0x95c/18c	I2CL_SDA	5	gpio[5]	ehrpwm0_sync1	I2C1_SCL	mmci2_sdwp	sp0_tdi	I2C1_SDA	mmci2_sdwp	A15			
P9_18	86	0x85c/158	I2CL_SDA	4	gpio[4]	ehrpwm0_trigzone	I2C1_SCL	mmci2_sdwp	sp0_tdi	I2C1_SDA	mmci2_sdwp	A15			
P9_19	89	0x878/178	I2CL_SDA	13	gpio[13]	pr1_uart0_rts_n	I2C1_SCL	mmci2_sdwp	sp0_tdi	I2C1_SDA	mmci2_sdwp	A15			
P9_20	94	0x878/178	I2CL_SDA	12	gpio[12]	pr1_uart0_rts_n	I2C2_SDA	mmci2_sdwp	sp0_tdi	I2C2_SDA	mmci2_sdwp	A16	Allocated [Group: plimms (2x pins)]		
P9_21	85	0x945/154	UART2_RXD	3	gpio[3]	EMU3_mux1	pr1_uart0_rts_n	ehrpwm0B	I2C2_SCL	uart2_tdi	sp0_d0	B17			
P9_22	84	0x945/154	UART2_RXD	2	gpio[2]	EMU2_mux1	pr1_uart0_rts_n	ehrpwm0A	I2C2_SDA	uart2_rxd	sp0_sck	B17			
P9_23	97	0x944/184	UART1_RXD	49	gpio[49]	pr1_uart0_rts_n	gmcic_a17	mmci2_sdwp	sp0_tdi	I2C1_SDA	mmci2_sdwp	C18			
P9_24	97	0x944/184	UART1_RXD	15	gpio[15]	pr1_uart0_rts_n	pr1_uart0_rts_n	I2C1_SCL	dcanc1_tx	mmci2_sdwp	uart1_tdi	D15			
P9_25	107	0x944/184	GP01_21	117	gpio[21]	pr1_uart0_rts_n	pr1_uart0_rts_n	EMU4_mux2	mcasp1_axr1	mcasp0_axr3	eQEP0_strobe	mcasp0_afhika	A14	Allocated [Group: mcasp0_pins]	
P9_26	96	0x880/180	UART1_RXD	14	gpio[14]	pr1_uart0_rts_n	pr1_uart0_rts_n	EMU4_mux2	mcasp1_axr1	mcasp0_axr3	eQEP0_strobe	mcasp0_afhika	A14	Allocated [Group: mcasp0_pins]	
P9_27	105	0x944/184	GP01_19	115	gpio[19]	pr1_uart0_rts_n	pr1_uart0_rts_n	EMU2_mux2	mcasp1_fx	mcasp0_axr3	eQEP0_in	mcasp0_afhika	C13		
P9_28	89	0x944/184	UART1_RXD	10	gpio[10]	pr1_uart0_rts_n	pr1_uart0_rts_n	EMU2_mux2	mcasp1_fx	mcasp0_axr3	eQEP0_in	mcasp0_afhika	C13		
P9_29	101	0x944/184	SP1L_D0	111	gpio[115]	pr1_uart0_rts_n	pr1_uart0_rts_n	EMU2_mux2	mcasp1_fx	mcasp0_axr3	eQEP0_in	mcasp0_afhika	C13		
P9_30	102	0x998/198	SP1L_D1	112	gpio[316]	pr1_uart0_rts_n	pr1_uart0_rts_n	EMU2_mux2	mcasp1_fx	mcasp0_axr3	eQEP0_in	mcasp0_afhika	C13		
P9_31	100	0x990/190	SP1L_SCLK	130	gpio[14]	pr1_uart0_rts_n	pr1_uart0_rts_n	EMU2_mux2	mcasp1_fx	mcasp0_axr3	eQEP0_in	mcasp0_afhika	C13		
P9_32			VADC											Voltage Reference for ADC (NB: 1.8V)	
P9_33			AIN1												
P9_34			AGND											CB NB: 1.8V tolerant	
P9_35			AIN6											CB Ground for ADC	
P9_36			AIN5											NB: 1.8V tolerant	
P9_37			AIN2											NB: 1.8V tolerant	
P9_38			AIN3											NB: 1.8V tolerant	
P9_39			AIN0											NB: 1.8V tolerant	
P9_40			AIN1											C7 NB: 1.8V tolerant	
P9_41A	109	0x94f/1b4	CLKOUT2	20	gpio[20]	EMU3_mux0	pr1_pru0_pru13_16	timer7_mux1	clkout2	tc1kin	adma_event_intr1	D14	Both signals are connected to P22 / P11		
P9_41B	109	0x94f/1a8	GP01_20	116	gpio[20]	pr1_pru0_pru13_16	pr1_pru0_pru13_0	emu3	mcasp1_axr0	eQEP0_index	mcasp0_axr1	D15	Both signals are connected to P22 / P11		
P9_42A	89	0x94d/164	GPIO_7	7	gpio[7]	sdma_event_intr2	mmc0_sdwp	sp1l_clk	pr1_ecap0_ecap_caplin_apmem_o	sp1l_csi1	uart3_tdi	eQEP0_in_PMM0_out	D16	Both signals are connected to P22 / P11	
P9_42B	0x9af/1a0	GP01_18	114	gpio[18]	pr1_pru0_pru13_4	pr1_pru0_pru13_0	mcasp1_arlkx	mcasp0_axr2	eQEP0A_in	mcasp0_arlkx	B12	Allocated? Mcasp0_pins? Check...			
P9_43			GND											- See Pg 50 of the SRM	
P9_44			GND												
P9_45			GND												
P9_46			GND												
P9 Header	cat SPINS	ADDR + Offset from:	Name	GPIO NO.	Mode 7 (Mode 0)									Notes	
	44+10000	44+10800			(Mode 7)									Please e-mail directly at: derk@derkmonks.ie	
														If you notice a mistake	
														Thanks Frank for the PRU work!	
														For updates see: www.derkmonks.ie	

<https://vadl.github.io/images/bbb/P9Header.png>

Cape Expansion Headers



P9

DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3
VDD_5V	5	6	VDD_5V
SYS_5V	7	8	SYS_5V
PWR_BUT	9	10	SYS_RESETN
UART4_RXD	11	12	GPIO_60
UART4_TXD	13	14	EHRPWM1A
GPIO_48	15	16	EHRPWM1B
SPI0_CS0	17	18	SPI0_D1
I2C2_SCL	19	20	I2C2_SDA
SPI0_DO	21	22	SPI0_SCLK
GPIO_49	23	24	UART1_TXD
GPIO_117	25	26	UART1_RXD
GPIO_115	27	28	SPI1_CS0
SPI1_DO	29	30	GPIO_112
SPI1_SCLK	31	32	VDD_ADC
AIN4	33	34	GNDA_ADC
AIN6	35	36	AIN5
AIN2	37	38	AIN3
AIN0	39	40	AIN1
GPIO_20	41	42	ECAPPWM0
DGND	43	44	DGND
DGND	45	46	DGND

P8

DGND	1	2	DGND
MMC1_DAT6	3	4	MMC1_DAT7
MMC1_DAT2	5	6	MMC1_DAT3
GPIO_66	7	8	GPIO_67
GPIO_69	9	10	GPIO_68
GPIO_45	11	12	GPIO_44
EHRPWM2B	13	14	GPIO_26
GPIO_47	15	16	GPIO_46
GPIO_27	17	18	GPIO_65
EHRPWM2A	19	20	MMC1_CMD
MMC1_CLK	21	22	MMC1_DAT5
MMC1_DAT4	23	24	MMC1_DAT1
MMC1_DATO	25	26	GPIO_61
LCD_VSYNC	27	28	LCD_PCLK
LCD_HSYNC	29	30	LCD_AC_BIAS
LCD_DATA14	31	32	LCD_DATA15
LCD_DATA13	33	34	LCD_DATA11
LCD_DATA12	35	36	LCD_DATA10
LCD_DATA8	37	38	LCD_DATA9
LCD_DATA6	39	40	LCD_DATA7
LCD_DATA4	41	42	LCD_DATA5
LCD_DATA2	43	44	LCD_DATA3
LCD_DATA0	45	46	LCD_DATA1

LEGEND

- POWER/GROUND/RESET
- AVAILABLE DIGITAL
- AVAILABLE PWM
- SHARED I2C BUS
- RECONFIGURABLE DIGITAL
- ANALOG INPUTS (1.8V)

Each digital I/O pin has 8 different modes that can be selected, including GPIO.

65 possible digital I/Os

P9				P8			
DGND	1	2	DGND	DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3	GPIO_38	3	4	GPIO_39
VDD_5V	5	6	VDD_5V	GPIO_34	5	6	GPIO_35
SYS_5V	7	8	SYS_5V	GPIO_66	7	8	GPIO_67
PWR_BUT	9	10	SYS_RESETN	GPIO_69	9	10	GPIO_68
GPIO_30	11	12	GPIO_60	GPIO_45	11	12	GPIO_44
GPIO_31	13	14	GPIO_50	GPIO_23	13	14	GPIO_26
GPIO_48	15	16	GPIO_51	GPIO_47	15	16	GPIO_46
GPIO_5	17	18	GPIO_4	GPIO_27	17	18	GPIO_65
I2C2_SCL	19	20	I2C2_SDA	GPIO_22	19	20	GPIO_63
GPIO_3	21	22	GPIO_2	GPIO_62	21	22	GPIO_37
GPIO_49	23	24	GPIO_15	GPIO_36	23	24	GPIO_33
GPIO_117	25	26	GPIO_14	GPIO_32	25	26	GPIO_61
GPIO_115	27	28	GPIO_113	GPIO_86	27	28	GPIO_88
GPIO_111	29	30	GPIO_112	GPIO_87	29	30	GPIO_89
GPIO_110	31	32	VDD_ADC	GPIO_10	31	32	GPIO_11
AIN4	33	34	GNDA_ADC	GPIO_9	33	34	GPIO_81
AIN6	35	36	AIN5	GPIO_8	35	36	GPIO_80
AIN2	37	38	AIN3	GPIO_78	37	38	GPIO_79
AIN0	39	40	AIN1	GPIO_76	39	40	GPIO_77
GPIO_20	41	42	GPIO_7	GPIO_74	41	42	GPIO_75
DGND	43	44	DGND	GPIO_72	43	44	GPIO_73
DGND	45	46	DGND	GPIO_70	45	46	GPIO_71

In GPIO mode, each digital I/O can produce interrupts.

8 PWMs and 4 timers

P9				P8			
DGND	1	2	DGND	DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3	GPIO_38	3	4	GPIO_39
VDD_5V	5	6	VDD_5V	GPIO_34	5	6	GPIO_35
SYS_5V	7	8	SYS_5V	TIMER4	7	8	TIMER7
PWR_BUT	9	10	SYS_RESETN	TIMER5	9	10	TIMER6
GPIO_30	11	12	GPIO_60	GPIO_45	11	12	GPIO_44
GPIO_31	13	14	EHRPWM1A	EHRPWM2B	13	14	GPIO_26
GPIO_48	15	16	EHRPWM1B	GPIO_47	15	16	GPIO_46
GPIO_5	17	18	GPIO_4	GPIO_27	17	18	GPIO_65
I2C2_SCL	19	20	I2C2_SDA	EHRPWM2A	19	20	GPIO_63
EHRPWMOB	21	22	EHRPMOA	GPIO_62	21	22	GPIO_37
GPIO_49	23	24	GPIO_15	GPIO_36	23	24	GPIO_33
GPIO_117	25	26	GPIO_14	GPIO_32	25	26	GPIO_61
GPIO_115	27	28	ECAPPWM2	GPIO_86	27	28	GPIO_88
EHRPWMOB	29	30	GPIO_112	GPIO_87	29	30	GPIO_89
EHRPMOA	31	32	VDD_ADC	GPIO_10	31	32	GPIO_11
AIN4	33	34	GNDA_ADC	GPIO_9	33	34	EHRPWM1B
AIN6	35	36	AIN5	GPIO_8	35	36	EHRPWM1A
AIN2	37	38	AIN3	GPIO_78	37	38	GPIO_79
AIN0	39	40	AIN1	GPIO_76	39	40	GPIO_77
GPIO_20	41	42	ECAPPWMO	GPIO_74	41	42	GPIO_75
DGND	43	44	DGND	GPIO_72	43	44	GPIO_73
DGND	45	46	DGND	EHRPWM2A	45	46	EHRPWM2B

Up to 8 digital I/O pins can be configured with pulse-width modulators (PWM) to produce signals to control motors or create analog voltage levels, without taking up any extra CPU cycles.

7 analog inputs (1.8V)

P9				P8			
DGND	1	2	DGND	DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3	GPIO_38	3	4	GPIO_39
VDD_5V	5	6	VDD_5V	GPIO_34	5	6	GPIO_35
SYS_5V	7	8	SYS_5V	GPIO_66	7	8	GPIO_67
PWR_BUT	9	10	SYS_RESETN	GPIO_69	9	10	GPIO_68
GPIO_30	11	12	GPIO_60	GPIO_45	11	12	GPIO_44
GPIO_31	13	14	GPIO_50	GPIO_23	13	14	GPIO_26
GPIO_48	15	16	GPIO_51	GPIO_47	15	16	GPIO_46
GPIO_5	17	18	GPIO_4	GPIO_27	17	18	GPIO_65
I2C2_SCL	19	20	I2C2_SDA	GPIO_22	19	20	GPIO_63
GPIO_3	21	22	GPIO_2	GPIO_62	21	22	GPIO_37
GPIO_49	23	24	GPIO_15	GPIO_36	23	24	GPIO_33
GPIO_117	25	26	GPIO_14	GPIO_32	25	26	GPIO_61
GPIO_115	27	28	GPIO_113	GPIO_86	27	28	GPIO_88
GPIO_111	29	30	GPIO_112	GPIO_87	29	30	GPIO_89
GPIO_110	31	32	VDD_ADC	GPIO_10	31	32	GPIO_11
AIN4	33	34	GNDA_ADC	GPIO_9	33	34	GPIO_81
AIN6	35	36	AIN5	GPIO_8	35	36	GPIO_80
AIN2	37	38	AIN3	GPIO_78	37	38	GPIO_79
AIN0	39	40	AIN1	GPIO_76	39	40	GPIO_77
GPIO_20	41	42	GPIO_7	GPIO_74	41	42	GPIO_75
DGND	43	44	DGND	GPIO_72	43	44	GPIO_73
DGND	45	46	DGND	GPIO_70	45	46	GPIO_71

Make sure you don't input more than 1.8V to the analog input pins.

This is a single 12-bit analog-to-digital converter with 8 channels, 7 of which are made available on the headers.

2 I2C ports					
P9			P8		
DGND	1	2	DGND		
VDD_3V3	3	4	VDD_3V3		
VDD_5V	5	6	VDD_5V		
SYS_5V	7	8	SYS_5V		
PWR_BUT	9	10	SYS_RESETN		
GPIO_30	11	12	GPIO_60		
GPIO_31	13	14	GPIO_50		
GPIO_48	15	16	GPIO_51		
I2C1_SCL	17	18	I2C1_SDA		
I2C2_SCL	19	20	I2C2_SDA		
I2C2_SCL	21	22	I2C2_SDA		
GPIO_49	23	24	I2C1_SCL		
GPIO_117	25	26	I2C1_SDA		
GPIO_115	27	28	GPIO_113		
GPIO_111	29	30	GPIO_112		
GPIO_110	31	32	VDD_ADC		
AIN4	33	34	GND_ADC		
AIN6	35	36	AIN5		
AIN2	37	38	AIN3		
AIN0	39	40	AIN1		
GPIO_20	41	42	GPIO_7		
DGND	43	44	DGND		
DGND	45	46	DGND		

The first I2C bus is utilized for reading EEPROMS on cape add-on boards and can't be used for other digital I/O operations without interfering with that function, but you can still use it to add other I2C devices at available addresses.

The second I2C bus is available for you to configure and use.

2 SPI ports

P9		P8	
DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3
VDD_5V	5	6	VDD_5V
SYS_5V	7	8	SYS_5V
PWR_BUT	9	10	SYS_RESETN
GPIO_30	11	12	GPIO_60
GPIO_31	13	14	GPIO_50
GPIO_48	15	16	GPIO_51
SPI0_CS0	17	18	SPI0_D1
SPI1_CS1	19	20	SPI1_CS0
SPI0_DO	21	22	SPI0_SCLK
GPIO_49	23	24	GPIO_15
GPIO_117	25	26	GPIO_14
GPIO_115	27	28	SPI1_CS0
SPI1_DO	29	30	SPI1_D1
SPI1_SCLK	31	32	VDD_ADC
AIN4	33	34	GNDA_ADC
AIN6	35	36	AIN5
AIN2	37	38	AIN3
AIN0	39	40	AIN1
GPIO_20	41	42	SPI1_CS1
DGND	43	44	DGND
DGND	45	46	DGND

For shifting out data fast, you might consider using one of the SPI ports.

BeagleBoard.org - bone101

version 1.1
April 1st, 06

vi / vim graphical cheat sheet

Esc	normal mode											
~ toggle case	! external filter	@ play macro	# prev ident	\$ col	% goto match	^ "soft" bol	& repeat :s	* next ident	(begin sentence) end sentence	"soft" bol _ down	+ next line
~* goto mark	1	2	3	4	5	6	7	8	9	0 "hard" bol	- prev line	= autoformat
Q ex mode	W next WORD	E end WORD	R replace mode	T back 'till	Y yank line	U undo line	I insert at bol	O open above	P paste before	{ begin parag.	}	end parag.
Q record macro	W next word	E end word	R replace char	T 'till	Y yank	U undo	I insert mode	O open below	P paste after	{ misc	}	• misc
A append at eol	S subst line	D delete to eol	F "back" find ch	G eof/ln	H screen top	J join lines	K help	L screen bottom	: ex cmd line	!! reg. spec	bol/ goto col	
a append	s subst char	d delete	f find char	g extra cmds	h ←	j ↓	k ↑	l →	; repeat ; t/f/F	' goto mk. bol	\ not used!	
Z quit ^	X back-space	C change to eol	V visual lines	B prev WORD	N prev (find)	M screen mid'	< un- ^	> indent ^	? find (rev.)			
Z extra ^	X delete char	C change	V visual mode	b prev word	n next (find)	m set mark	, reverse , t/F/f/F	+ repeat cmd	/ find			
Q·												
motion	moves the cursor, or defines the range for an operator	command	direct action command, if red, it enters insert mode	operator	requires a motion afterwards, operates between cursor & destination	extra	special functions, requires extra input	visual mode:	Main command line commands ('ex'):	Notes:		
								:w (save), :q (quit), :q! (quit w/o saving)	(1) use "x before a yank/paste/del command to use that register ('clipboard') (x=a..z,*)			
								:e f (open file f), :%s/x/y/g (replace 'x' by 'y' filewide), :h (help in vim), :new (new file in vim),	(e.g.: "\$y\$ to copy rest of line to reg 'a')			
									(2) type in a number before any action to repeat it that number of times (e.g.: 2p, d2w, 5i, d4j)			
									(3) duplicate operator to act on current line (dd = delete line, >> = indent line)			
									(4) ZZ to save & quit, ZQ to quit w/o saving			
									(5) zt: scroll cursor to top, zb: bottom, zz: center			
									(6) gg: top of file (vim only), gf: open file under cursor (vim only)			

For a graphical vi/vim tutorial & more tips, go to www.viemu.com - home of ViEmu, vi/vim emulation for Microsoft Visual Studio

Vim(Terminal Text Editor) Cheat Sheet

- Flashing

[Updating the software image on a BeagleBone Black - Zageo LLC - Medium](#)

[Forum | Factory defaults for a Beaglebone Black - GHI Electronics](#)

- Expanding Storage

[Beagleboard:Expanding File System Partition On A microSD - eLinux.org](#)

[Extending Micro SD Card space for Beaglebone Bl... | element14 | BeagleBoard](#)

[How to resize Debian 9.2 root partition to fit SD card - SomLabs Wiki](#)

[Noob's guide to Embedded Systems: How to Increase Memory of Beaglebone Black](#)

- Running program on boot

[Booting Beaglebone Black with Autorun Python Script](#)

[BeagleBone Black Launch Python Script at Boot like Arduino Sketch | Billwaa's Blog](#)