

Beaglebone Intro

Login/Access Test Functions

- Connect to BeagleBone-8EC2
- Wifi password: BeagleBone
- Re-upload code

```
sudo rm -R blood-smear  
git clone https://github.com/mucolon/blood-smear.git
```

- Run basic smear code

```
sudo python3 blood-smear/test/basic_smear.py
```

- Edit code

```
sudo nano blood-smear/test/basic_smear.py
```

- Shutdown

```
sudo shutdown -h now
```

Setup Steps

1. Flash Debian 9.5 LXQT Version
2. Connect to wifi [Robot Control Library: Networking WiFi](#)
3. Repartition to use all of the microSD's memory

```
sudo /opt/scripts/tools/grow_partition.sh
```

Reboot after

```
reboot
```

4. Download Micro text editor (personal preference)

```
curl https://getmic.ro | bash
```

Move to bin directory

```
sudo mv ~/micro /bin/micro
```

5. Update packages

```
sudo apt update  
sudo apt upgrade -y  
sudo apt install python3 python3-pip python3-numpy build-essential python3-dev  
autoconf-archive python3-scipy python3-tk -y  
sudo pip3 install rcpy Adafruit_BBIO  
sudo pip3 install --upgrade Adafruit_BBIO
```

6. Disable Bluetooth (speeds up boot time)

Open this file `sudo micro /etc/rc.local`

File may be blank. Add this below

```
rfkill block bluetooth  
exit 0
```

8. Clone GitHub repository with blood smearing device code

```
git clone https://github.com/mucolon/blood-smear.git
```

Linux Commands

Underlined text is your input (ie. _yourLogin_)

GitHub

- Establish GitHub connection

```
git remote set-url origin git@github.com:mucolon/blood-smear.git
```

- Commands to push latest code updates to GitHub

```
git add -A  
git commit -am "my update msg"  
git push
```

Account Management

- Add new user account

```
sudo useradd -m _yourLogin_
```

- Add user password (You will be prompted to enter your new password)

```
sudo passwd _yourLogin_
```

- Add user to the sudo group

```
sudo usermod -aG sudo _yourLogin_
```

- Edit the `/etc/passwd` to set terminal to bash instead of dash

```
sudo nano /etc/passwd
```

Add `/bin/bash` at the very end of the file after the last `:` of your added username

General

- Shutdown
 - `sudo shutdown -h now`
- Logout
 - `logout` or `sudo logout`
- List contents of current directory
 - `ls`

- Show current directory
 - `pwd`
- Change directory (folder)

```
cd _directoryName_
cd / (Beaglebone root directory)
cd ~ (User home directory)
cd .. (Previous directory)
```

- Make a new directory (folder)

```
mkdir _directoryName_
```

- Create/Open a text file

```
nano _fileName_ (basic text editor)
micro _fileName_ (better text editor) (See below for download instructions)
```

- Create/Open a text file (show line numbers)

```
nano -c _fileName_
```

- Rename/Move file

```
mv _oldFileName_ _newFileName_
```

- Remove directory or file

```
rm -R _fileName_
```

- Superuser login/logout

```
sudo su (login)
```

```
sudo -s (login)
```

```
exit (logout)
```

Troubleshooting

- Resetting remote host ID
 - `ssh-keygen -R 192.168.8.1`
- Check Debian version
 - `cat /etc/debian_version`
 - `cat /etc/dogtag`
- Disabling Bluetooth (speeds up boot time)

Open this file `sudo micro /etc/rc.local`

File may be blank. Add this below

```
rfkill block bluetooth  
exit 0
```

- Update eMMC uBoot for GPIO pin access

```
sudo dd if=/dev/zero of=/dev/mmcblk1 bs=1M count=100
```

Networking

[Robot Control Library: Networking WiFi](#)

Upgrading Packages (RobotControlLib)

- Update the list of available packages

```
sudo apt update
```

- Upgrade important package

```
sudo apt upgrade apt
```

- Upgrade to the latest version of the RobotControlLib (may be out of date)

```
sudo apt upgrade RobotControlLib
```

- Test if everything is working correctly

```
rc_test_drivers
```

- Making a Program Run on Boot

[Robot Control Library: Making a Program Run on Boot](#)

- Background Services

[Robot Control Library: Background Services](#)

Micro Text Editor

- Better text editor than nano that allows for easy color customization and mouse actions over ssh

```
curl https://getmic.ro | bash (Download link)
```

```
mv /home/mae156/micro /bin/micro (Add micro to bin for use in any directory)
```

Hardware

- 5V pins

VDD: input pins to power BeagleBone

SYS: output pins to power external components

Running Programs

- Python

```
sudo python3 _programName.py_
```

Python Libraries

```
sudo apt update  
sudo apt install python3 python3-pip python3-numpy  
sudo apt install build-essential python-dev python-pip -y  
sudo pip3 install Adafruit_BBIO  
sudo pip3 install --upgrade Adafruit_BBIO  
sudo apt install autoconf-archive python3-scipy  
sudo pip3 install rcpy
```

[GitHub - mucolon/adafruit-beaglebone-io-python: Adafruit's BeagleBone IO Python Library](#)

[GitHub - mucolon/rcpy: Python Library for Robotics Cape on Beaglebone Black and Beaglebone Blue](#)

- Interrupts

[Detecting Interrupts and Distinguishing between Rising and Falling · Issue #16 · adafruit/adafruit-beaglebone-io-python · GitHub](#)

UART

[UART | Setting up IO Python Library on BeagleBone Black | Adafruit Learning System](#)
[RoboGoby - ROV/AUV Submersible: April 2014](#)

Extras

- Kill a user's session

```
sudo pkill -9 -u _userName_
```

- Installing Java

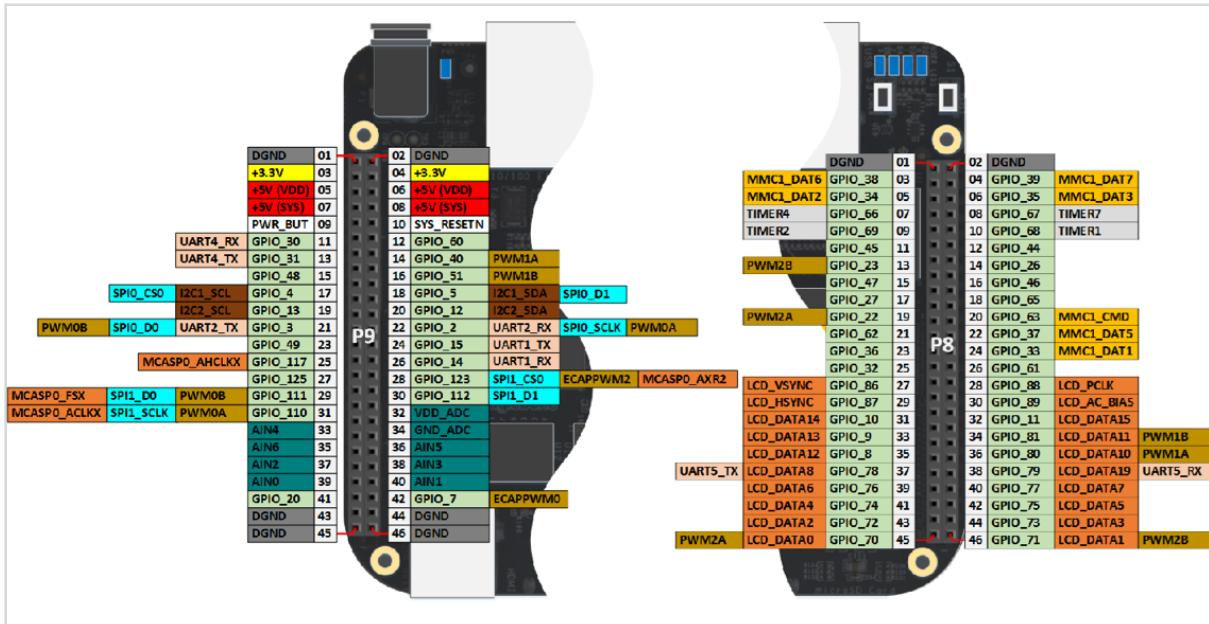
[Setting up Oracle Java 8, Gradle, and Maven On Beaglebone Green | AVR | Hackaday.io](#)

apt command	the command it replaces	function of the command
apt install	apt-get install	Installs a package
apt remove	apt-get remove	Removes a package
apt purge	apt-get purge	Removes package with configuration
apt update	apt-get update	Refreshes repository index
apt upgrade	apt-get upgrade	Upgrades all upgradable packages
apt autoremove	apt-get autoremove	Removes unwanted packages
apt full-upgrade	apt-get dist-upgrade	Upgrades packages with auto-handling of dependencies
apt search	apt-cache search	Searches for the program
apt show	apt-cache show	Shows package details

new apt command	function of the command
apt list	Lists packages with criteria (installed, upgradable etc)
apt edit-sources	Edits sources list

Resources

- Pin Setup



https://www.mathworks.com/help/supportpkg/beagleboneio/ug/beaglebone_black_pinmap.png

P8				P9				
	1	2		1	2			
	GND	 	GND		GND	 	GND	
GPIO1_6	P8.3	 	P8.4	GPIO1_7		VDD 3.3V	 	VDD 3.3V
GPIO1_2	P8.5	 	P8.6	GPIO1_3		VDD 5V	 	VDD 5V
GPIO2_2	P8.7	 	P8.8	GPIO2_3		SYS 5V	 	SYS 5V
GPIO2_5	P8.9	 	P8.10	GPIO2_4		PWR_BUT	 	SYS_RESETn
GPIO1_13	P8.11	 	P8.12	GPIO1_12	GPIO0_30	P9.11	 	P9.12 GPIO1_28
GPIO0_23	P8.13	 	P8.14	GPIO0_26	GPIO0_31	P9.13	 	P9.14 GPIO1_18
GPIO1_15	P8.15	 	P8.16	GPIO1_14	GPIO1_16	P9.15	 	P9.16 GPIO1_19
GPIO0_27	P8.17	 	P8.18	GPIO2_1	GPIO0_5	P9.17	 	P9.18 GPIO0_4
GPIO0_22	P8.19	 	P8.20	GPIO1_31	GPIO0_13	P9.19	 	P9.20 GPIO0_12
GPIO1_30	P8.21	 	P8.22	GPIO1_5	GPIO0_3	P9.21	 	P9.22 GPIO0_2
GPIO1_4	P8.23	 	P8.24	GPIO1_1	GPIO1_17	P9.23	 	P9.24 GPIO0_15
GPIO1_0	P8.25	 	P8.26	GPIO1_29	GPIO3_21	P9.25	 	P9.26 GPIO0_14
GPIO2_22	P8.27	 	P8.28	GPIO2_24	GPIO3_19	P9.27	 	P9.28 GPIO3_17
GPIO2_23	P8.29	 	P8.30	GPIO2_25	GPIO3_15	P9.29	 	P9.30 GPIO3_16
GPIO0_10	P8.31	 	P8.32	GPIO0_11	GPIO3_14	P9.31	 	VDD_ADC(1.8V)
GPIO0_9	P8.33	 	P8.34	GPIO2_17	AIN4	P9.33	 	GNDA_ADC
GPIO0_8	P8.35	 	P8.36	GPIO2_16	AIN6	P9.35	 	P9.36 AIN5
GPIO2_14	P8.37	 	P8.38	GPIO2_15	AIN2	P9.37	 	P9.38 AIN3
GPIO2_12	P8.39	 	P8.40	GPIO2_13	AIN0	P9.39	 	P9.40 AIN1
GPIO2_10	P8.41	 	P8.42	GPIO2_11	GPIO0_20	P9.41	 	P9.42 GPIO0_7
GPIO2_8	P8.43	 	P8.44	GPIO2_9		GND	 	GND
GPIO2_6	P8.45	 	P8.46	GPIO2_7		GND	 	GND
	45	46			45	46		

<http://mkaczanowski.com/wp-content/uploads/2014/03/resizedimage600667-hwio-beaglebone-ports2.png>

Beaglebone Black P8 Header

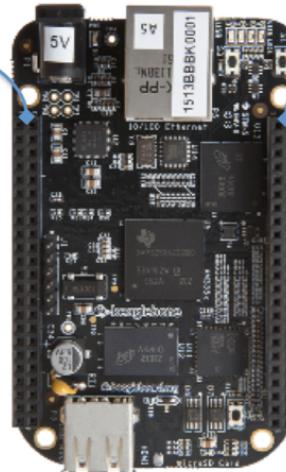
Header_pin	SPINS	ADDR/OFFSET	GPIO NO.	Name	Mode7	Mode6	Mode5	Mode4	Mode3	Mode2	Mode1	Mode0	PIN	Notes
PB_01	1	0x838/008	38	GPIO1_6	gpio[1 6]								gnd	Ground
PB_02	2	0x838/010	39	GPIO1_7	gpio[1 7]								gnd	Ground
PB_03	6	0x838/018	38	GPIO1_6	gpio[1 6]								R9	Used on Board (Group: primux_emmc2_pins)
PB_04	7	0x838/01c	39	GPIO1_7	gpio[1 7]								T9	Used on Board (Group: primux_emmc2_pins)
PB_05	2	0x808/008	34	GPIO1_2	gpio[1 2]								R8	Used on Board (Group: primux_emmc2_pins)
PB_06	3	0x808/00c	35	GPIO1_3	gpio[1 3]								T8	Used on Board (Group: primux_emmc2_pins)
PB_07	36	0x808/00e	66	TIMER8	gpio[2 3]									
PB_08	37	0x804/004	67	TIMER7	gpio[2 3]								T7	
PB_09	39	0x808/00c	69	TIMER8	gpio[2 5]								T6	
PB_10	38	0x808/008	68	TIMER6	gpio[2 4]								U6	
PB_11	13	0x834/034	45	QSPI0_4	spi0[1 4]									
PB_12	12	0x834/030	44	GPIO1_12	gpio[1 12]	pr1_pru0_pru<_30_14	0x0f12_0x0	MMC1_DAT0	mmc1_dat0	mmc1_dat5	gpmc_acd6	gpmc_acd7	R12	
PB_13	9	0x834/024	23	EHRPWM2	gpio[2 3]								T12	
PB_14	10	0x828/028	26	GPIO0_26	gpio[2 6]								T10	
PB_15	15	0x838/03c	47	GPIO1_15	gpio[1 15]	pr1_pru0_pru<_31_15	eCEP2_index	mmc2_dat0	mmc1_dat2	mmc1_dat7	gpmc_acd9	gpmc_acd10	T11	
PB_16	14	0x838/038	46	GPIO1_14	gpio[1 14]	pr1_pru0_pru<_31_14	eCEP2_index	mmc2_dat1	mmc1_dat3	mmc1_dat8	gpmc_acd10	gpmc_acd11	U13	
PB_17	11	0x834/030	22	GPIO0_22	gpio[2 1]								T11	
PB_18	35	0x808/008	65	GPIO1_2	gpio[2 1]	mcasp0_fsr		eHrpwm0_sync	mmc2_dat2	mmc1_dat3	mmc1_dat8	gpmc_acd11	U12	
PB_19	8	0x800/020	22	EHRPWM2A	gpio[2 2]								U10	
PB_20	33	0x884/084	63	GPIO1_31	gpio[1 31]	pr1_pru0_pru<_31_13	pr1_pru1_pru<_30_13	mmc2_dat4	mmc1_dat0	mmc1_dat5	gpmc_be1n	gpmc_cen2	V9	Used on Board (Group: primux_emmc2_pins)
PB_21	21	0x800/030	62	GPIO1_30	gpio[1 30]	pr1_pru1_pru<_31_12	pr1_pru1_pru<_30_12	mmc1_clk	mmc1_dat1	mmc1_dat6	gpmc_cen1	U9	Used on Board (Group: primux_emmc2_pins)	
PB_22	5	0x834/034	37	GPIO0_24	gpio[2 4]								V8	Used on Board (Group: primux_emmc2_pins)
PB_23	4	0x834/030	36	GPIO0_24	gpio[2 5]								U8	Used on Board (Group: primux_emmc2_pins)
PB_24	1	0x804/004	33	GPIO1_1	gpio[1 1]								V7	Used on Board (Group: primux_emmc2_pins)
PB_25	0	0x800/000	32	GPIO1_0	gpio[1 0]								U7	Used on Board (Group: primux_emmc2_pins)
PB_26	31	0x838/07c	61	GPIO1_29	gpio[1 29]								U6	
PB_27	56	0x800/000	86	GPIO0_22	gpio[2 2]	pr1_pru0_pru<_31_8	pr1_pru1_pru<_30_8	gpmc_ac	gpmc_ac1	gpmc_ac2	gpmc_ac3	gpmc_ac4	U5	Allocated (Group: npl_hdmi_bonel_pins)
PB_28	58	0x800/000	87	GPIO0_24	gpio[2 4]	pr1_pru0_pru<_31_10	pr1_pru1_pru<_30_10	gpmc_ac	gpmc_ac1	gpmc_ac2	gpmc_ac3	gpmc_ac4	V5	Allocated (Group: npl_hdmi_bonel_pins)
PB_29	57	0x800/004	87	GPIO0_22	gpio[2 1]	pr1_pru0_pru<_31_9	pr1_pru1_pru<_30_9	gpmc_ac	gpmc_ac1	gpmc_ac2	gpmc_ac3	gpmc_ac4	V5	Allocated (Group: npl_hdmi_bonel_pins)
PB_30	59	0x800/000	89	GPIO0_25	gpio[2 5]								U4	
PB_31	54	0x800/000	76	GPIO0_22	gpio[2 1]	pr1_pru0_pru<_31_6	pr1_pru1_pru<_30_6	uart1_ctn	uart1_rxn	uart1_txn	uart1_rxn	uart1_txn	V4	Allocated (Group: npl_hdmi_bonel_pins)
PB_32	55	0x800/000	71	UART1_RTNS	gpio[1 11]	uart1_rxn		mcasp0_rxr3	mcasp0_rxh3	eCEP1_index	gpmc_ac19	gpmc_ac20	T5	Allocated (Group: npl_hdmi_bonel_pins)
PB_33	53	0x800/000	74	UART1_TSNS	gpio[1 11]	uart1_rxn		mcasp0_rxr4	mcasp0_rxh4	eCEP1_strobe	gpmc_ac17	gpmc_ac18	V3	Allocated (Group: npl_hdmi_bonel_pins)
PB_34	51	0x800/000	81	UART3_RTNS	gpio[2 17]	uart1_rxn		mcasp0_rxr2	mcasp0_rxh2	eHrpwm1	gpmc_ac15	gpmc_ac16	U4	Allocated (Group: npl_hdmi_bonel_pins)
PB_35	52	0x800/000	80	UART3_TSNS	gpio[2 18]	uart1_rxn		mcasp0_rxr3	mcasp0_rxh3	eCEP1a_index	gpmc_ac16	gpmc_ac17	V2	Allocated (Group: npl_hdmi_bonel_pins)
PB_36	50	0x800/000	80	UART3_TSNS	gpio[2 16]	uart1_rxn		mcasp0_rxr4	mcasp0_rxh4	eHrpwm1a	gpmc_ac14	gpmc_ac15	U3	Allocated (Group: npl_hdmi_bonel_pins)
PB_37	48	0x800/000	79	UART3_RXD	gpio[2 14]	uart2_ctn		mcasp0_rxr5	mcasp0_rxh5	eHrpwm0_trigzone_in	gpmc_ac12	gpmc_ac13	U3	Allocated (Group: npl_hdmi_bonel_pins)
PB_38	49	0x800/000	79	UART3_RXD	gpio[2 13]	uart2_ctn		mcasp0_rxr6	mcasp0_rxh6	eHrpwm0_trigzone_in	gpmc_ac11	gpmc_ac12	U2	Allocated (Group: npl_hdmi_bonel_pins)
PB_39	46	0x800/000	76	GPIO0_22	gpio[2 17]	pr1_pru0_pru<_31_7	pr1_pru1_pru<_30_7	uart0_ctn	uart0_rxn	uart0_txn	uart0_rxn	uart0_txn	T3	Allocated (Group: npl_hdmi_bonel_pins)
PB_40	47	0x800/000	77	GPIO0_23	gpio[1 3]	pr1_pru0_pru<_31_4	pr1_pru1_pru<_30_4	uart0_rxn	uart0_txn	uart0_txn	uart0_rxn	uart0_txn	T4	Allocated (Group: npl_hdmi_bonel_pins)
PB_41	44	0x800/000	74	GPIO0_20	gpio[2 10]	pr1_pru0_pru<_31_4	pr1_pru1_pru<_30_4	uart0_rxn	uart0_txn	uart0_txn	uart0_rxn	uart0_txn	T1	Allocated (Group: npl_hdmi_bonel_pins)
PB_42	45	0x800/000	75	GPIO0_21	gpio[2 11]	pr1_pru0_pru<_31_5	pr1_pru1_pru<_30_5	uart0_rxn	uart0_txn	uart0_txn	uart0_rxn	uart0_txn	T2	Allocated (Group: npl_hdmi_bonel_pins)
PB_43	43	0x800/000	78	GPIO0_22	gpio[2 12]	pr1_pru0_pru<_31_2	pr1_pru1_pru<_30_2	uart0_rxn	uart0_txn	uart0_txn	uart0_rxn	uart0_txn	R3	Allocated (Group: npl_hdmi_bonel_pins)
PB_44	43	0x800/000	79	GPIO0_23	gpio[2 13]	pr1_pru0_pru<_31_3	pr1_pru1_pru<_30_3	uart0_rxn	uart0_txn	uart0_txn	uart0_rxn	uart0_txn	R4	Allocated (Group: npl_hdmi_bonel_pins)
PB_45	40	0x800/000	70	GPIO0_24	gpio[2 14]	pr1_pru0_pru<_31_0	pr1_pru1_pru<_30_0	uart0_rxn	uart0_txn	uart0_txn	uart0_rxn	uart0_txn	R1	Allocated (Group: npl_hdmi_bonel_pins)
PB_46	41	0x800/000	71	GPIO0_27	gpio[2 7]	pr1_pru0_pru<_31_1	pr1_pru1_pru<_30_1	uart0_rxn	uart0_txn	uart0_txn	uart0_rxn	uart0_txn	R2	Allocated (Group: npl_hdmi_bonel_pins)
PB Header	cat SPINS	ADDR +	GPIO NO.	Name	Mode 7	Mode 6	Mode 5	Mode 4	Mode 3	Mode 2	Mode 1	Mode 0	CPU	Updates Available at www.derekmolloy.ie
	Allocated	44e10000	(Mode 7)										PIN	
	Offset from:	44e10000												

GPIO Settings														
Bit 6	Bit 5	Bit 4	Bit 3	Bit 2,1,0										
Slew Control	Receiver Active	Pullup/Pulldown	Enable/Pullup/down	Mask Mode										
0 Fast	O Disable	O Pullup	O Pulldown select	000 Mode 0 to										
1 Slow	1 Pullup	1 Select	1 Pulldown	111 Mode 7										

e.g. OUTPUT GPIO0(mode7) #0x7 pulldown, #0x17 pullup, #0x7 no pullup/down
e.g. INPUT GPIO0(mode7) #0x27 pulldown, #0x37 pullup, #0x7 no pullup/down

Header_pin	SPINS	ADDR/OFFSET	GPIO NO.	Name	GPIO NO.	Mode 7	GPIO Settings	Mode 1	Mode 0	CPU	Notes
PB_01	1	0x878/008	53	GPIO1_21							
PB_02	2	0x878/00c	86	GPIO1_22							
PB_03	3	0x878/008	87	GPIO1_23							
PB_04	4	0x878/000	88	GPIO1_24							
PB_05	5	0x878/000	89	UART1_RXD	30	gpio[1 30]	uart1_rxn	mmc1_scl0	mmc1_dat0	mmc1_dat5	RESET_OUT
PB_06	6	0x878/000	90	UART1_RXD	31	gpio[1 31]	uart1_rxn	mmc2_scl0	mmc2_dat0	mmc2_dat5	T17
PB_07	12	0x878/000	91	UART1_RXD	31	gpio[1 31]	uart1_rxn	mmc2_scl1	mmc2_dat1	mmc2_dat6	NB: GPIOs limit current to 4mA output, and approx. 8mA on input.
PB_08	13	0x878/000	92	UART1_RXD	31	gpio[1 31]	uart1_rxn	mmc2_scl2	mmc2_dat2	mmc2_dat7	250mA Max Current
PB_09	14	0x878/000	93	UART1_RXD	31	gpio[1 31]	uart1_rxn	mmc2_scl3	mmc2_dat3	mmc2_dat8	250mA Max Current
PB_10	15	0x878/000	94	UART1_RXD	31	gpio[1 31]	uart1_rxn	mmc2_scl4	mmc2_dat4	mmc2_dat9	1A Max Current (only if EC Jack powered)
PB_11	16	0x878/000	95	UART1_RXD	31	gpio[1 31]	uart1_rxn	mmc2_scl5	mmc2_dat5	mmc2_dat10	1A Max Current (only if EC Jack powered)
PB_12	17	0x878/000	96	UART1_RXD	31	gpio[1 31]	uart1_rxn	mmc2_scl6	mmc2_dat6	mmc2_dat11	250mA Max Current
PB_13	29	0x878/000	97	UART1_RXD	31	gpio[1 31]	uart1_rxn	mmc2_scl7	mmc2_dat7	mmc2_dat12	Has a 5V Level (pulled up by TP56217C)
PB_14	18	0x844/008	50	GPIO0_26	gpio[2 6]	mcasp0_trigzone_input		gpmc_a18	gpmc_a19	gpmc_a20	
PB_15	16	0x844/008	51	GPIO0_26	gpio[2 6]	mcasp0_trigzone_input		gpmc_a19	gpmc_a20	gpmc_a21	
PB_16	19	0x844/008	52	EHRPWM1B	gpio[2 6]	mcasp0_trigzone_input		gpmc_a19	gpmc_a20	gpmc_a21	
PB_17	20	0x958/15c	53	GPIO1_29	5	gpio[1 5]	ehrpwm0	mmc1_dat0	mmc1_dat1	mmc1_dat6	
PB_18	18	0x958/15c	54	GPIO1_29	5	gpio[1 5]	ehrpwm0	mmc1_dat0	mmc1_dat1	mmc1_dat6	
PB_19	21	0x958/15c	55	GPIO1_29	5	gpio[1 5]	ehrpwm0	mmc1_dat0	mmc1_dat1	mmc1_dat6	
PB_20	20	0x958/15c	56	GPIO1_29	5	gpio[1 5]	ehrpwm0	mmc1_dat0	mmc1_dat1	mmc1_dat6	
PB_21	23	0x958/15c	57	GPIO1_29	5	gpio[1 5]	ehrpwm0	mmc1_dat0	mmc1_dat1	mmc1_dat6	
PB_22	24	0x958/15c	58	GPIO1_29	5	gpio[1 5]	ehrpwm0	mmc1_dat0	mmc1_dat1	mmc1_dat6	
PB_23	25	0x958/15c	59	GPIO1_29	5	gpio[1 5]	ehrpwm0	mmc1_dat0	mmc1_dat1		

Cape Expansion Headers



P9

DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3
VDD_5V	5	6	VDD_5V
SYS_5V	7	8	SYS_5V
PWR_BUT	9	10	SYS_RESETN
UART4_RXD	11	12	GPIO_60
UART4_TXD	13	14	EHRPWM1A
GPIO_48	15	16	EHRPWM1B
SPI0_CS0	17	18	SPI0_D1
I2C2_SCL	19	20	I2C2_SDA
SPI0_DO	21	22	SPI0_SCLK
GPIO_49	23	24	UART1_TXD
GPIO_117	25	26	UART1_RXD
GPIO_115	27	28	SPI1_CS0
SPI1_DO	29	30	GPIO_112
SPI1_SCLK	31	32	VDD_ADC
AIN4	33	34	GNDA_ADC
AIN6	35	36	AIN5
AIN2	37	38	AIN3
AIN0	39	40	AIN1
GPIO_20	41	42	ECAPPWM0
DGND	43	44	DGND
DGND	45	46	DGND

P8

DGND	1	2	DGND
MMC1_DAT6	3	4	MMC1_DAT7
MMC1_DAT2	5	6	MMC1_DAT3
GPIO_66	7	8	GPIO_67
GPIO_69	9	10	GPIO_68
GPIO_45	11	12	GPIO_44
EHRPWM2B	13	14	GPIO_26
GPIO_47	15	16	GPIO_46
GPIO_27	17	18	GPIO_65
EHRPWM2A	19	20	MMC1_CMD
MMC1_CLK	21	22	MMC1_DAT5
MMC1_DAT4	23	24	MMC1_DAT1
MMC1_DATO	25	26	GPIO_61
LCD_VSYNC	27	28	LCD_PCLK
LCD_HSYNC	29	30	LCD_AC_BIAS
LCD_DATA14	31	32	LCD_DATA15
LCD_DATA13	33	34	LCD_DATA11
LCD_DATA12	35	36	LCD_DATA10
LCD_DATA8	37	38	LCD_DATA9
LCD_DATA6	39	40	LCD_DATA7
LCD_DATA4	41	42	LCD_DATA5
LCD_DATA2	43	44	LCD_DATA3
LCD_DATA0	45	46	LCD_DATA1

LEGEND

- POWER/GROUND/RESET
- AVAILABLE DIGITAL
- AVAILABLE PWM
- SHARED I2C BUS
- RECONFIGURABLE DIGITAL
- ANALOG INPUTS (1.8V)

Each digital I/O pin has 8 different modes that can be selected, including GPIO.

65 possible digital I/Os

P9				P8			
DGND	1	2	DGND	DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3	GPIO_38	3	4	GPIO_39
VDD_5V	5	6	VDD_5V	GPIO_34	5	6	GPIO_35
SYS_5V	7	8	SYS_5V	GPIO_66	7	8	GPIO_67
PWR_BUT	9	10	SYS_RESETN	GPIO_69	9	10	GPIO_68
GPIO_30	11	12	GPIO_60	GPIO_45	11	12	GPIO_44
GPIO_31	13	14	GPIO_50	GPIO_23	13	14	GPIO_26
GPIO_48	15	16	GPIO_51	GPIO_47	15	16	GPIO_46
GPIO_5	17	18	GPIO_4	GPIO_27	17	18	GPIO_65
I2C2_SCL	19	20	I2C2_SDA	GPIO_22	19	20	GPIO_63
GPIO_3	21	22	GPIO_2	GPIO_62	21	22	GPIO_37
GPIO_49	23	24	GPIO_15	GPIO_36	23	24	GPIO_33
GPIO_117	25	26	GPIO_14	GPIO_32	25	26	GPIO_61
GPIO_115	27	28	GPIO_113	GPIO_86	27	28	GPIO_88
GPIO_111	29	30	GPIO_112	GPIO_87	29	30	GPIO_89
GPIO_110	31	32	VDD_ADC	GPIO_10	31	32	GPIO_11
AIN4	33	34	GNDA_ADC	GPIO_9	33	34	GPIO_81
AIN6	35	36	AIN5	GPIO_8	35	36	GPIO_80
AIN2	37	38	AIN3	GPIO_78	37	38	GPIO_79
AIN0	39	40	AIN1	GPIO_76	39	40	GPIO_77
GPIO_20	41	42	GPIO_7	GPIO_74	41	42	GPIO_75
DGND	43	44	DGND	GPIO_72	43	44	GPIO_73
DGND	45	46	DGND	GPIO_70	45	46	GPIO_71

In GPIO mode, each digital I/O can produce interrupts.

8 PWMs and 4 timers

P9				P8			
DGND	1	2	DGND	DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3	GPIO_38	3	4	GPIO_39
VDD_5V	5	6	VDD_5V	GPIO_34	5	6	GPIO_35
SYS_5V	7	8	SYS_5V	TIMER4	7	8	TIMER7
PWR_BUT	9	10	SYS_RESETN	TIMER5	9	10	TIMER6
GPIO_30	11	12	GPIO_60	GPIO_45	11	12	GPIO_44
GPIO_31	13	14	EHRPWM1A	EHRPWM2B	13	14	GPIO_26
GPIO_48	15	16	EHRPWM1B	GPIO_47	15	16	GPIO_46
GPIO_5	17	18	GPIO_4	GPIO_27	17	18	GPIO_65
I2C2_SCL	19	20	I2C2_SDA	EHRPWM2A	19	20	GPIO_63
EHRPWMOB	21	22	EHRPMOA	GPIO_62	21	22	GPIO_37
GPIO_49	23	24	GPIO_15	GPIO_36	23	24	GPIO_33
GPIO_117	25	26	GPIO_14	GPIO_32	25	26	GPIO_61
GPIO_115	27	28	ECAPPWM2	GPIO_86	27	28	GPIO_88
EHRPWMOB	29	30	GPIO_112	GPIO_87	29	30	GPIO_89
EHRPMOA	31	32	VDD_ADC	GPIO_10	31	32	GPIO_11
AIN4	33	34	GNDA_ADC	GPIO_9	33	34	EHRPWM1B
AIN6	35	36	AIN5	GPIO_8	35	36	EHRPWM1A
AIN2	37	38	AIN3	GPIO_78	37	38	GPIO_79
AIN0	39	40	AIN1	GPIO_76	39	40	GPIO_77
GPIO_20	41	42	ECAPPWMO	GPIO_74	41	42	GPIO_75
DGND	43	44	DGND	GPIO_72	43	44	GPIO_73
DGND	45	46	DGND	EHRPWM2A	45	46	EHRPWM2B

Up to 8 digital I/O pins can be configured with pulse-width modulators (PWM) to produce signals to control motors or create analog voltage levels, without taking up any extra CPU cycles.

7 analog inputs (1.8V)

P9				P8			
DGND	1	2	DGND	DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3	GPIO_38	3	4	GPIO_39
VDD_5V	5	6	VDD_5V	GPIO_34	5	6	GPIO_35
SYS_5V	7	8	SYS_5V	GPIO_66	7	8	GPIO_67
PWR_BUT	9	10	SYS_RESETN	GPIO_69	9	10	GPIO_68
GPIO_30	11	12	GPIO_60	GPIO_45	11	12	GPIO_44
GPIO_31	13	14	GPIO_50	GPIO_23	13	14	GPIO_26
GPIO_48	15	16	GPIO_51	GPIO_47	15	16	GPIO_46
GPIO_5	17	18	GPIO_4	GPIO_27	17	18	GPIO_65
I2C2_SCL	19	20	I2C2_SDA	GPIO_22	19	20	GPIO_63
GPIO_3	21	22	GPIO_2	GPIO_62	21	22	GPIO_37
GPIO_49	23	24	GPIO_15	GPIO_36	23	24	GPIO_33
GPIO_117	25	26	GPIO_14	GPIO_32	25	26	GPIO_61
GPIO_115	27	28	GPIO_113	GPIO_86	27	28	GPIO_88
GPIO_111	29	30	GPIO_112	GPIO_87	29	30	GPIO_89
GPIO_110	31	32	VDD_ADC	GPIO_10	31	32	GPIO_11
AIN4	33	34	GNDA_ADC	GPIO_9	33	34	GPIO_81
AIN6	35	36	AIN5	GPIO_8	35	36	GPIO_80
AIN2	37	38	AIN3	GPIO_78	37	38	GPIO_79
AIN0	39	40	AIN1	GPIO_76	39	40	GPIO_77
GPIO_20	41	42	GPIO_7	GPIO_74	41	42	GPIO_75
DGND	43	44	DGND	GPIO_72	43	44	GPIO_73
DGND	45	46	DGND	GPIO_70	45	46	GPIO_71

Make sure you don't input more than 1.8V to the analog input pins.

This is a single 12-bit analog-to-digital converter with 8 channels, 7 of which are made available on the headers.

2 I2C ports					
P9			P8		
DGND	1	2	DGND		
VDD_3V3	3	4	VDD_3V3		
VDD_5V	5	6	VDD_5V		
SYS_5V	7	8	SYS_5V		
PWR_BUT	9	10	SYS_RESETN		
GPIO_30	11	12	GPIO_60		
GPIO_31	13	14	GPIO_50		
GPIO_48	15	16	GPIO_51		
I2C1_SCL	17	18	I2C1_SDA		
I2C2_SCL	19	20	I2C2_SDA		
I2C2_SCL	21	22	I2C2_SDA		
GPIO_49	23	24	I2C1_SCL		
GPIO_117	25	26	I2C1_SDA		
GPIO_115	27	28	GPIO_113		
GPIO_111	29	30	GPIO_112		
GPIO_110	31	32	VDD_ADC		
AIN4	33	34	GND_ADC		
AIN6	35	36	AIN5		
AIN2	37	38	AIN3		
AIN0	39	40	AIN1		
GPIO_20	41	42	GPIO_7		
DGND	43	44	DGND		
DGND	45	46	DGND		

The first I2C bus is utilized for reading EEPROMS on cape add-on boards and can't be used for other digital I/O operations without interfering with that function, but you can still use it to add other I2C devices at available addresses.

The second I2C bus is available for you to configure and use.

2 SPI ports

P9		P8	
DGND	1	2	DGND
VDD_3V3	3	4	VDD_3V3
VDD_5V	5	6	VDD_5V
SYS_5V	7	8	SYS_5V
PWR_BUT	9	10	SYS_RESETN
GPIO_30	11	12	GPIO_60
GPIO_31	13	14	GPIO_50
GPIO_48	15	16	GPIO_51
SPI0_CS0	17	18	SPI0_D1
SPI1_CS1	19	20	SPI1_CS0
SPI0_DO	21	22	SPI0_SCLK
GPIO_49	23	24	GPIO_15
GPIO_117	25	26	GPIO_14
GPIO_115	27	28	SPI1_CS0
SPI1_DO	29	30	SPI1_D1
SPI1_SCLK	31	32	VDD_ADC
AIN4	33	34	GNDA_ADC
AIN6	35	36	AIN5
AIN2	37	38	AIN3
AIN0	39	40	AIN1
GPIO_20	41	42	SPI1_CS1
DGND	43	44	DGND
DGND	45	46	DGND

For shifting out data fast, you might consider using one of the SPI ports.

BeagleBoard.org - bone101

version 1.1
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vi / vim graphical cheat sheet

Esc normal mode																
~ toggle case `* goto mark	! external filter	@ play macro	# prev ident	\$ col	% goto match	^ "soft" bol	& repeat :s	* next ident	(begin sentence) end sentence	"soft" bol	_ down	+ next line			
1	2	3	4	5	6	7	8	9	0 "hard" bol	- prev line	= autoformat					
Q ex mode q record macro	W next WORD w next word	E end WORD e end word	R replace mode r replace char	T back 'till t 'till	Y yank line y yank	U undo line u undo	I insert at bol i insert mode	O open above o open below	P paste before p paste after	{ begin parag. } end parag.						
A append at eol a append	S subst line s subst char	D delete to eol d delete	F "back" find ch f find char	G eof/ln g extra cmd	H screen top h <	J join lines j ↓	K help k ↑	L screen bottom l →	: ex cmd line : repeat ; t/f/F	!! reg. spec ! . goto mk. bol	bol/ goto col \\ not used!					
Z quit ^Z Z extra ^X Z cmdns	X back-space X delete char	C change to eol C change	V visual lines V visual mode	B prev WORD b prev word	N prev (find) n next (find)	M screen mid'l m set mark	< un- ³ , reverse , t/f/F	> indent ³ + repeat cmd	? find (rev.) / find							
motion moves the cursor, or defines the range for an operator	Main command line commands ('ex'):															
command direct action command, if red, it enters insert mode	:w (save), :q (quit), :q! (quit w/o saving) :e f (open file f), .‰s/x/y/g (replace 'x' by 'y' filewide), .h (help in vim), :new (new file in vim),															
operator requires a motion afterwards, operates between cursor & destination	Other important commands: CTRL-R: redo (vim), CTRL-F/-B: page up/down, CTRL-E/-Y: scroll line up/down, CTRL-V: block-visual mode (vim only)															
extra special functions, requires extra input	Visual mode: Move around and type operator to act on selected region (vim only)															
Q commands with a dot need a char argument afterwards	Notes:															
bol = beginning of line, eol = end of line, mk = mark, yank = copy	(1) use "x before a yank/paste/del command to use that register ('clipboard') (x=a..z,*) (e.g.: "ay\$ to copy rest of line to reg 'a')															
words: <code>quux(foo, bar, baz);</code>	(2) type in a number before any action to repeat it that number of times (e.g.: 2p, d2w, 5i, d4j)															
WORDS: <code>quux(foo, bar, baz);</code>	(3) duplicate operator to act on current line (dd = delete line, >> = indent line)															
	(4) ZZ to save & quit, ZQ to quit w/o saving															
	(5) zt: scroll cursor to top, zb: bottom, zz: center															
	(6) gg: top of file (vim only), gf: open file under cursor (vim only)															

For a graphical vi/vim tutorial & more tips, go to www.viemu.com - home of ViEmu, vi/vim emulation for Microsoft Visual Studio

Vim(Terminal Text Editor) Cheat Sheet

- Flashing

[Updating the software image on a BeagleBone Black - Zageo LLC - Medium](#)

[Forum | Factory defaults for a Beaglebone Black - GHI Electronics](#)

- Expanding Storage

[Beagleboard:Expanding File System Partition On A microSD - eLinux.org](#)

[Extending Micro SD Card space for Beaglebone Bl... | element14 | BeagleBoard](#)

[How to resize Debian 9.2 root partition to fit SD card - SomLabs Wiki](#)

[Noob's guide to Embedded Systems: How to Increase Memory of Beaglebone Black](#)

- Running program on boot

[Booting Beaglebone Black with Autorun Python Script](#)

[BeagleBone Black Launch Python Script at Boot like Arduino Sketch | Billwaa's Blog](#)