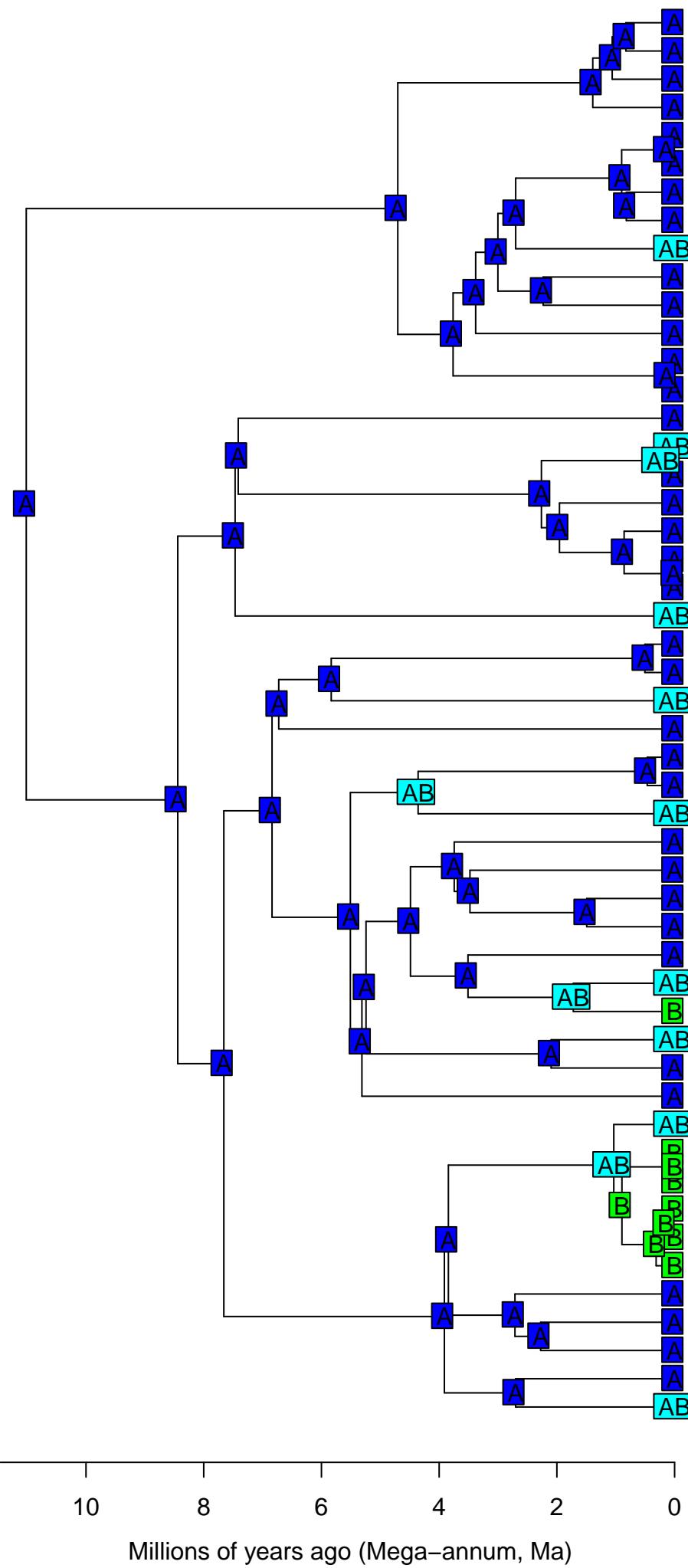
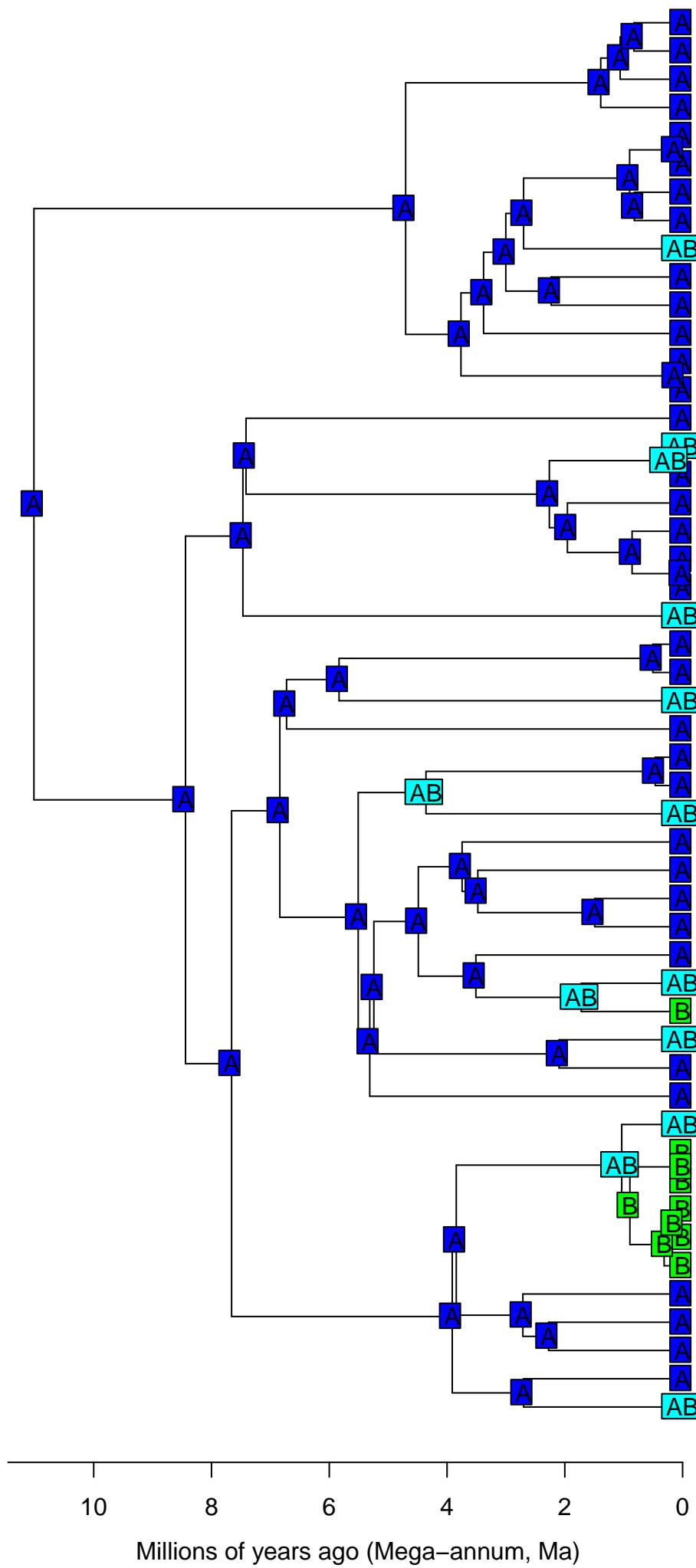


# Plotting simtree & states from simulate\_tdsse2()



**simulate\_tdsse2() tree after reordering attempt #2  
to default APE format -- CORRECT!**



**simulate\_tdsse2() tree after reordering attempt #1  
to default APE format**

