Sheet: Video Interface and VGA Output

File: VideoInterfaceAndVGA.sch

Sheet: FPGA and CPLD Module

File: FPGACPLD.sch

Sheet: Power Supply

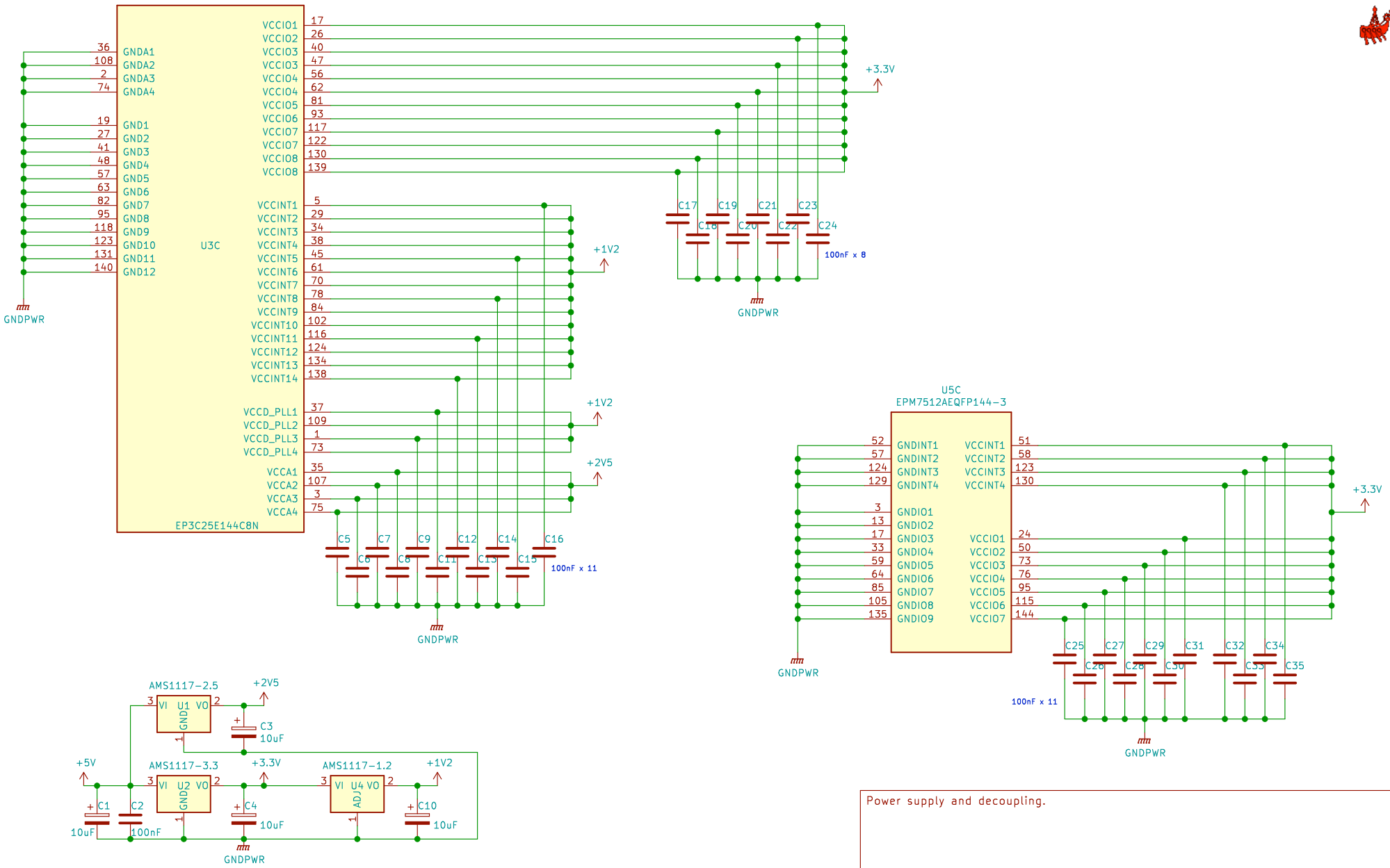
File: PowerSupply.sch

An add on board, located as an extender in sockets of IC20, 15 and 8 along with the colour board connector CN1 extends the MZ-80A specification to allow 40/80 column colour output and MZ80B graphics capability.

Sheet: /
File: MZ80-80CLR.sch

Title: Sharp MZ-80A Video Module

Size: A4	Date: 2020-07-07	**Rev: 2.0a**
KiCad E.D.A. kicad (5.1.2-1)-1		Id: 1/4

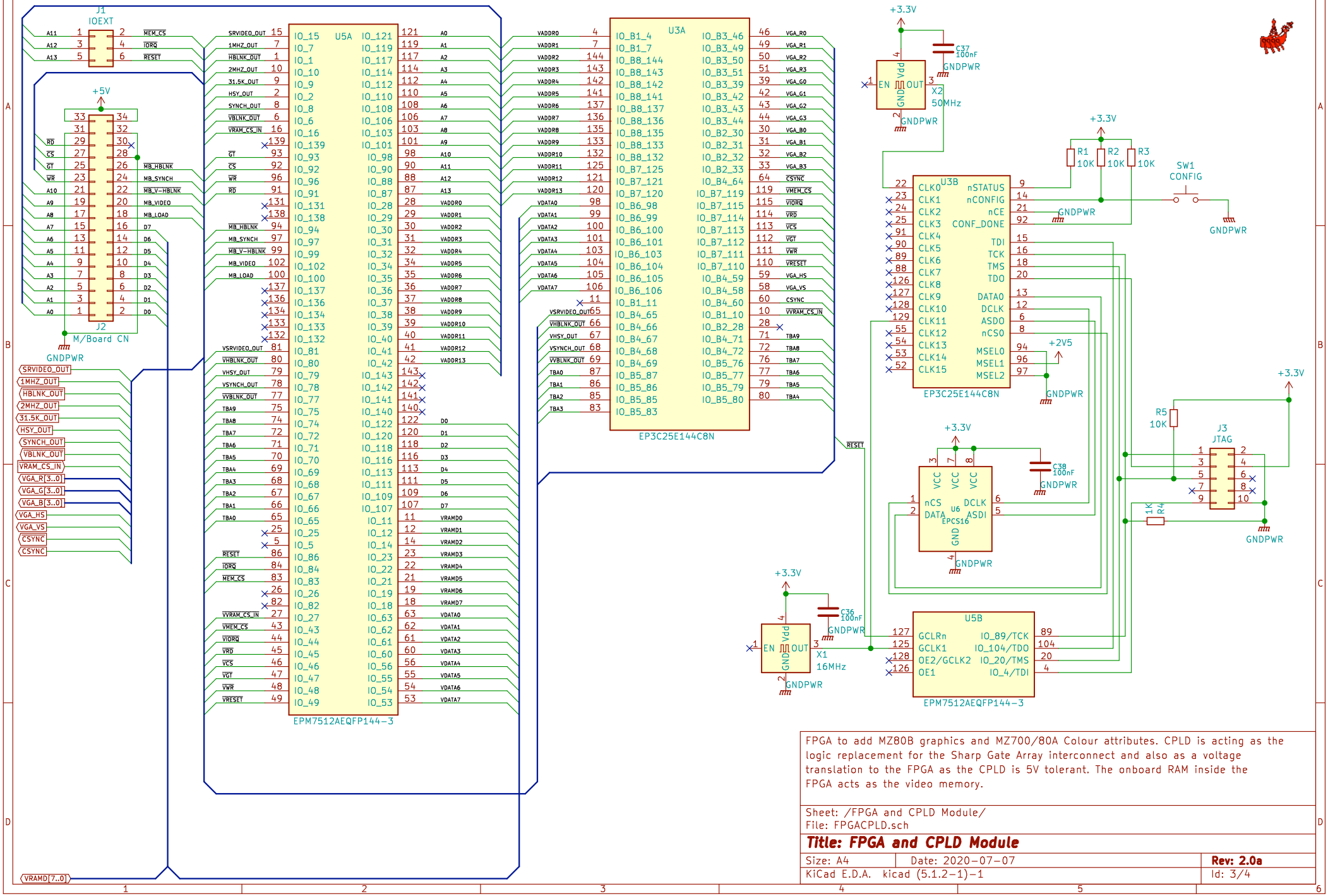


Power supply and decoupling.

Sheet: /Power Supply/
File: PowerSupply.sch

Title: Power Supply

Size: A4	Date: 2020-07-07	**Rev: 2.0a**
KiCad E.D.A. kicad (5.1.2-1)-1		Id: 2/4

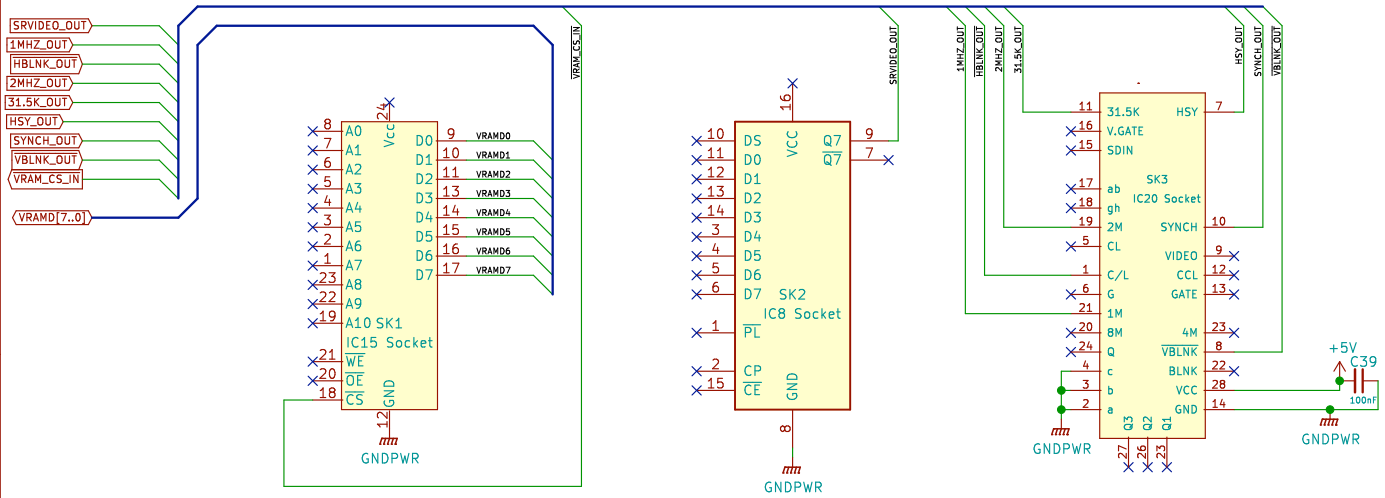


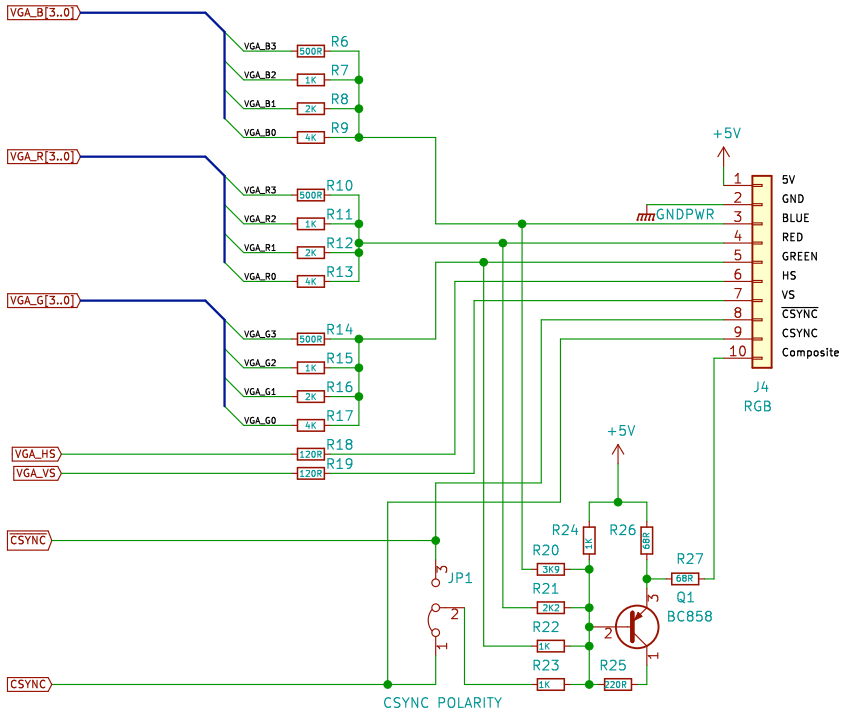
FPGA to add MZ80B graphics and MZ700/80A Colour attributes. CPLD is acting as the logic replacement for the Sharp Gate Array interconnect and also as a voltage translation to the FPGA as the CPLD is 5V tolerant. The onboard RAM inside the FPGA acts as the video memory.

Sheet: /FPGA and CPLD Module/
File: FPGACPLD.sch

Title: FPGA and CPLD Module

Size: A4	Date: 2020-07-07	**Rev: 2.0a**
KiCad E.D.A. kicad (5.1.2-1)-1		Id: 3/4





Adding CPLD and FPGA functionality, the main circuitry can be moved off the main board, removing the CGROM, VRAM and Gate Arrays, all now situated in the FPGA/CPLD.

Sheet: /Video Interface and VGA Output/
File: VideoInterfaceAndVGA.sch

Title: MZ-80A Video Interface and VGA Output

Size: A4	Date: 2020-07-07	**Rev: 2.0a**
KiCad E.D.A. kicad (5.1.2-1)-1		Id: 4/4