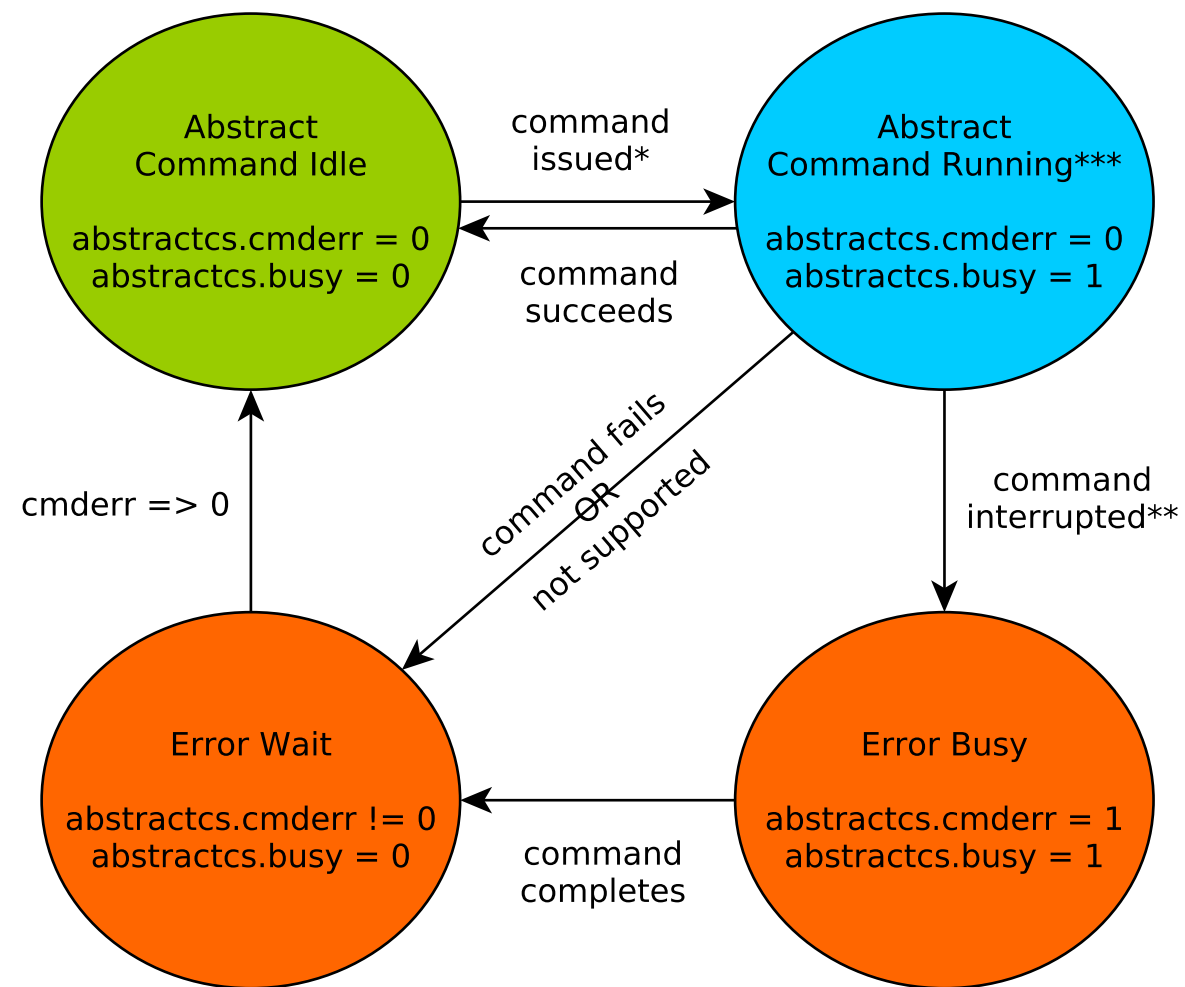
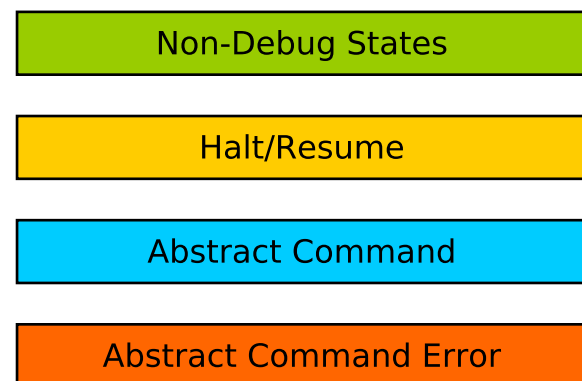


#### Legend



\*) Commands are issued by writing to command, or by accessing dataN when autoexecdataN is set, or by accessing progbufN when autoexecprogbufN is set.

\*\*) Commands are interrupted when command, abstractcs, or abstractauto are written, or when data or progbuf registers are accessed, while abstractcs.busy=1.

\*\*\*) Quick Access Commands are not shown here in detail. They offer an alternate way to quickly go through the Halting/Halted/Halted Abstract Command/Halted/Resuming states.

This diagram shows states involved while debugging normally, but is not exhaustive.