

# 512-bit

In computer architecture, **512-bit** integers, memory addresses or other data units are those that are 512 bits wide. Also, 512-bit CPU and ALU architectures are those that are based on registers, address buses, or data buses of that size.

There are currently no mainstream general-purpose processors built to operate on 512-bit integers or addresses, though a number of processors do operate on 512-bit data. As of 2013, the Intel Xeon Phi has a vector processing unit with 512-bit vector registers, each one holding sixteen 32-bit elements or eight 64-bit elements, and a single instruction can operate on all these values in parallel. However, the Xeon Phi's vector processing unit does not operate on individual numbers that are 512bits in length.<sup>[1]</sup>

## Uses

- Some GPUs such as the Nvidia GTX280,<sup>[2]</sup> GTX285,<sup>[3]</sup> Quadro FX 5800<sup>[4]</sup> and several Tesla products move data across a 512-bit memory bus. Then AMD Radeon R9 290, R9 290X and 295X2 followed.
- Many hash functions, such as SHA-512, have a 512-bit output.
- AVX-512 are 512-bit extensions to the 256-bit Advanced Vector Extensions SIMD instructions for x86instruction set architecture proposed by Intel in July 2013, and released on 2016 with Knights Landing, and in 2018 on the HEDT and consumer server platform, with Skylake-X and Skylake-SP respectively



The AMD Radeon R9 290X (Sapphire OEM version pictured here) uses a 512 bit memory bus

## References

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- "NVIDIA® Quadro® FX 5800 provides professionals with visual supercomputing from their desktops delivering results that push visualization beyond traditional 3D"([http://www.nvidia.com/object/product\\_quadro\\_fx\\_5800\\_us.html](http://www.nvidia.com/object/product_quadro_fx_5800_us.html)). Nvidia.com. Retrieved 2013-08-13.

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