

# Module Guide for ANN (Artificial Neural Newcounter)

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# 1 Revision History

Date	Version	Notes
March 15	1.0	Initial Draft

## 2 Reference Material

This section records information for easy reference.

### 2.1 Abbreviations and Acronyms

symbol	description
AC	Anticipated Change
DAG	Directed Acyclic Graph
M	Module
MG	Module Guide
OS	Operating System
R	Requirement
SC	Scientific Computing
SRS	Software Requirements Specification
ProgName	Explanation of program name
UC	Unlikely Change
<a href="#">[etc. —SS]</a>	<a href="#">[... —SS]</a>

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### 3 Introduction

Decomposing a system into modules is a commonly accepted approach to developing software. A module is a work assignment for a programmer or programming team (Parnas et al., 1984). We advocate a decomposition based on the principle of information hiding (Parnas, 1972). This principle supports design for change, because the “secrets” that each module hides represent likely future changes. Design for change is valuable in SC, where modifications are frequent, especially during initial development as the solution space is explored.

Our design follows the rules laid out by Parnas et al. (1984), as follows:

- System details that are likely to change independently should be the secrets of separate modules.
- Each data structure is implemented in only one module.
- Any other program that requires information stored in a module’s data structures must obtain it by calling access programs belonging to that module.

After completing the first stage of the design, the Software Requirements Specification (SRS), the Module Guide (MG) is developed (Parnas et al., 1984). The MG specifies the modular structure of the system and is intended to allow both designers and maintainers to easily identify the parts of the software. The potential readers of this document are as follows:

- New project members: This document can be a guide for a new project member to easily understand the overall structure and quickly find the relevant modules they are searching for.
- Maintainers: The hierarchical structure of the module guide improves the maintainers’ understanding when they need to make changes to the system. It is important for a maintainer to update the relevant sections of the document after changes have been made.
- Designers: Once the module guide has been written, it can be used to check for consistency, feasibility, and flexibility. Designers can verify the system in various ways, such as consistency among modules, feasibility of the decomposition, and flexibility of the design.

The rest of the document is organized as follows. Section 4 lists the anticipated and unlikely changes of the software requirements. Section 5 summarizes the module decomposition that was constructed according to the likely changes. Section 6 specifies the connections between the software requirements and the modules. Section 7 gives a detailed description of the modules. Section 8 includes two traceability matrices. One checks the completeness of the design against the requirements provided in the SRS. The other shows the relation between anticipated changes and the modules. Section 9 describes the use relation between modules.

## 4 Anticipated and Unlikely Changes

This section lists possible changes to the system. According to the likeliness of the change, the possible changes are classified into two categories. Anticipated changes are listed in Section 4.1, and unlikely changes are listed in Section 4.2.

### 4.1 Anticipated Changes

Anticipated changes are the source of the information that is to be hidden inside the modules. Ideally, changing one of the anticipated changes will only require changing the one module that hides the associated decision. The approach adapted here is called design for change.

**AC1:** The specific hardware on which the software is running.

**AC2:** The format of the initial input data.

**AC3:** Evolution of methods for processing and analyzing input images to extract features.

**AC4:** Modifications in neural network training algorithms and parameter adjustments.

**AC5:** Updates in the scheduling and methodology for model training and updating.

**AC6:** Evolution in the format and structure of the output data, including classification results, by developing GUI.

**AC7:** Changes in the algorithms and techniques used for image classification.

### 4.2 Unlikely Changes

The module design should be as general as possible. However, a general system is more complex. Sometimes this complexity is not necessary. Fixing some design decisions at the system architecture stage can simplify the software design. If these decision should later need to be changed, then many parts of the design will potentially need to be modified. Hence, it is not intended that these decisions will be changed.

**UC1:** Input/Output devices (Input: File and/or Keyboard, Output: File, Memory, and/or Screen).

**UC2:** The source of input is always external to the software.

**UC3:** The classification results (outputs) are always displayed on the output software.

**UC4:** The nature of outputs as image classifications is a fixed aspect of the system.

## 5 Module Hierarchy

This section provides an overview of the module design. Modules are summarized in a hierarchy decomposed by secrets in Table 1. The modules listed below, which are leaves in the hierarchy tree, are the modules that will actually be implemented.

**M1:** Hardware-Hiding Module

**M2:** ANN Control Module

**M3:** Output Module

**M4:** Saved ANN Model Module

**M5:** Input Classifier Module

**M6:** Input Preparing and Preprocessing Module

**M7:** Setting Input Image Module

**M8:** Training Module

**M9:** Training Model Module

**M10:** Training Data Preparing and Preprocessing Module

**M11:** Testing Module

**M12:** Plotting Module

**M13:** Setting Feedback Module

## 6 Connection Between Requirements and Design

The design of the system is intended to satisfy the requirements developed in the SRS [Djavaherpour \(2024\)](#). In this stage, the system is decomposed into modules. The connection between requirements and modules is listed in Table 2.

## 7 Module Decomposition

Modules are decomposed according to the principle of “information hiding” proposed by [Parnas et al. \(1984\)](#). The *Secrets* field in a module decomposition is a brief statement of the design decision hidden by the module. The *Services* field specifies *what* the module will do without documenting *how* to do it. For each module, a suggestion for the implementing software is given under the *Implemented By* title. If the entry is *OS*, this means that the



Level 1	Level 2
Hardware-Hiding Module	
	ANN Control Module
	Plotting Module
	Saved ANN Model Module
Behaviour-Hiding Module	Setting Input Image Module
	Testing Module
	Training Data Preparing and Preprocessing Module
	Training Model Module
	Training Module
Software Decision Module	Input Classifier Module
	Input Preparing and Preprocedding Module
	Output Module
	Setting Feedback Module

Table 1: Module Hierarchy

module is provided by the operating system or by standard programming language libraries. *ProgName* means the module will be implemented by the ProgName software.

Only the leaf modules in the hierarchy have to be implemented. If a dash (-) is shown, this means that the module is not a leaf and will not have to be implemented.

## 7.1 Hardware Hiding Modules (M1)

**Secrets:** The data structure and algorithm used to implement the virtual hardware.

**Services:** Serves as a virtual hardware used by the rest of the system. This module provides the interface between the hardware and the software. So, the system can use it to display outputs or to accept inputs.

**Implemented By:** OS

## 7.2 Behaviour-Hiding Module

**Secrets:** The contents of the required behaviours.

**Services:** Includes programs that provide externally visible behaviour of the system as specified in the software requirements specification (SRS) documents [Djavaherpour \(2024\)](#). This module serves as a communication layer between the hardware-hiding

module and the software decision module. The programs in this module will need to change if there are changes in the SRS [Djavaherpour \(2024\)](#).

**Implemented By:** –

#### **7.2.1 ANN Control Module (M2)**

**Secrets:** The algorithm for coordinating the running of the program.

**Services:** Provides the main program.

**Implemented By:** ANN

**Type of Module:** Abstract Data Type

#### **7.2.2 Output Module (M3)**

**Secrets:** The format and structure of the output data.

**Services:** Outputs the class of the uploaded image.

**Implemented By:** ANN

**Type of Module:** Abstract Object

#### **7.2.3 Output Module (M4)**

**Secrets:** The weights and biases that make up the ANN classification model for th 10 classes of CIFAR-10 dataset [Krizhevsky \(2009\)](#).

**Services:** Saves the data of the highest performance model trained by ANN in a .txt file.

**Implemented By:** ANN

**Type of Module:** Record

#### **7.2.4 Input Classifier Module(M5)**

**Secrets:** The algorithm for classification given the input image and using the saved ANN model.

**Services:** Predicts the class of the uploaded image by restoring the model using saved weights and biases in [M4](#).

**Implemented By:** ANN

**Type of Module:** Abstract Data Type

#### 7.2.5 Etc.

### 7.3 Software Decision Module

**Secrets:** The design decision based on mathematical theorems, physical facts, or programming considerations. The secrets of this module are *not* described in the SRS.

**Services:** Includes data structure and algorithms used in the system that do not provide direct interaction with the user.

**Implemented By:** –

#### 7.3.1 Etc.

## 8 Traceability Matrix

This section shows two traceability matrices: between the modules and the requirements and between the modules and the anticipated changes.

Req.	Modules
R1	M1, M2, M6, M7
R2	M2, M5, M6
R3	M2, M5, M9
R4	M2, M3, M13
R5	M2, M3, M5

Table 2: Trace Between Requirements and Modules

AC	Modules
AC1	M1
AC2	M7
AC3	M6
AC4	M9
AC5	M8
AC6	M3
AC7	M5

Table 3: Trace Between Anticipated Changes and Modules

## 9 Use Hierarchy Between Modules

In this section, the uses hierarchy between modules is provided. Parnas (1978) said of two programs A and B that A *uses* B if correct execution of B may be necessary for A to complete the task described in its specification. That is, A *uses* B if there exist situations in which the correct functioning of A depends upon the availability of a correct implementation of B. Figure 1 illustrates the use relation between the modules. It can be seen that the graph is a directed acyclic graph (DAG). Each level of the hierarchy offers a testable and usable subset of the system, and modules in the higher level of the hierarchy are essentially simpler because they use modules from the lower levels.

Figure 1: Use hierarchy among modules

## 10 User Interfaces

[Design of user interface for software and hardware. Attach an appendix if needed. Drawings, Sketches, Figma —SS]

## 11 Design of Communication Protocols

[If appropriate —SS]

## 12 Timeline

[Schedule of tasks and who is responsible —SS]

[You can point to GitHub if this information is included there —SS]

## References

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