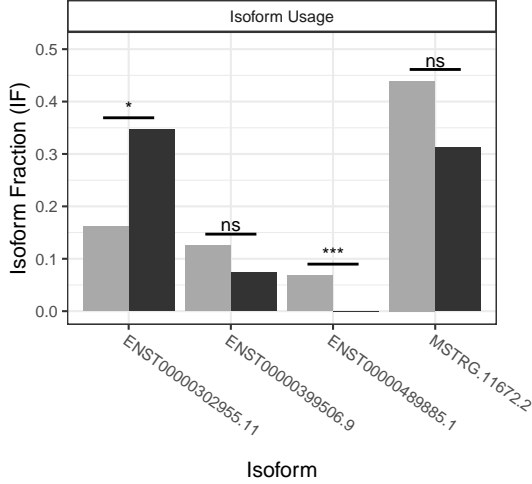
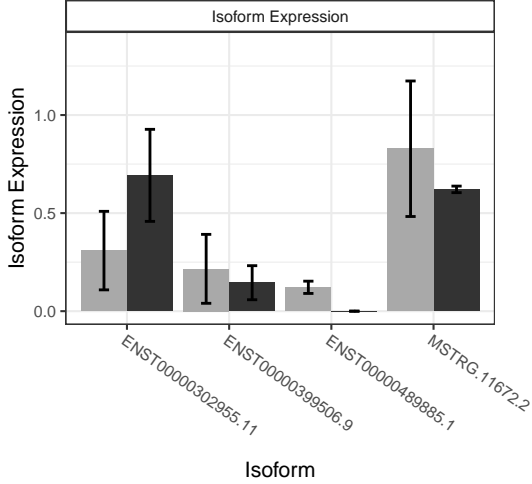
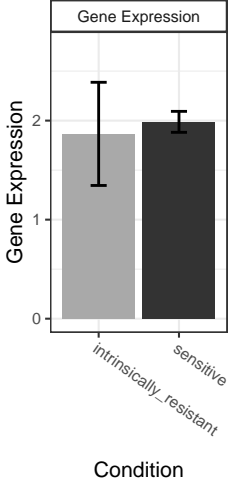
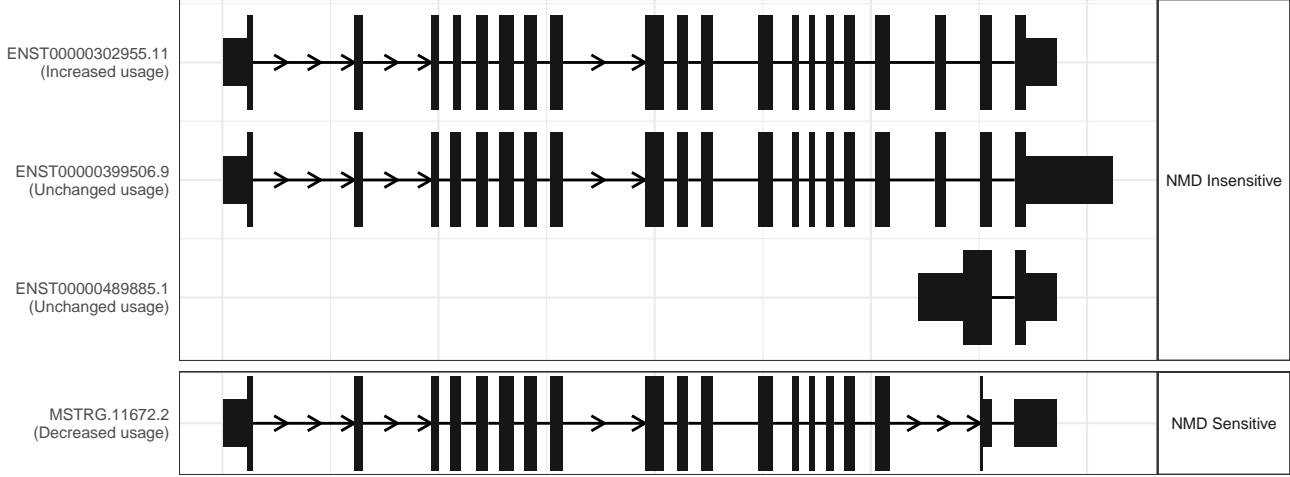


The isoform switch in DLG4 (intrinsically_resistant vs sensitive)



Condition

- intrinsically_resistant
- sensitive