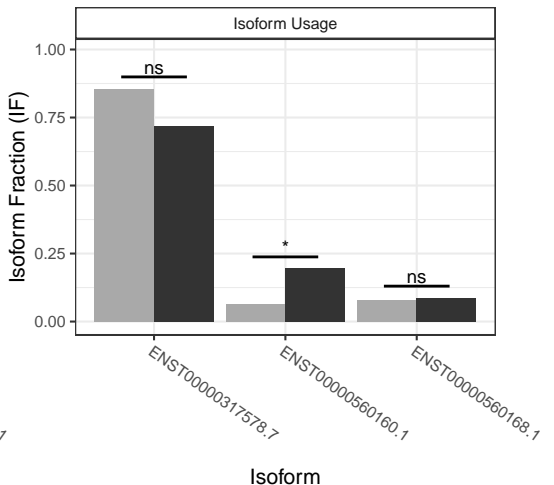
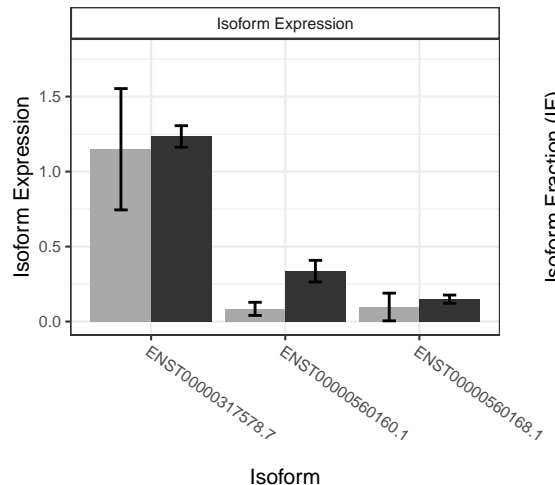
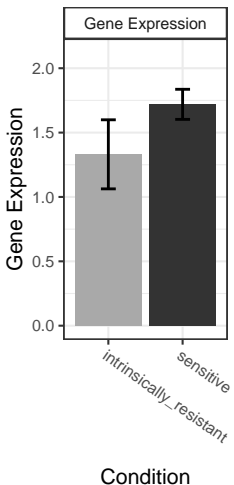
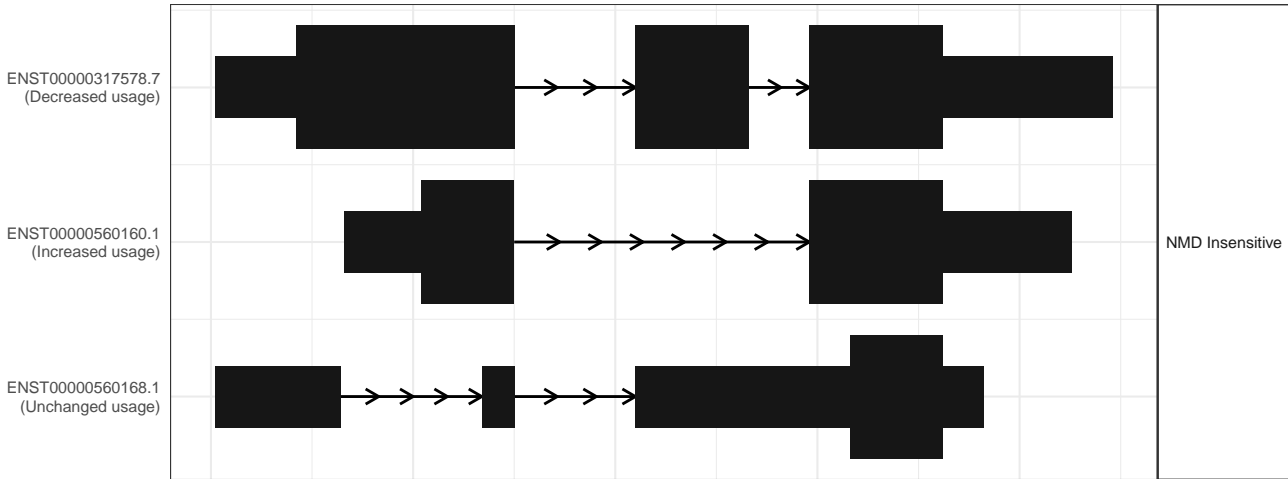


The isoform switch in SIX5 (intrinsically_resistant vs sensitive)



Condition

intrinsically_resistant

sensitive