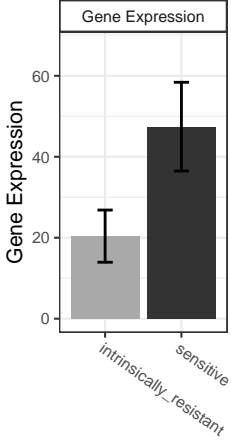
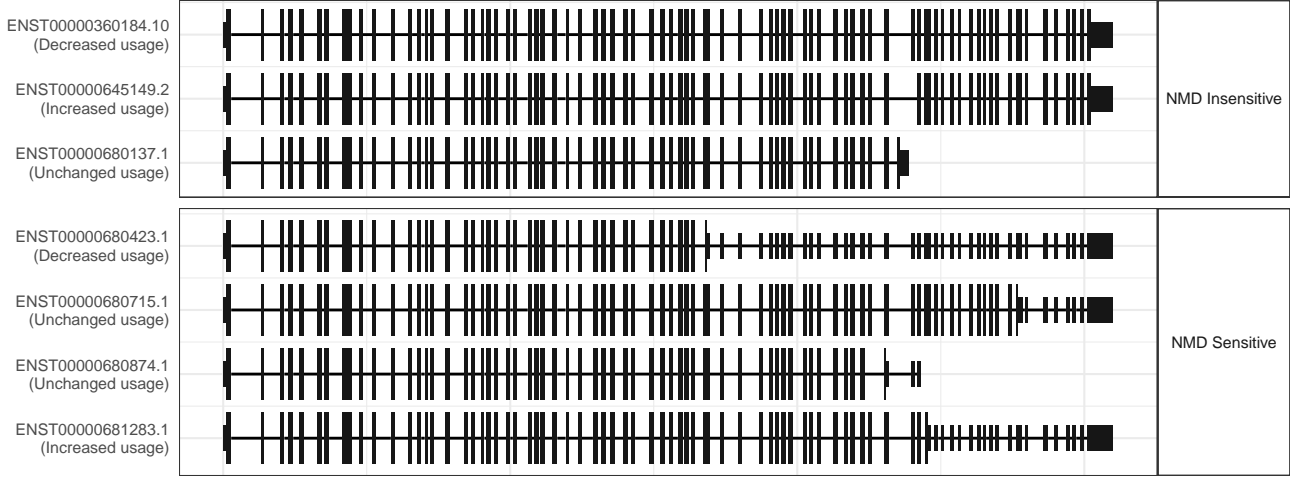
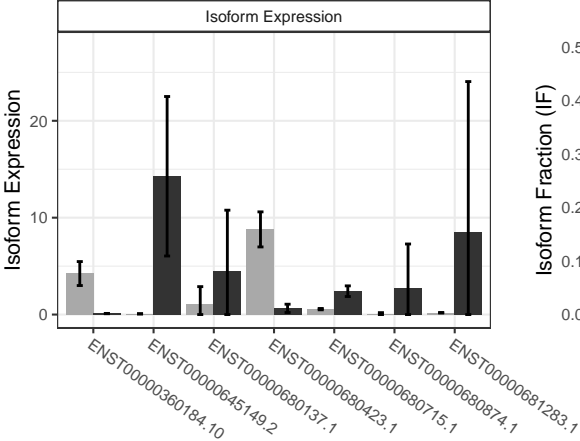


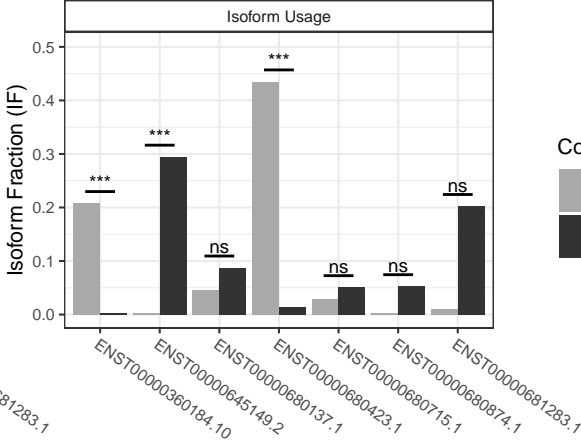
The isoform switch in DYNC1H1 (intrinsically_resistant vs sensitive)



Condition



Isoform



Isoform

Condition

- intrinsically_resistant
- sensitive