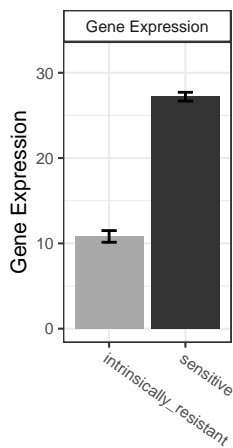
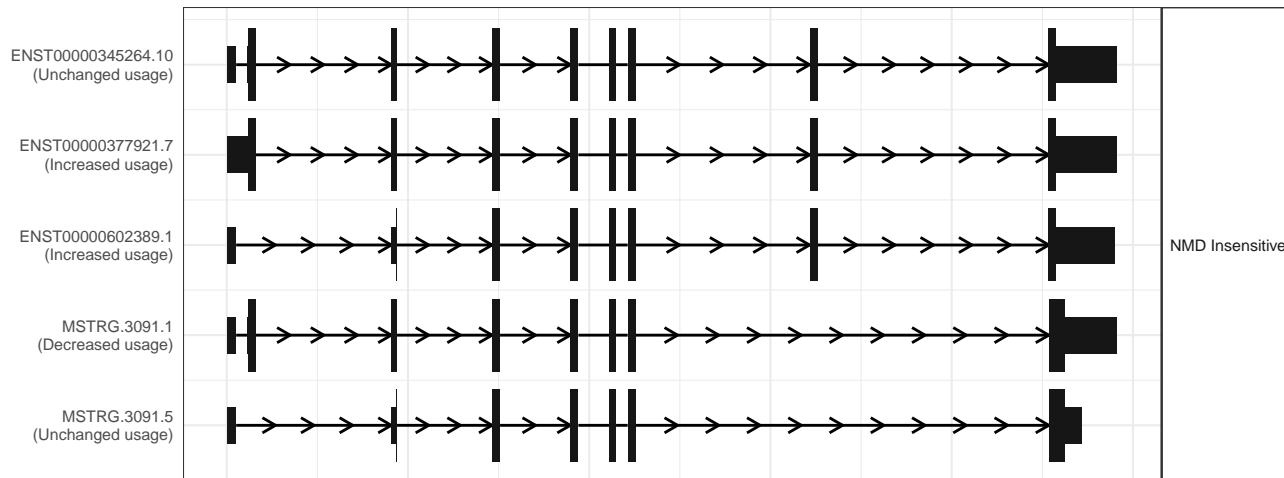
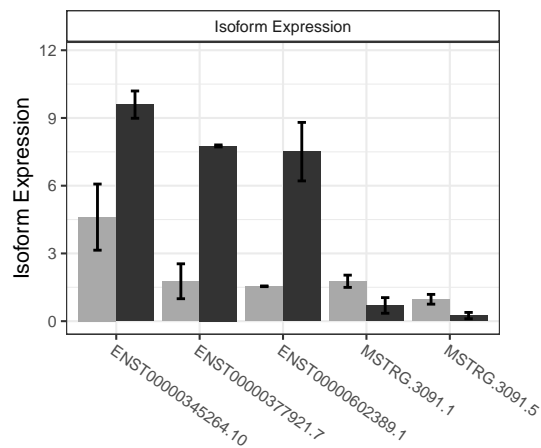


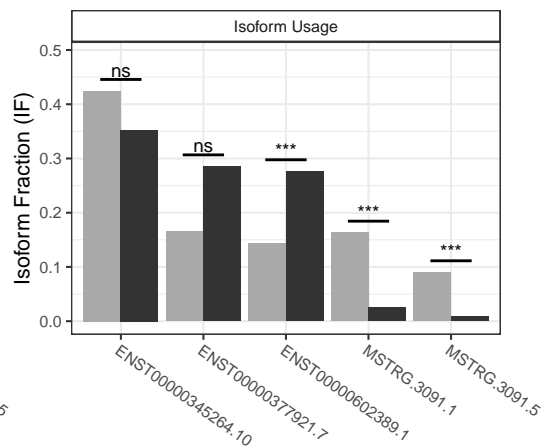
# The isoform switch in RSU1 (intrinsically\_resistant vs sensitive)



Condition



Isoform



Isoform

Condition

