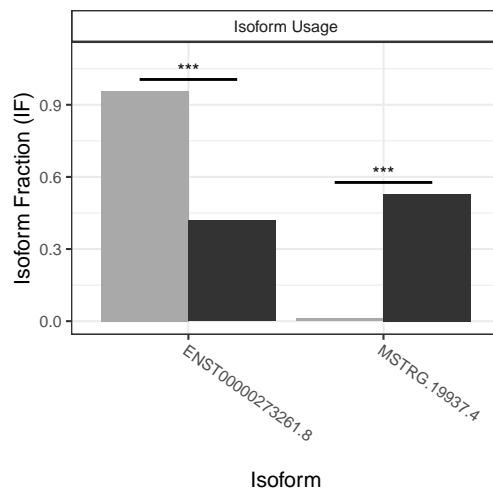
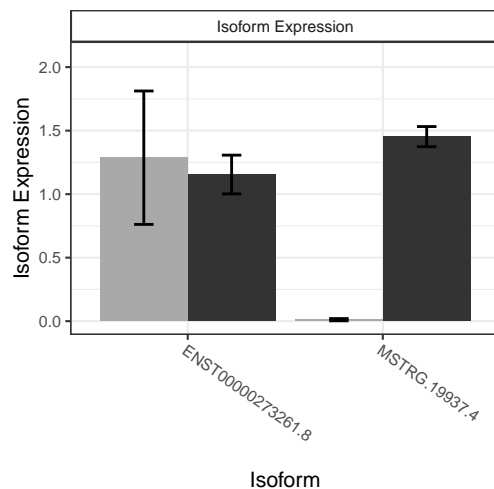
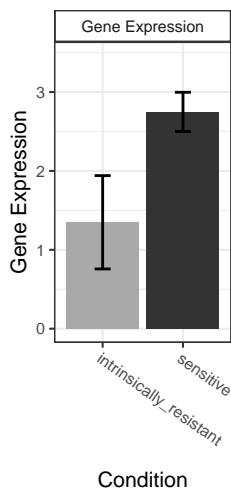
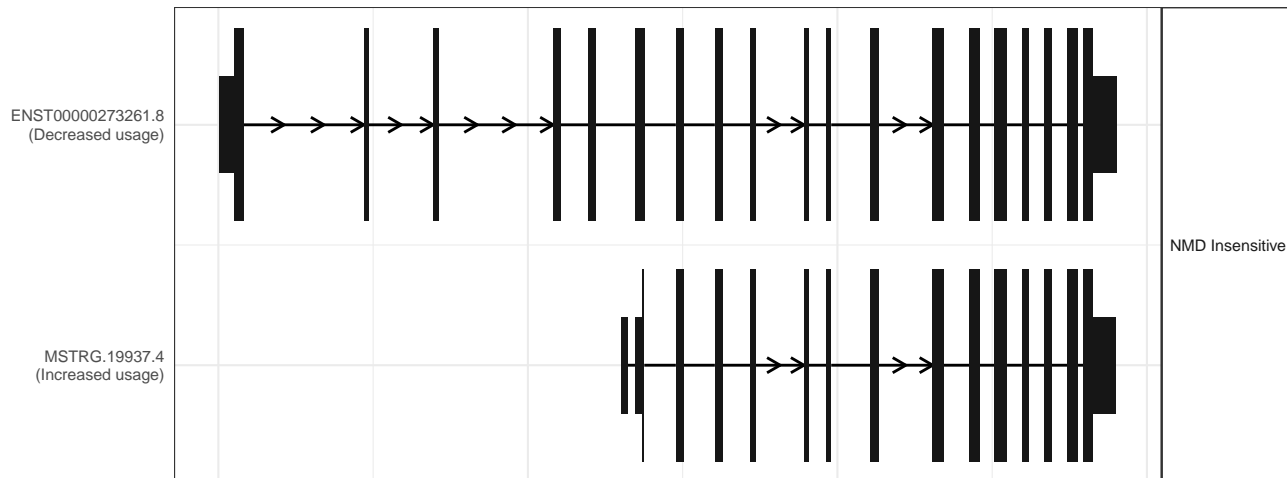


The isoform switch in LRIG1 (intrinsically_resistant vs sensitive)



Condition

intrinsically_resistant

sensitive