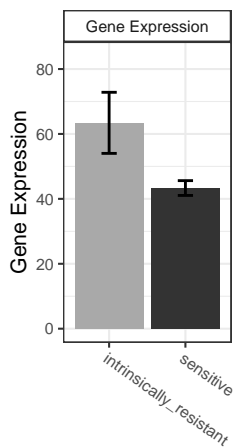
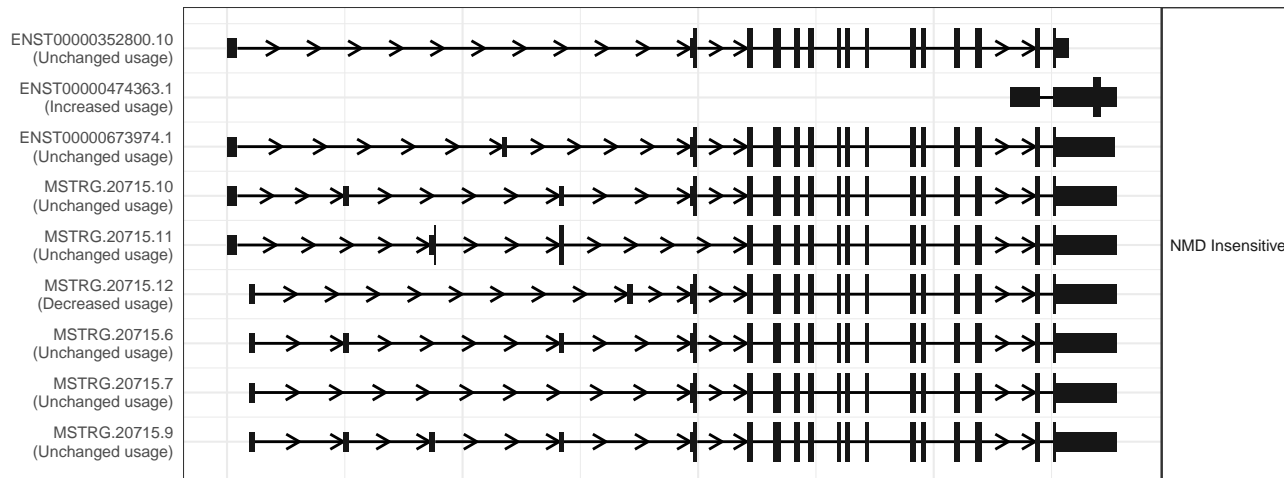
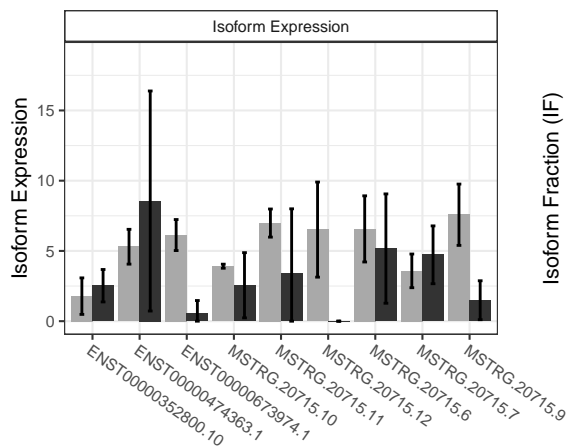


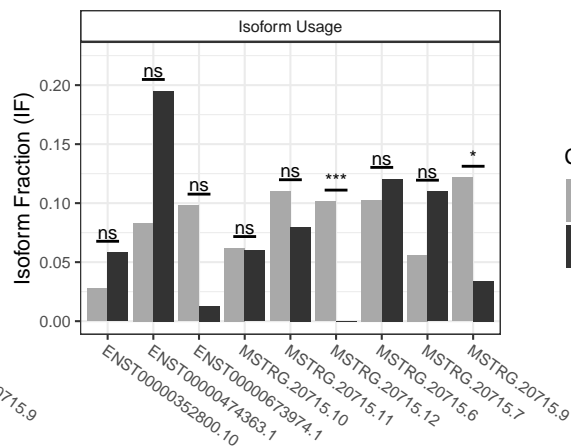
The isoform switch in TBL1XR1 (intrinsically_resistant vs sensitive)



Condition



Isoform



Isoform

Condition

