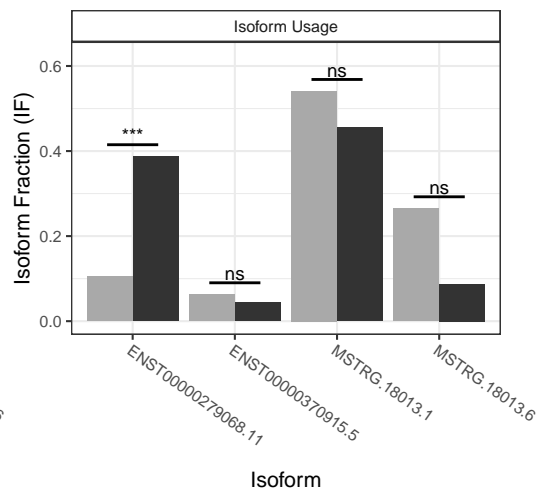
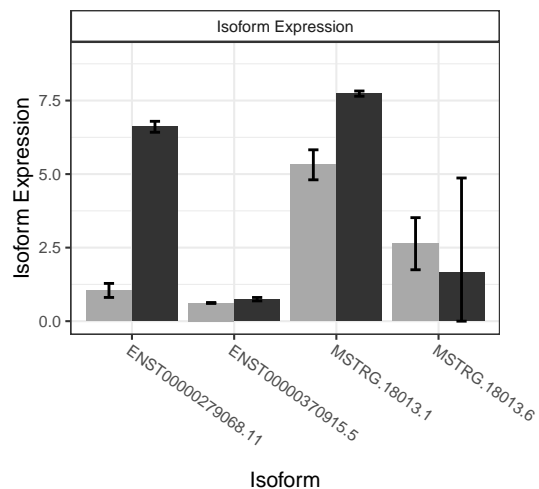
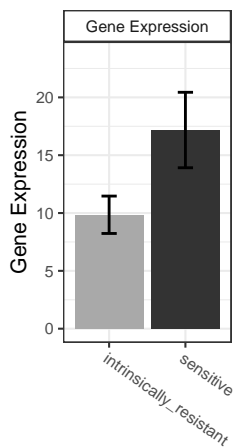
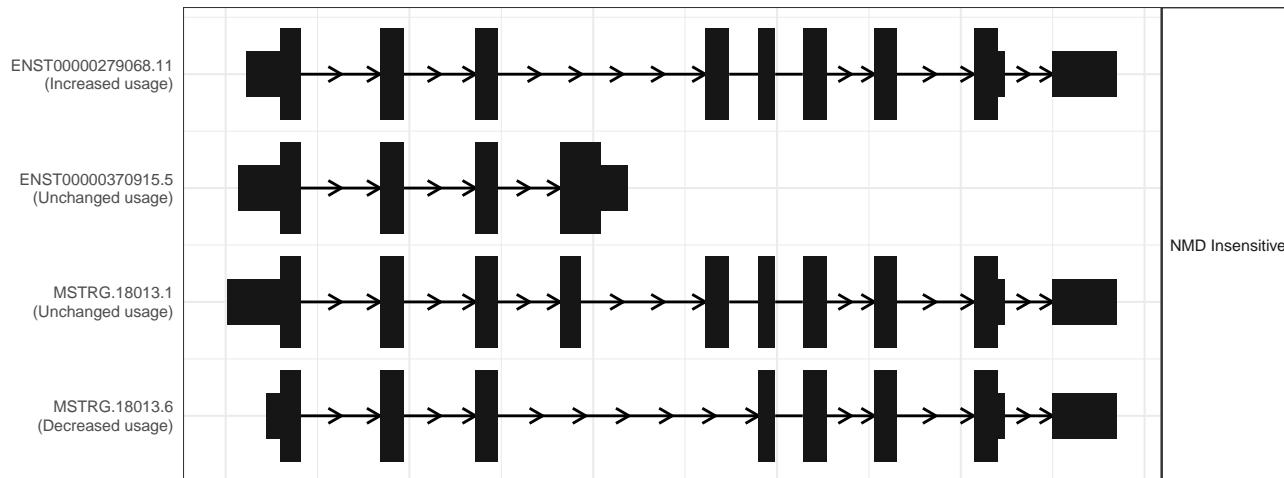


The isoform switch in LSM14B (intrinsically_resistant vs sensitive)



Condition

- intrinsically_resistant
- sensitive