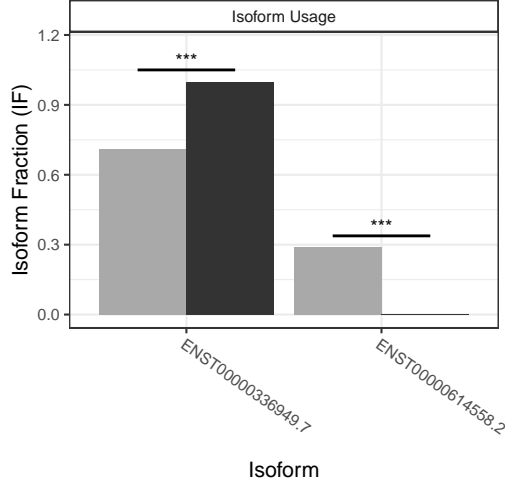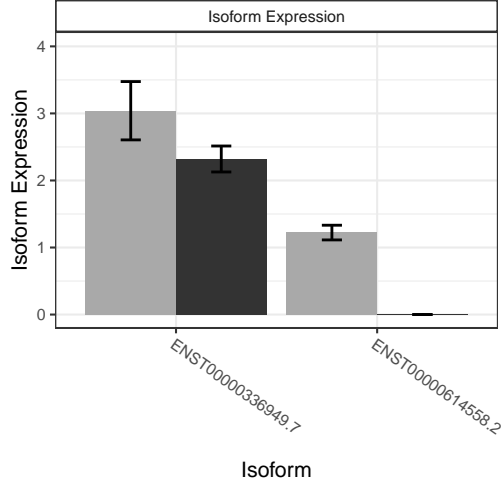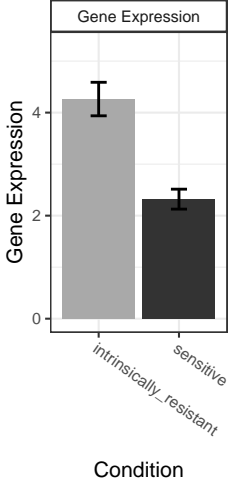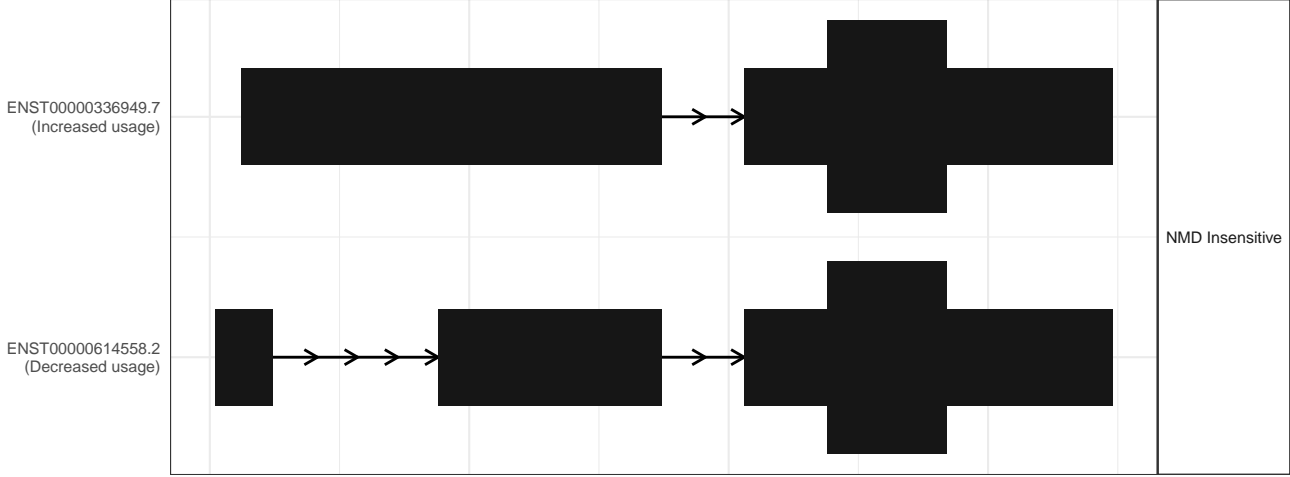The isoform switch in MID1IP1 (intrinsically_resistant vs sensitive)