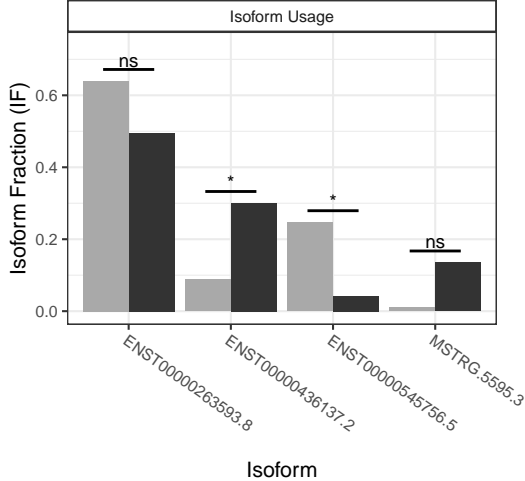
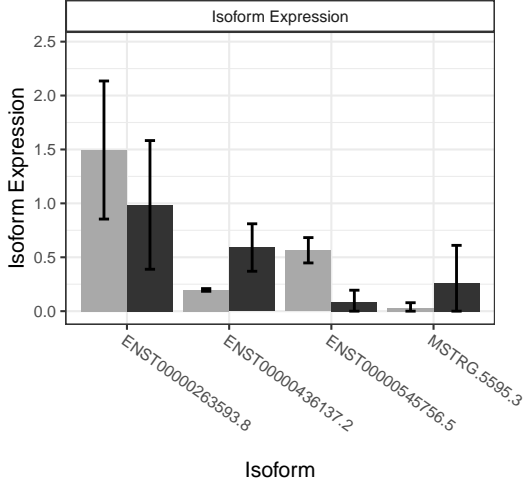
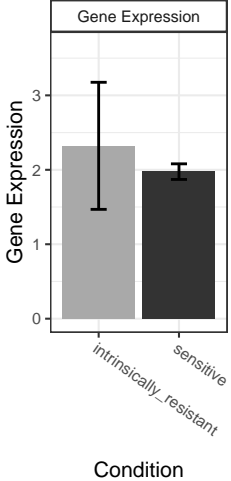
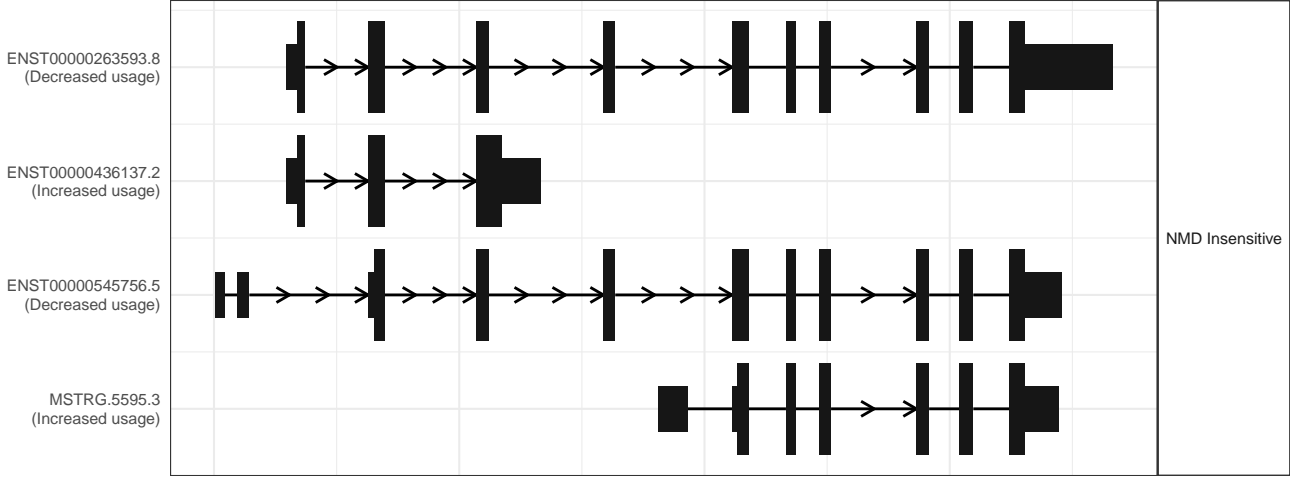


The isoform switch in SIAE (intrinsically_resistant vs sensitive)



Condition

intrinsically_resistant

sensitive