

## 05-2 Adding to the Kernel, Kernel Initialization

### Adding to the Kernel

- Makefile Targets
- Kernel Configuration
- Custom Configuration Options
- Kernel Makefiles
- Kernel Documentation

### Composite Kernel Image

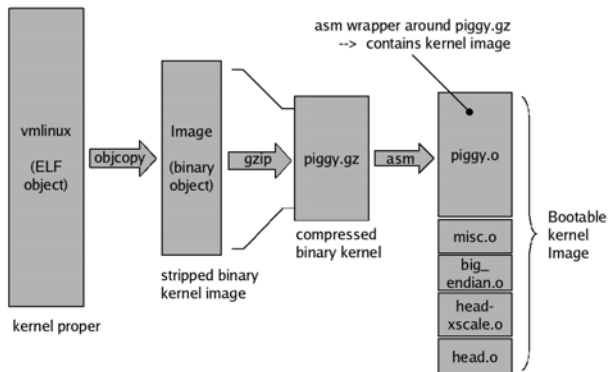


Figure 5.1 page 103

3.2 kernel

### piggy.S

```
.section .piggydata,#alloc
.globl input_data
input_data:
.incbin "arch/arm/boot/compressed/piggy.gz"
.globl input_data_end
input_data_end:
```

### Compiling Kernel

```
host$ source ~/crossCompileEnv.sh
host$ make -j3 uImage
... < many build steps omitted for clarity >
AS      arch/arm/boot/compressed/head.o
XZKERN  arch/arm/boot/compressed/piggy.xzkern
...
AS      arch/arm/boot/compressed/piggy.xzkern.o
LD      arch/arm/boot/compressed/vmlinux
OBJCOPY arch/arm/boot/zImage
Kernel: arch/arm/boot/zImage is ready
UIMAGE arch/arm/boot/uImage
Image Name: Linux-3.8.13+
Created: Thu Oct 3 17:13:18 2013
Image Type: ARM Linux Kernel Image (uncompressed)
Data Size: 2898464 Bytes = 2830.53 kB = 2.76 MB
Load Address: 80008000
Entry Point: 80008000
Image arch/arm/boot/uImage is ready
```

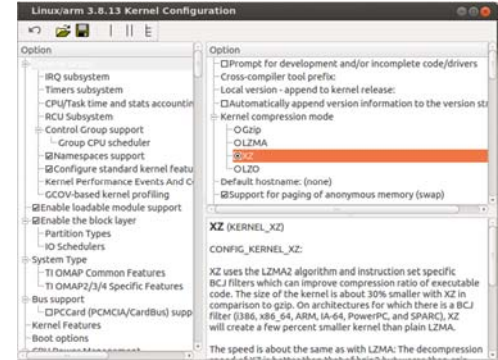
### arch/arm/boot/compressed

```
host$ ls
ashldi3.o      hyp-stub.o      piggy.lzo.S
ashldi3.S      hyp-stub.S      piggy.xzkern
atags_to_fdt.c liblfuncs.o     piggy.xzkern.o
big-endian.S   liblfuncs.S     piggy.xzkern.S
decompress.c  libfdt_env.h    sdhi-sh7372.c
decompress.o  ll_char_wr.S    sdhi-shmobile.c
head.o        Makefile        sdhi-shmobile.h
head.S        misc.c          string.c
head-sal100.S misc.o          string.o
head-shark.S  mmcif-sh7372.c vmlinux
head-sharpsl.S ofw-shark.c    vmlinux.lds
head-shmobile.S piggy.gzip.S   vmlinux.lds.in
head-xscale.S piggy.lzma.S
```

## piggy.xzkern.S

```
.section .piggydata, #alloc
.globl input_data
input_data:
.incbin "arch/arm/boot/compressed/piggy.xzkern"
.globl input_data_end
input_data_end:
```

## How does it know to use kernxz?



## Bootstrap Loader (not bootloader)

- Provide context for kernel
  - Enable instruction set
  - Data caches
  - Disable interrupt
  - C runtime environment
- Decompress (misc.o)
- Relocate kernel image

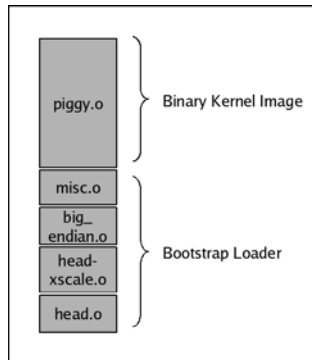


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## Bootstrap Loader (not bootloader)

```
LD vmlinux
SORTEX vmlinux
sort done marker at 81c420
SYMDEF System.map
OBJCOPY arch/arm/boot/Image
Kernel: arch/arm/boot/Image is ready
AS arch/arm/boot/compressed/head.o
XZKERN arch/arm/boot/compressed/piggy.xzkern
CC arch/arm/boot/compressed/misc.o
CC arch/arm/boot/compressed/decompress.o
CC arch/arm/boot/compressed/string.o
SHIPPED arch/arm/boot/compressed/hyp-stub.S
AS arch/arm/boot/compressed/liblfuncs.o
AS arch/arm/boot/compressed/ashldi3.o
AS arch/arm/boot/compressed/hyp-stub.o
AS arch/arm/boot/compressed/piggy.xzkern.o
LD arch/arm/boot/compressed/vmlinux
OBJCOPY arch/arm/boot/zImage
Kernel: arch/arm/boot/zImage is ready
UIMAGE arch/arm/boot/uImage
```

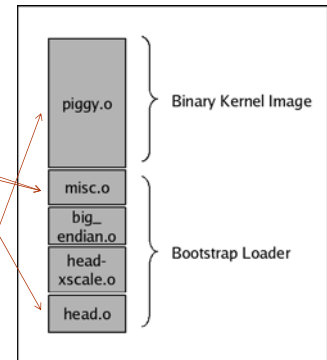


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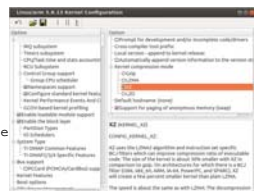
## decode.c

```
#ifdef CONFIG_KERNEL_GZIP
#include ".../lib/decompress_inflate"
#endif

#ifdef CONFIG_KERNEL_LZO
#include ".../lib/decompress_unlzo.c"
#endif

#ifdef CONFIG_KERNEL_LZMA
#include ".../lib/decompress_unlzma.c"
#endif

#ifdef CONFIG_KERNEL_XZ
#define memmove memmove
#define memcpy memcpy
#include ".../lib/decompress_unxz.c"
#endif
```



## Boot Messages

- See handout
  - Note kernel version string
  - Note kernel command line
  - Exercise 21a shows how to display the messages in the handout
- ```
beagle$ mount /dev/mmcblk0p1 /media/BEAGLEBONE/
beagle$ cd /media/BEAGLEBONE/
beagle$ ls
App ID.txt MLO.orig autorun.inf u-boot.img.orig
Docs LICENSE.txt README.md u-boot.img uEnv.txt
Drivers MLO START.htm u-boot.img.new uEnv.txt.orig
beagle$ cat uEnv.txt
optargs=quiet drm.debug=7
capemgr.disable_partno=BB-BONELT-HDMI, BB-BONELT-HDMIN
```

remove

### 5-3 ARM boot control flow

The diagram illustrates the ARM boot control flow. It starts with a 'PowerOn' event (indicated by a lightning bolt) triggering the 'Bootloader' (U-boot). The 'Bootloader' then executes 'start' to load the 'Bootstrap loader' (head.o). The 'Bootstrap loader' then executes 'start' to load the 'Kernel vmlinux' (head.o). Finally, the 'Kernel vmlinux' executes 'start\_kernel' to load the 'Kernel main.o' (main.o).

```
graph LR; PowerOn --> Bootloader; Bootloader -- start --> Bootstrap_loader; Bootstrap_loader -- start --> Kernel_vmlinux; Kernel_vmlinux -- start_kernel --> Kernel_main_o;
```

PowerOn

U-boot

start

head.o

start

head.o

start\_kernel

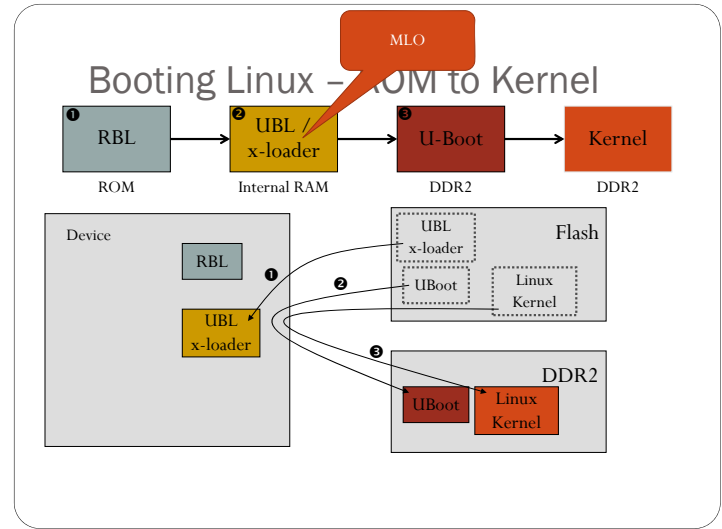
main.o

Bootloader

Bootstrap loader

Kernel vmlinux

Kernel main.o



# .../arch/arm/boot/compressed/head.S

```
#include <linux/linkage.h>

#ifdef DEBUG

#if defined(CONFIG_DEBUG_LL)

#ifdef CONFIG_CPU_V6

        .macro   loadsp, rb
        .endm

        .macro   writeb, ch, rb
        mcr      pl4, 0, \ch, c0, c5, 0
        .endm

#else

        .macro   loadsp, rb
        .endm

        .macro   writeb, ch, rb
        mcr      pl4, 0, \ch, c1, c0, 0
        .endm

#endif

#endif

#

#else

#include <mach/debug-macro.S>

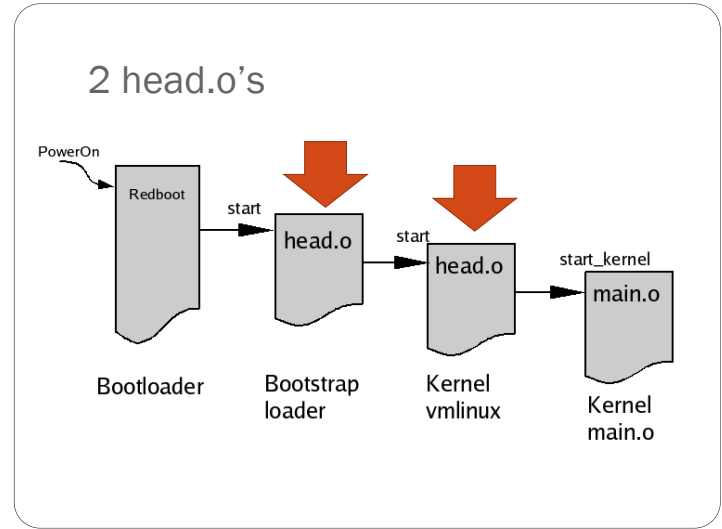
        .macro   writeb, ch, rb
        senduart \ch, \rb
        .endm

#if defined(CONFIG_ARCH_SAL100)

        .macro   loadsp, rb
        mov      \rb, #0x80000000 @
        physical base address

#endif
```

How do you find the value?




## .../arch/arm/kernel/head.S

1. Checks of valid processor and architecture
2. Creates initial page table entries
3. Enables the processor's memory management unit (MMU)
4. Establishes limited error detection and reporting
5. Jumps to the start of the kernel proper,  
**start\_kernel()** in **main.c**.

Find these on the handout

# Kernel Startup

- `arch/arm/kernel/head.S`

`b start_kernel`  Find this for HW 6

## .../init/main.c

```
asmlinkage void __init start_kernel(void)
{
    char * command_line;
    extern struct kernel_param __start__param[], __stop__param[];

    smp_setup_processor_id();

    /*
     * Need to run as early as possible, to initialize the
     * lockdep hash:
     */
    lockdep_init();
    debug_objects_early_init();
    cgroup_init_early();

    local_irq_disable();
    early_boot_irqs_off();
    early_init_irq_lock_class();
}
```

## Kernel Command Line Processing

- Read 5.3 on Kernel Command-Line Processing
- It presents the **\_\_setup** macro

```
console=ttyO0,115200n8
run_hardware_tests
root=/dev/mmcblk0p2 ro
rootfstype=ext4 rootwait
```

## Console Setup Code Snippet

```
/*
 * Setup a list of consoles. Called from init/main.c
 */
static int __init console_setup(char *str)
{
    char buf[sizeof(console_cmdline[0].name) + 4]; /* 4 for index */
    char *s, *options, *bri_options = NULL;
    int idx;
    ...
    <body omitted for clarity...>
    ...
    return 1;
}
__setup("console=", console_setup);
```

Registration  
function

From .../kernel/printk.c

## .../include/linux/init.h

```
/*
 * Only for really core code. See moduleparam.h for the normal way.
 */
/* Force the alignment so the compiler doesn't space elements of the
 * obs_kernel_param "array" too far apart in .init.setup.
 */
#define __setup_param(str, unique_id, fn, early) \
    static char __setup_str_##unique_id[] __initdata __aligned(1) = str; \
    \
    static struct obs_kernel_param __setup_##unique_id \
    __used __section(.init.setup) \
    __attribute__((aligned((sizeof(long))))) \
    = { __setup_str_##unique_id, fn, early }

#define __setup(str, fn) \
    __setup_param(str, fn, fn, 0)
```

## \_\_setup

```
__setup("console=", console_setup);
```

- Expands to

```
static const char __setup_str_console_setup[] __initconst \
__aligned(1) = "console=";
static struct obs_kernel_param __setup_console_setup __used \
__section(.init.setup) __attribute__ \
((aligned((sizeof(long))))) \
= { __setup_str_console_setup, console_setup, early};
```

- Which expands to

```
static struct obs_kernel_param __setup_console_setup \
__section(.init.setup) = { __setup_str_console_setup, \
console_setup, early};
```

- This stores the code in a table in section **.init.setup**.

## On initialization...

- The table in **.init.setup** has
  - Parameter string ("**console=**") and
  - Pointer to the function that processes it.
- This way the initialization code can process everything on the command line without knowing at compile time where all the code is.
- See section 5.3 for more details.