

1

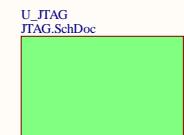
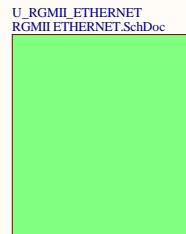
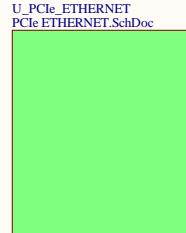
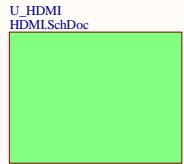
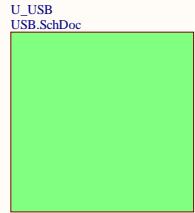
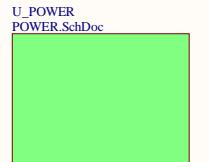
2

3

4

5

6



U_JETSON_AXG_CONNECTOR
JETSON AGX CONNECTOR.SchDoc

NV_UPHY7
NV_PCIE_CTRL3
NV_I2C2
NV_I2S2
NV_UART5
NV_USB2
NV_PCIE_WAKE_N

U_M2_E-KEY
M2 E-KEY.SchDoc

NV_UPHY7
NV_PCIE_CTRL3
NV_I2C2
NV_I2S2
NV_UART5
NV_USB2
NV_PCIE_WAKE_N

GPIO01_WIFI_DISABLE
GPIO03_AP_WAKE_BT_M2
GPIO12_M2_WAKE_AP
GPIO13_BT_WAKE_AP
GPIO14_M2_EN
GPIO24_SAR_TOUT
GPIO26_BT_RST_D_M2
GPIO30_M2_E_ALERT

U_M2_M-KEY
M2 M-KEY.SchDoc

NV_GPIO

GPIO01 → NV_GPIO.GPIO01
 GPIO02 → NV_GPIO.GPIO03
 GPIO03 → NV_GPIO.GPIO12
 GPIO04 → NV_GPIO.GPIO13
 GPIO05 → NV_GPIO.GPIO14
 GPIO06 → NV_GPIO.GPIO24
 GPIO07 → NV_GPIO.GPIO26
 GPIO08 → NV_GPIO.GPIO30

U_CAN
CAN.SchDoc

Title: High Level Design	
Project: Jetson AGX Xavier Carrier Board.PjPcb	
Rev: 1	Reviewer: *
Engineer: Cindy Li, Chealsie Bains	Date: 2021-04-27 Sheet: 1 of 20

1 | 2 | 3 | 4 | 5 | 6

A

A

B

B

C

C

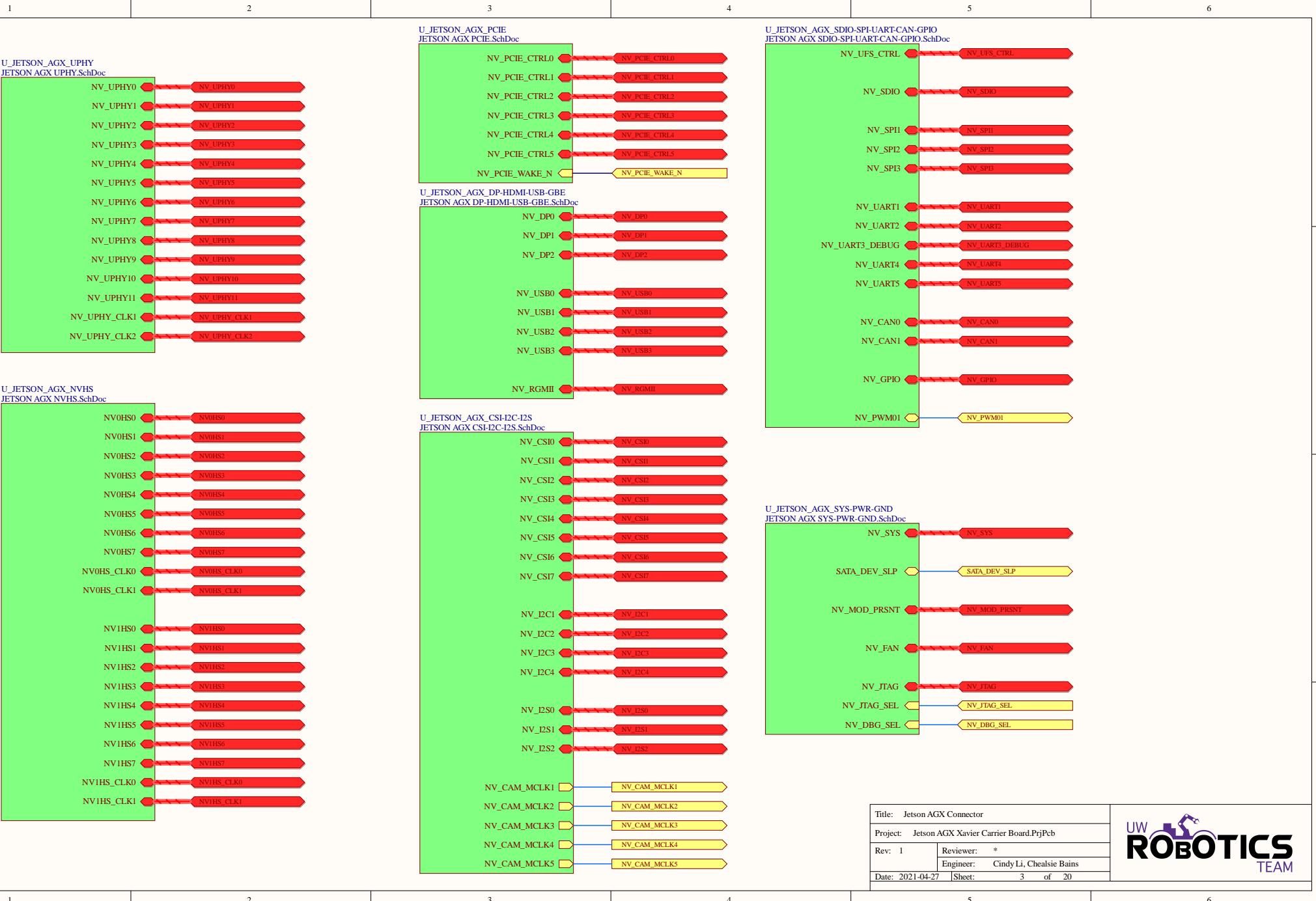
D

D

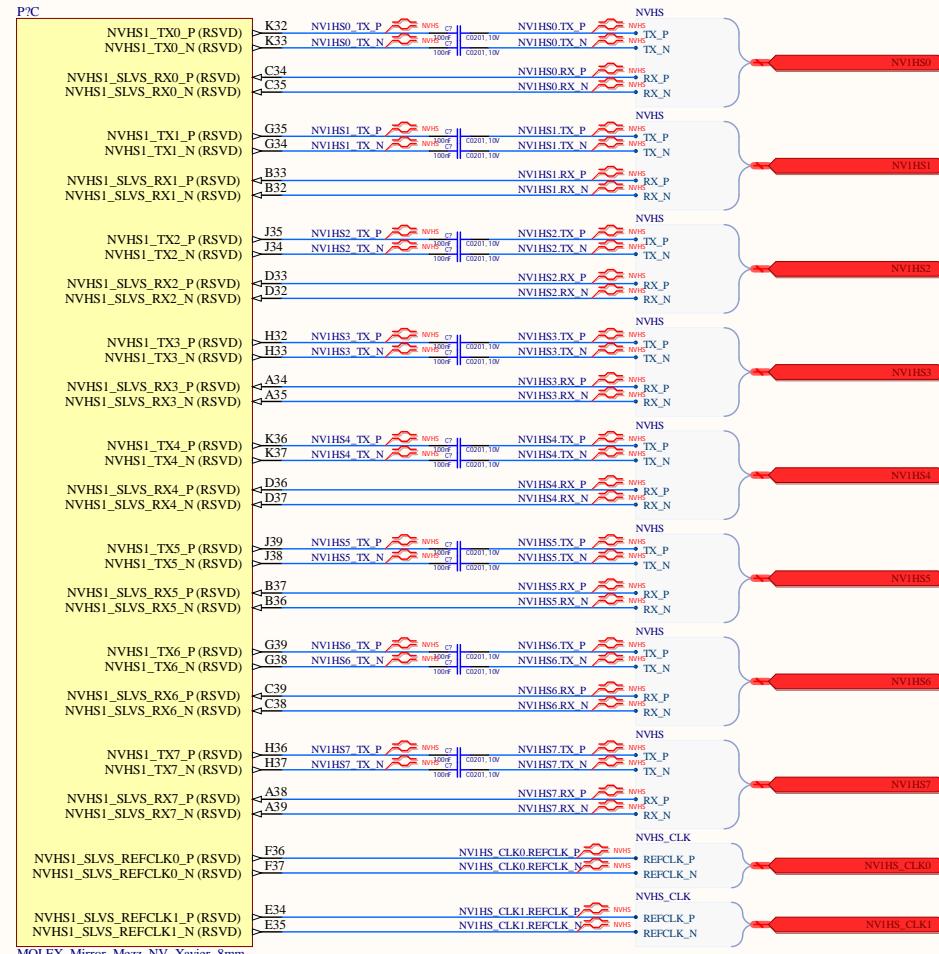
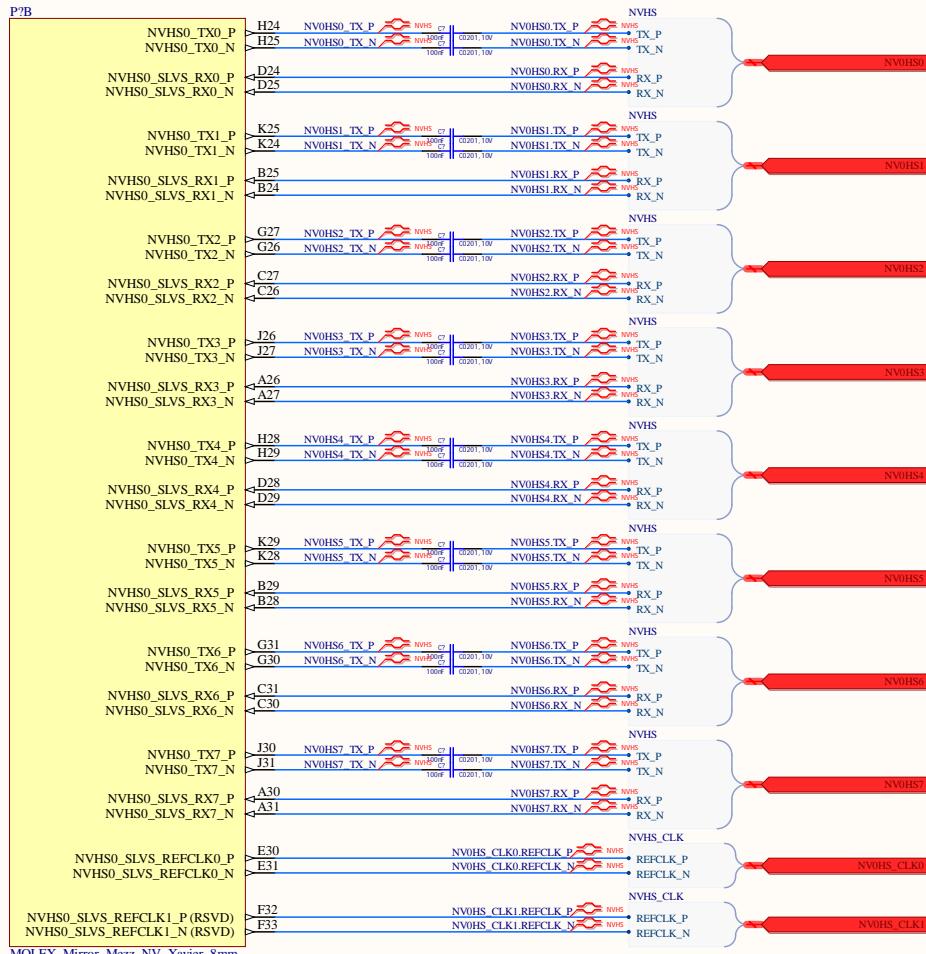
Title: Power	
Project: Jetson AGX Xavier Carrier Board.PrjPcb	
Rev: 1	Reviewer: *
	Engineer: Cindy Li, Chealsie Bains
Date: 2021-04-27	Sheet: 2 of 20



1 | 2 | 3 | 4 | 5 | 6



A



TODO: replace passives
with our library parts

A

B

C

D

Title: Jetson AGX NVHS

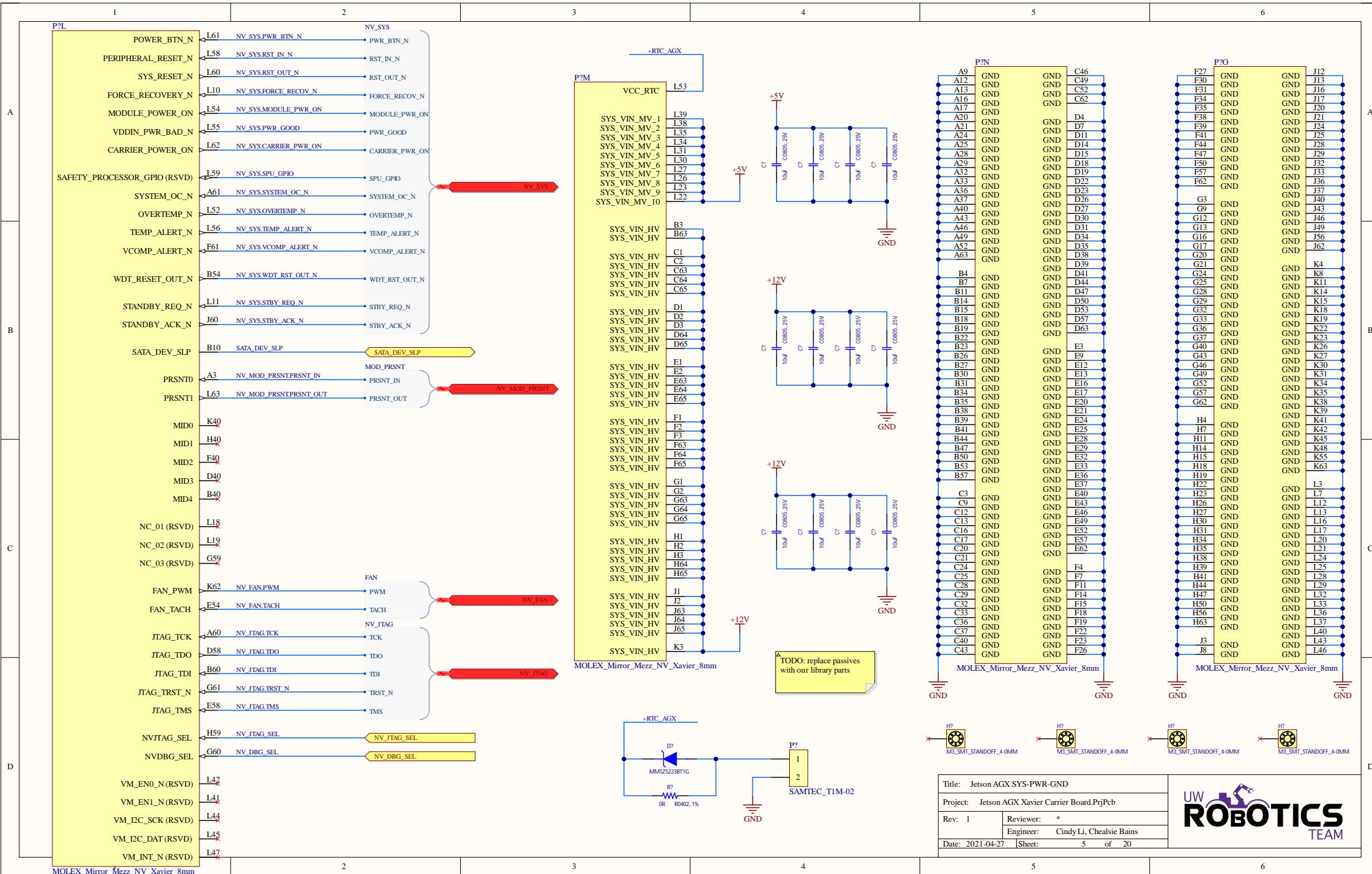
Project: Jetson AGX Xavier Carrier Board.PnjPcb

Rev: 1 Reviewer: *

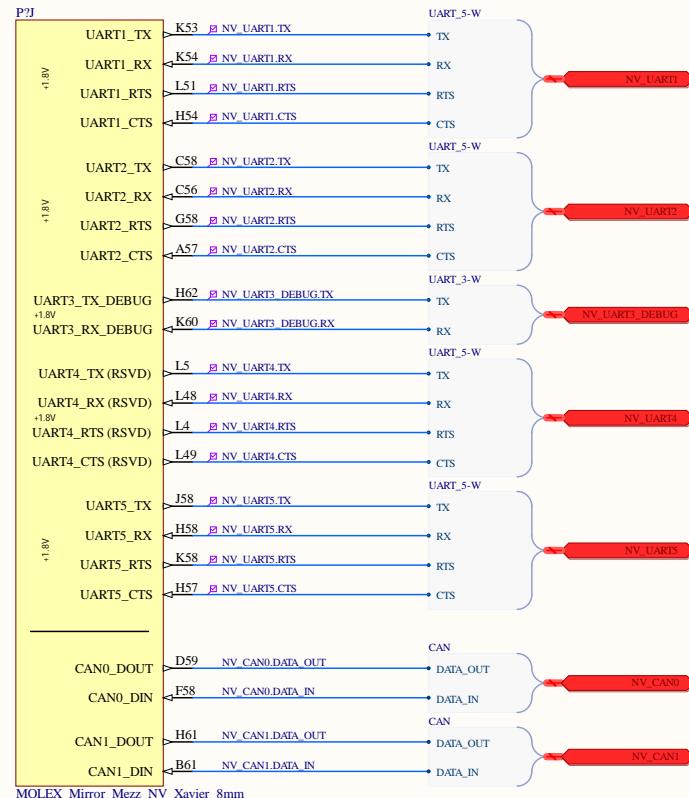
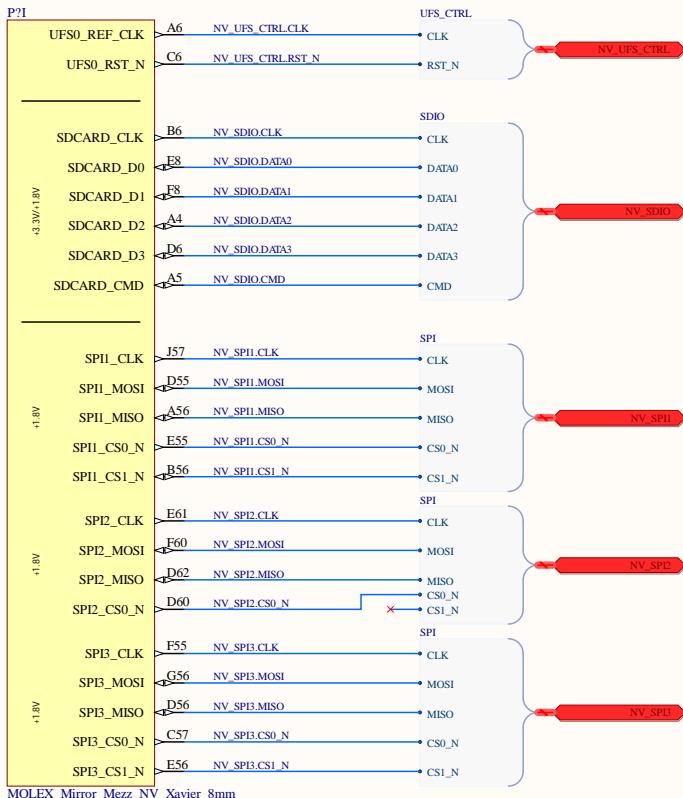
Engineer: Cindy Li, Chealsie Bains

Date: 2021-04-27 Sheet: 4 of 20





1 2 3 4 5 6

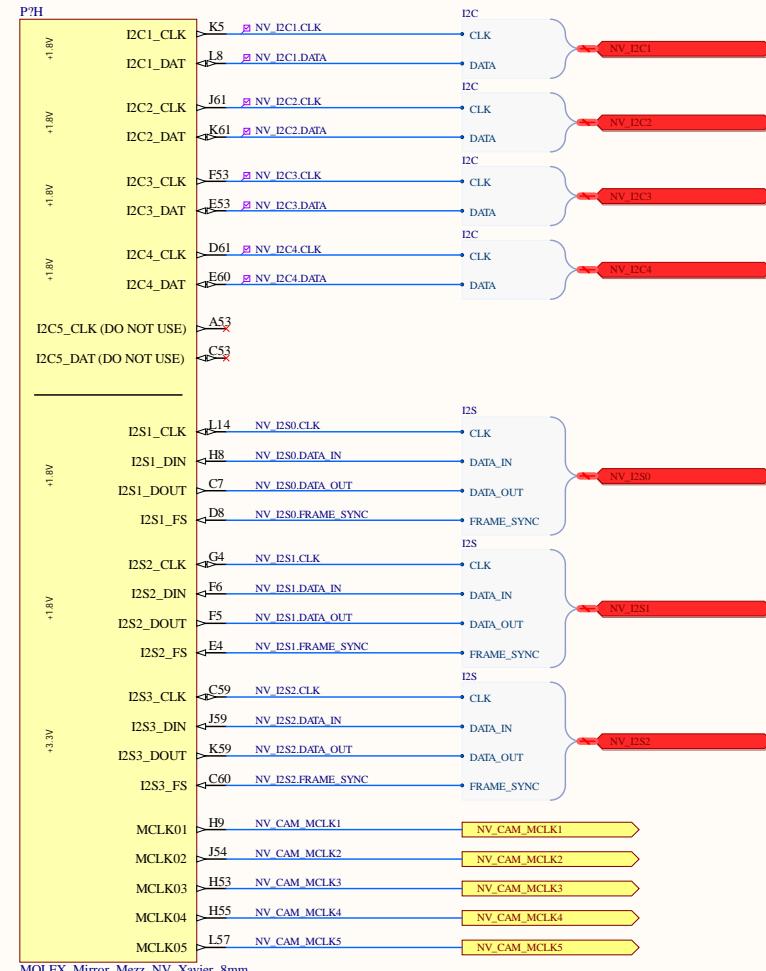
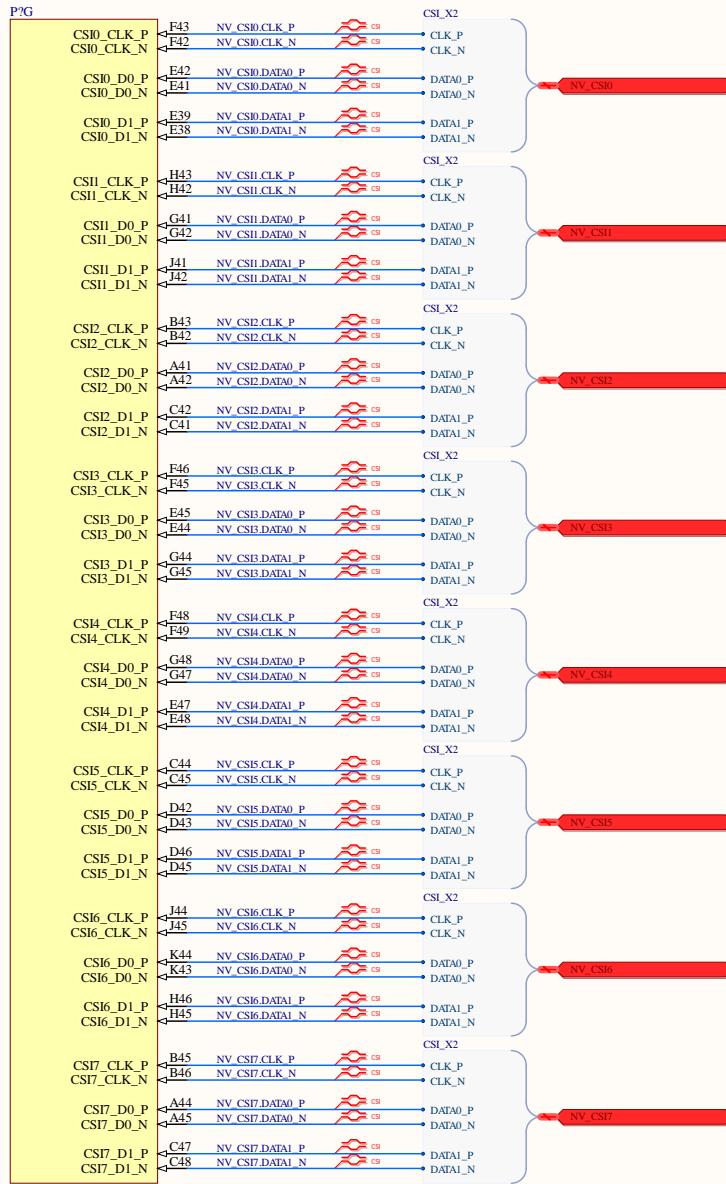


Title: Jetson AGX SDIO-SPI-UART-CAN-GPIO	
Project: Jetson AGX Xavier Carrier Board.PrdPcb	
Rev: 1	Reviewer: *
Engineer: Cindy Li, Chealsie Bains	
Date: 2021-04-27	Sheet: 6 of 20



1 2 3 4 5 6

1 2 3 4 5 6



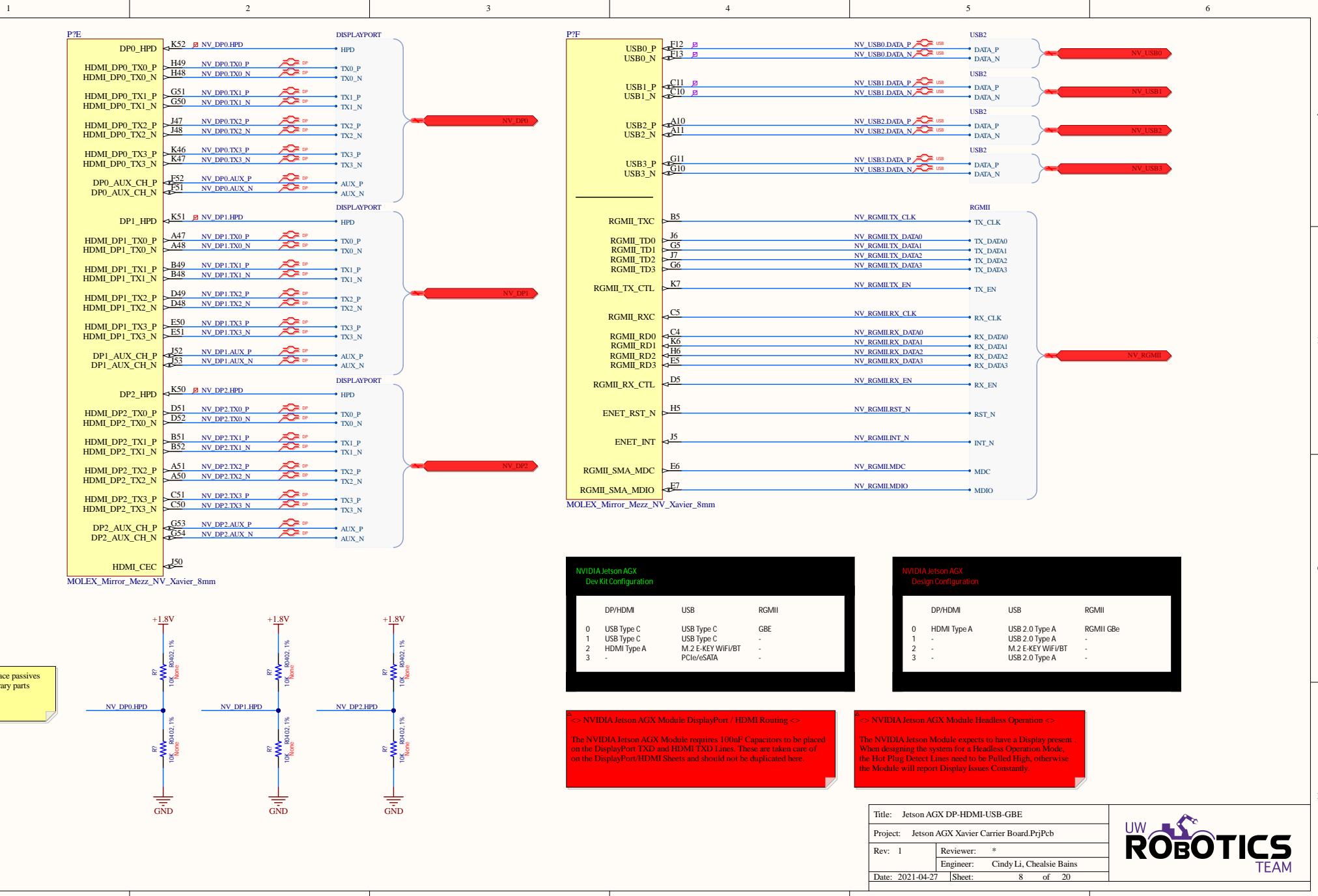
NVIDIA Jetson AGX Dev Kit Configuration		
CSI	I2C	I2S
0 CAM Expansion	-	-
1 CAM Expansion	ID EEPROM / Debug	-
2 CAM Expansion	PCIe x16 / M.2 E-Key	40 Pin Expansion
3 CAM Expansion	-	M.2 E-Key WiFi/BT
4 CAM Expansion	-	CAM Expansion
5 CAM Expansion	-	M.2 M.2 NVMe
6 CAM Expansion	-	40 Pin Expansion
7 CAM Expansion	-	-

NVIDIA Jetson AGX Design Configuration		
CSI	I2C	I2S
0 -	-	-
1 -	-	-
2 -	-	M.2 E-Key WiFi/BT
3 -	-	M.2 E-Key WiFi/BT
4 -	-	-
5 -	-	-
6 -	-	-
7 -	-	-

Title:	Jetson AGX CSI-I2C-I2S	
Project:	Jetson AGX Xavier Carrier Board.PnjPcb	
Rev: 1	Reviewer: *	
Engineer:	Cindy Li, Chealsie Bains	
Date: 2021-04-27	Sheet:	7 of 20



1 2 3 4 5 6



A

A

B

B

C

C

D

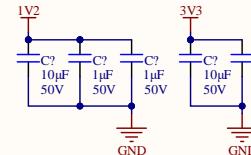
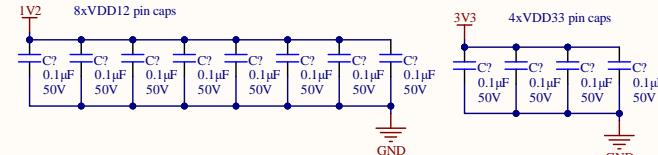
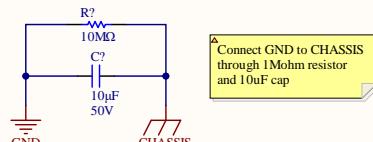
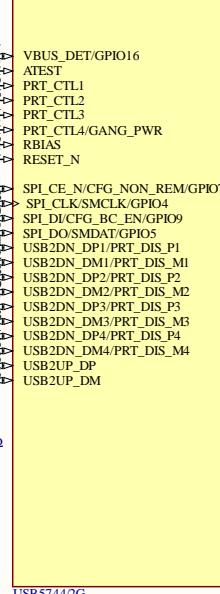
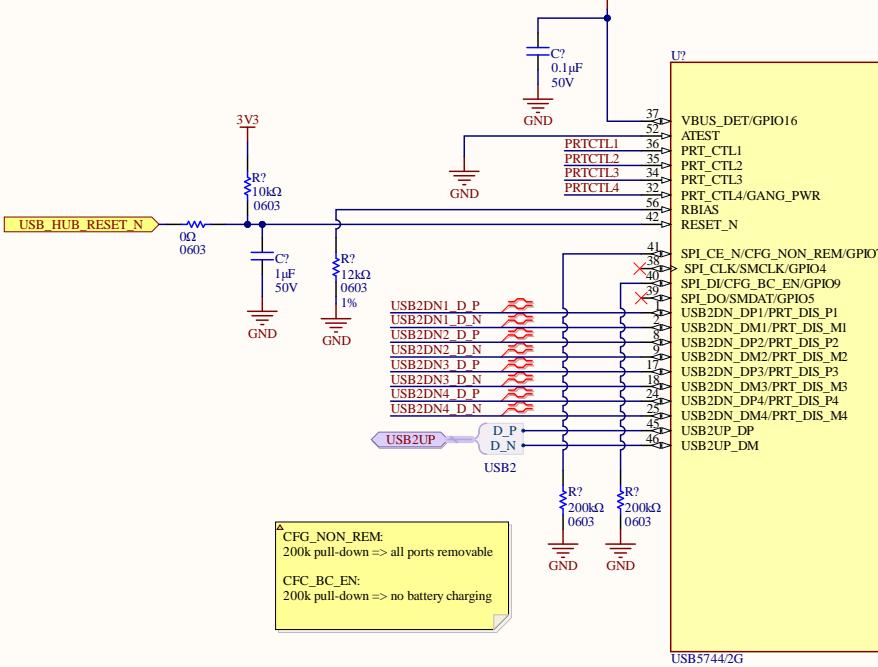
D



Title: Jetson AGX PCIe	
Project: Jetson AGX Xavier Carrier Board.PnjPcb	
Rev: 1	Reviewer: *
Engineer: Cindy Li, Chealsie Bains	Date: 2021-04-27 Sheet: 9 of 20





Bulk Capacitors**Decoupling Capacitors****USB Controller**

Title: USB	
Project: Jetson AGX Xavier Carrier Board.PrjPcb	
Rev: 1	Reviewer: *
Engineer: Cindy Li, Chealsie Bains	Date: 2021-04-27
Sheet: 11 of 20	

A

A

B

B

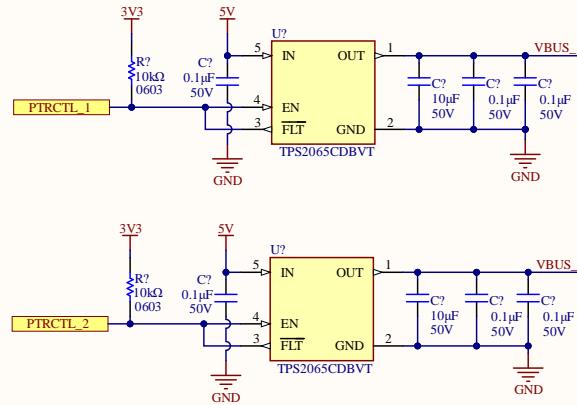
C

C

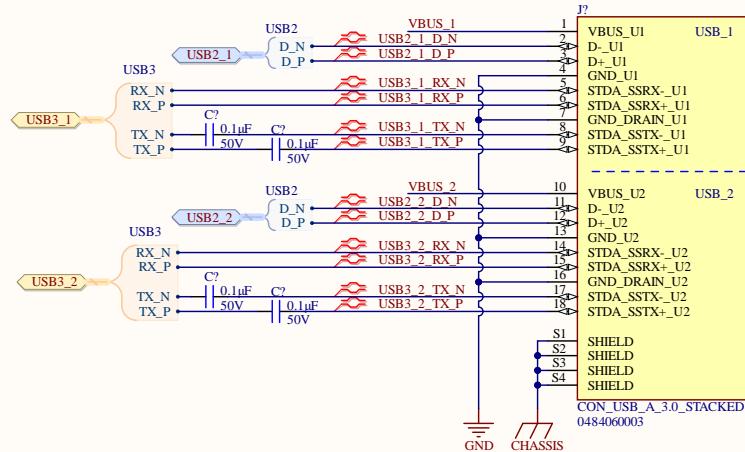
D

D

Power Switches



USB Ports



Title: USB Port	
Project: Jetson AGX Xavier Carrier Board.PjPcb	
Rev: 1	Reviewer: *
Engineer: Cindy Li, Chealsie Bains	
Date: 2021-04-27	Sheet: 12 of 20

1 | 2 | 3 | 4 | 5 | 6

A

A

B

B

C

C

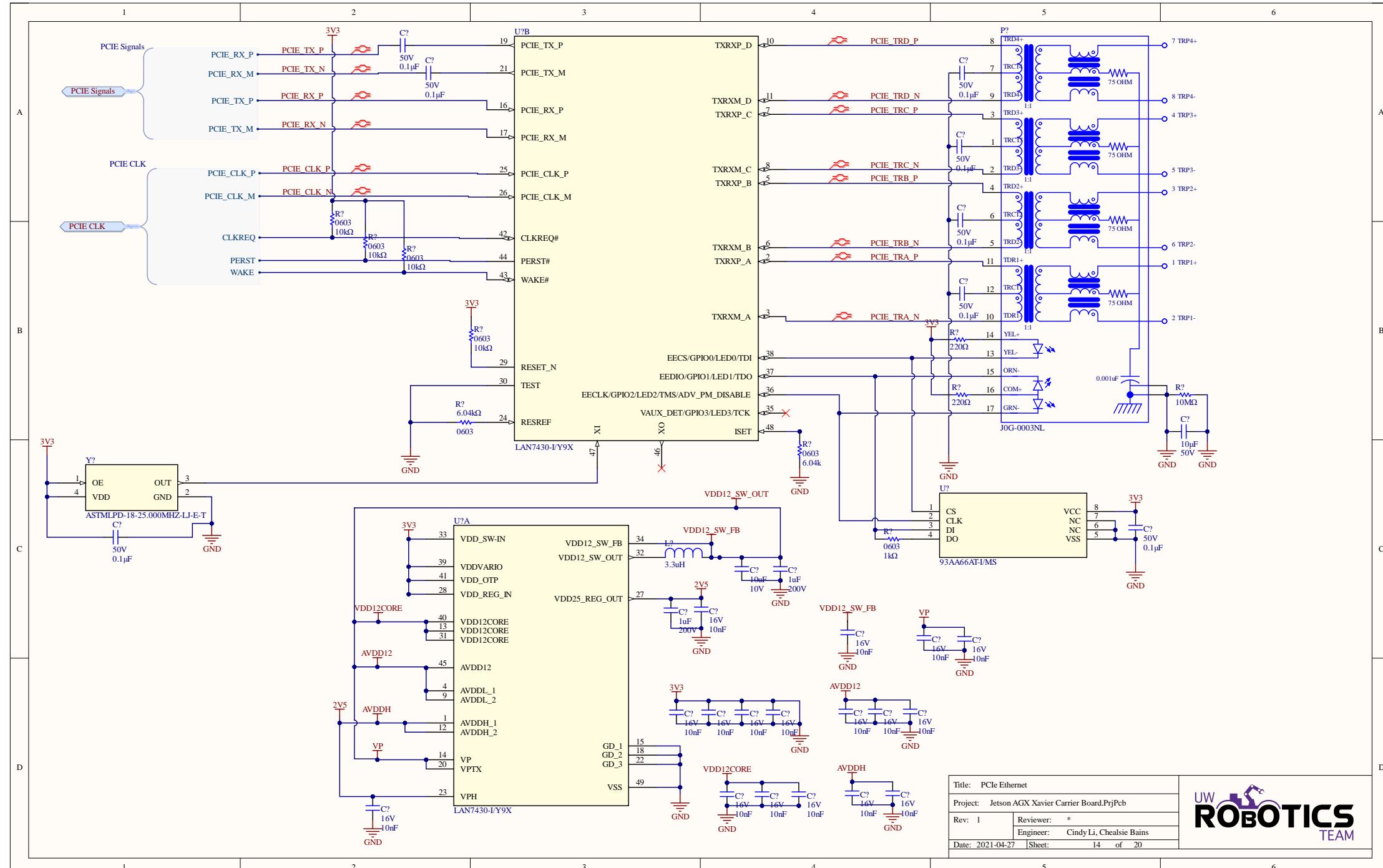
D

D

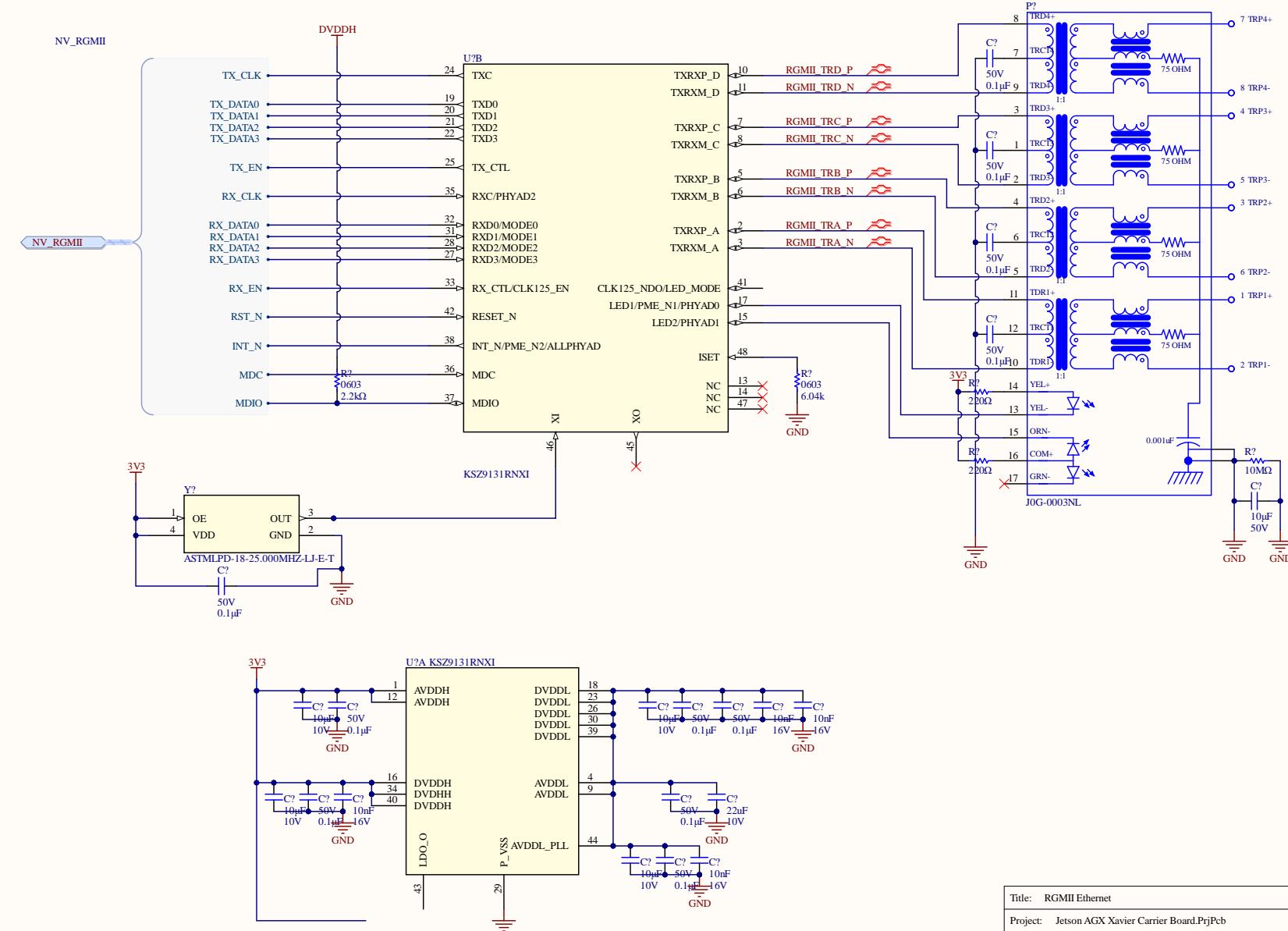
Title: HDMI	
Project: Jetson AGX Xavier Carrier Board.PrjPcb	
Rev: 1	Reviewer: *
	Engineer: Cindy Li, Chealsie Bains
Date: 2021-04-27	Sheet: 13 of 20



1 | 2 | 3 | 4 | 5 | 6

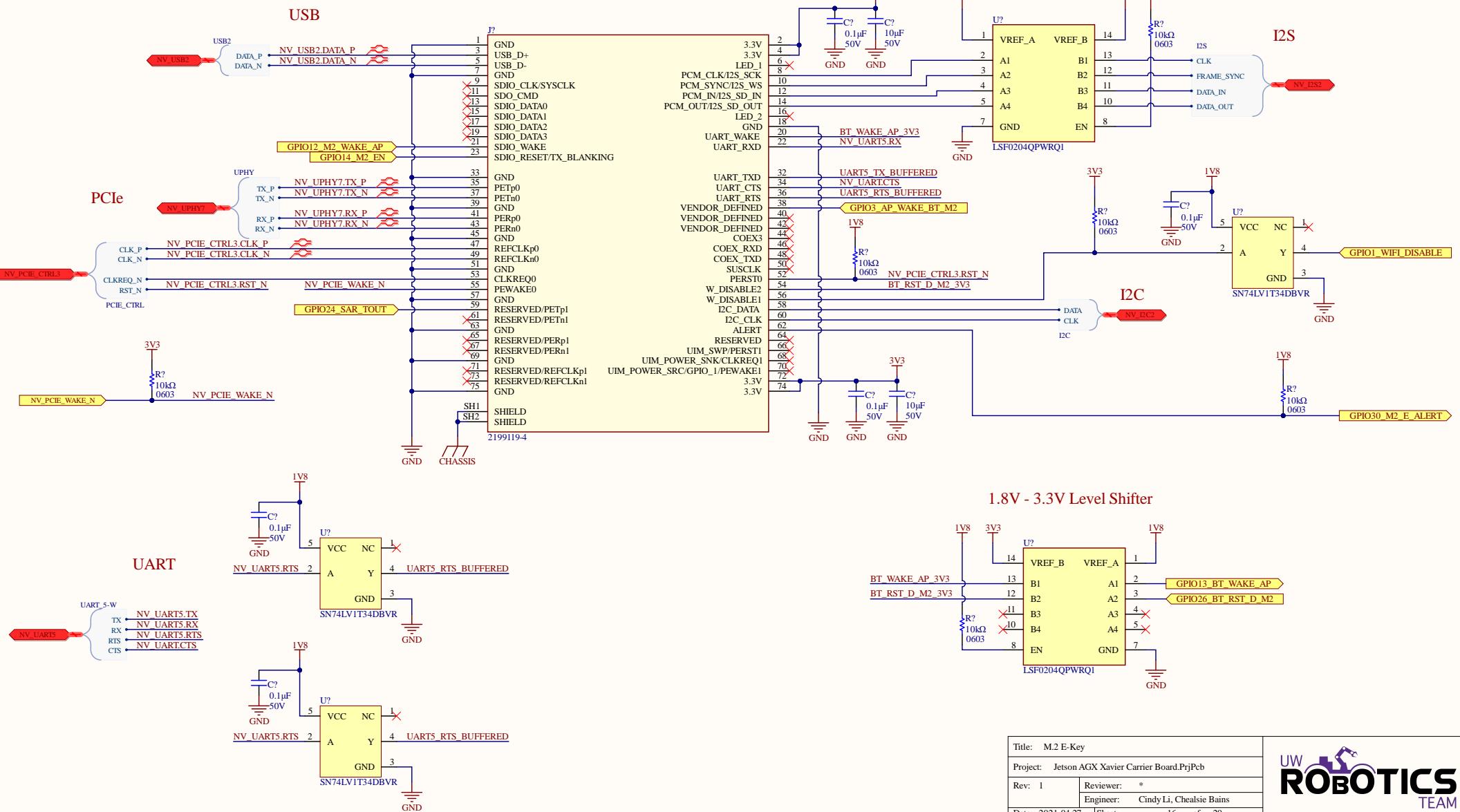


Title: PCIe Ethernet	
Project:	Jetson AGX Xavier Carrier Board.PjPcb
Rev: 1	Reviewer: *
Engineer:	Cindy Li, Chealsie Bains

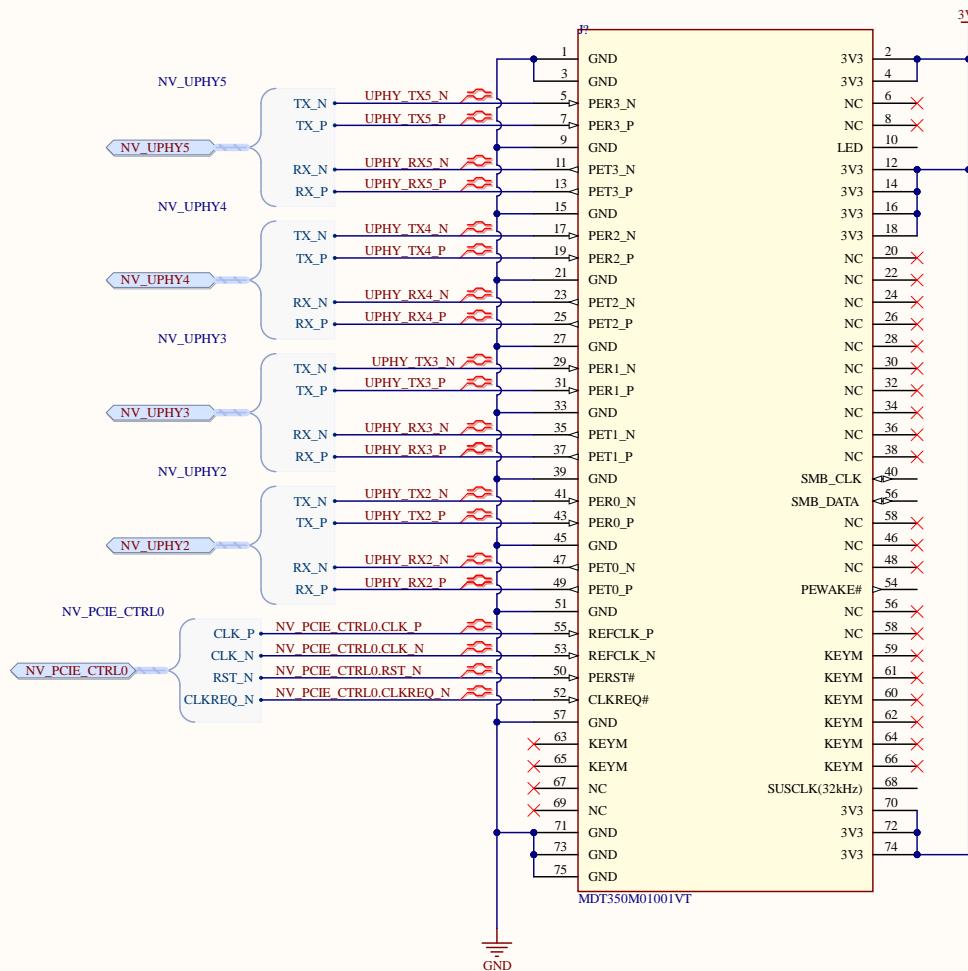


Title:	RGMII Ethernet
Project:	Jetson AGX Xavier Carrier Board.PrjPcb
Rev:	1
Date:	2021-04-27
Reviewer:	*
Engineer:	Cindy Li, Chealsie Bains
Sheet:	15 of 20



M.2 E-Key Wifi/Bluetooth Connector

Title: M.2 E-Key	
Project: Jetson AGX Xavier Carrier Board.PjPcb	
Rev: 1	Reviewer: *
Engineer: Cindy Li, Chealsie Bains	Date: 2021-04-27



Title: M.2 M-Key	
Project: Jetson AGX Xavier Carrier Board.PnjPcb	
Rev: 1	Reviewer: *
Engineer: Cindy Li, Chealsie Bains	Date: 2021-04-27
Sheet: 17	of 20

1 | 2 | 3 | 4 | 5 | 6

A

A

B

B

C

C

D

D

Title: JTAG	
Project: Jetson AGX Xavier Carrier Board.PrjPcb	
Rev: 1	Reviewer: *
	Engineer: Cindy Li, Chealsie Bains
Date: 2021-04-27	Sheet: 18 of 20



1 | 2 | 3 | 4 | 5 | 6

1 | 2 | 3 | 4 | 5 | 6

A

A

B

B

C

C

D

D

Title: GPIO and Serial	
Project: Jetson AGX Xavier Carrier Board.PrjPcb	
Rev: 1	Reviewer: *
	Engineer: Cindy Li, Chealsie Bains
Date: 2021-04-27	Sheet: 19 of 20



1 | 2 | 3 | 4 | 5 | 6

1 | 2 | 3 | 4 | 5 | 6

A

A

B

B

C

C

D

D

Title: CAN	
Project: Jetson AGX Xavier Carrier Board.PrjPcb	
Rev: 1	Reviewer: *
	Engineer: Cindy Li, Chealsie Bains
Date: 2021-04-27	Sheet: 20 of 20



1 | 2 | 3 | 4 | 5 | 6