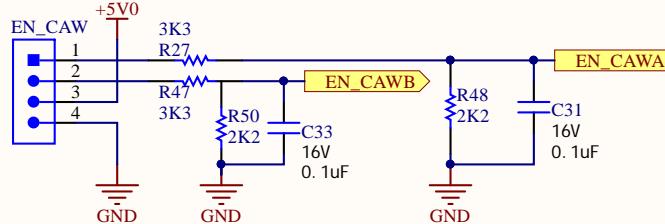
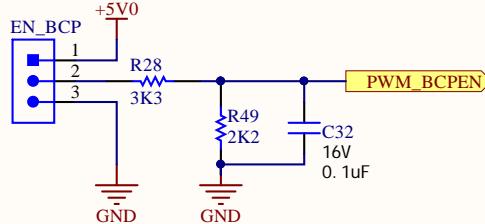
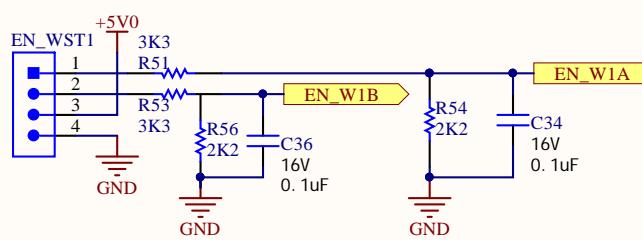
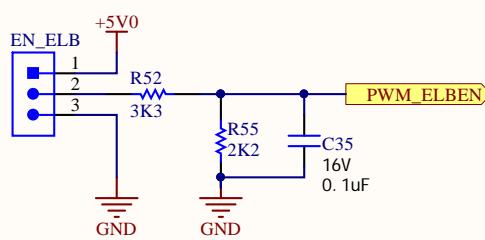


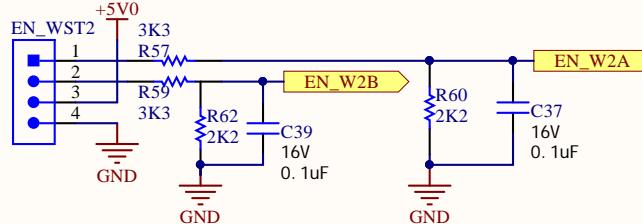
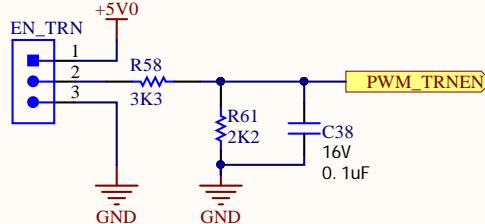
A



B



C



D

Title

Size

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Revision

Date: 2019-12-28

Sheet of

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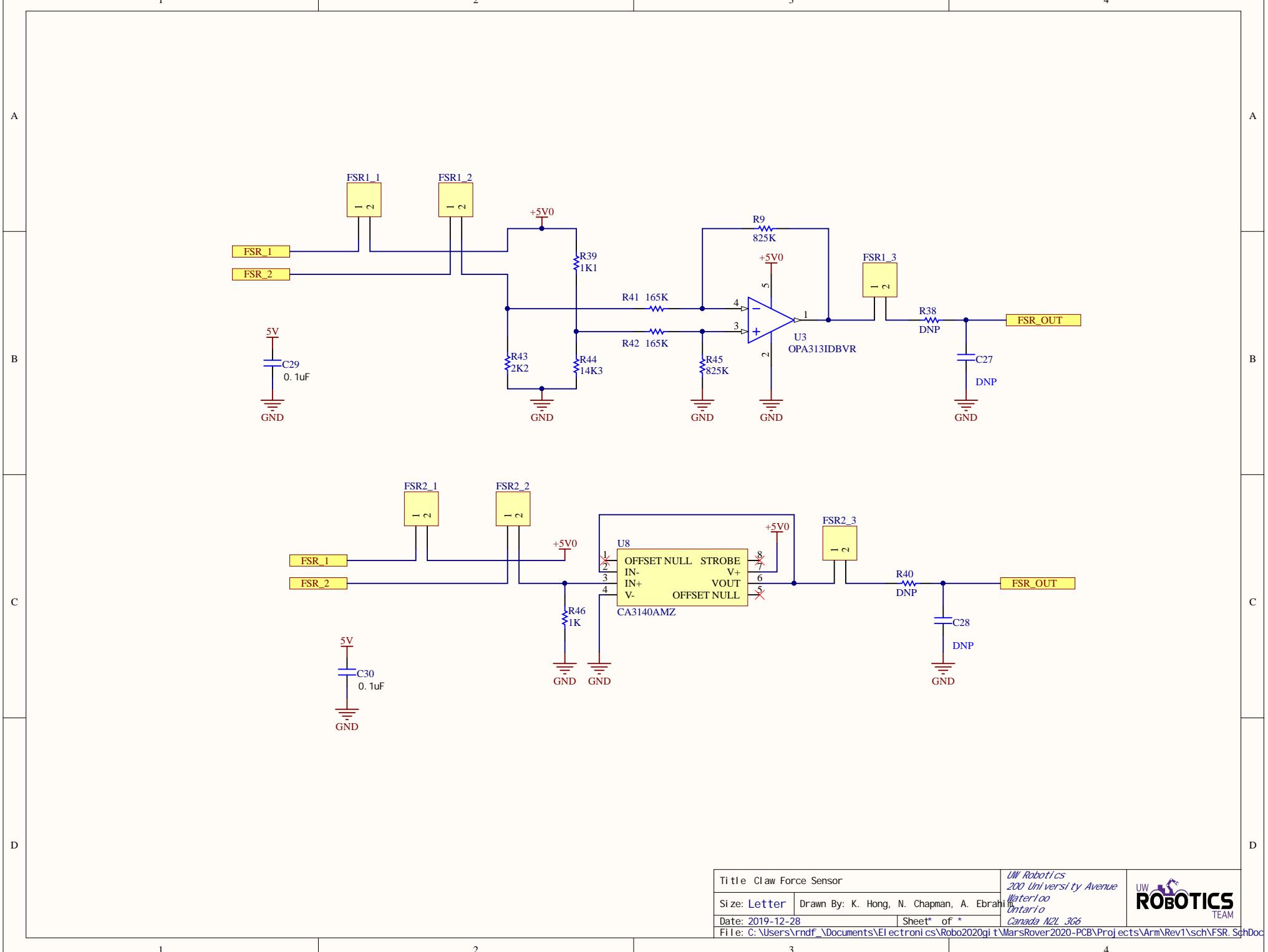
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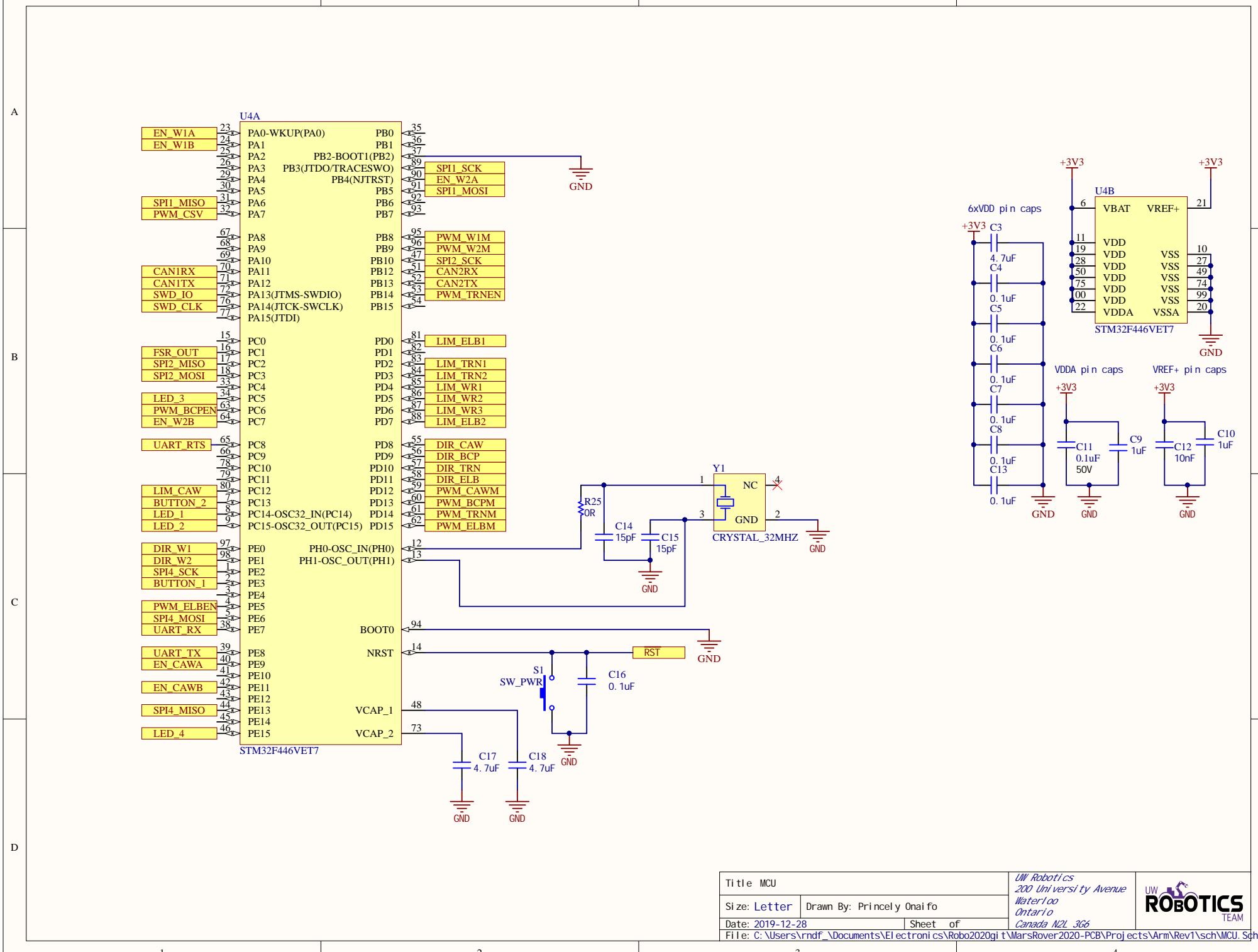
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A

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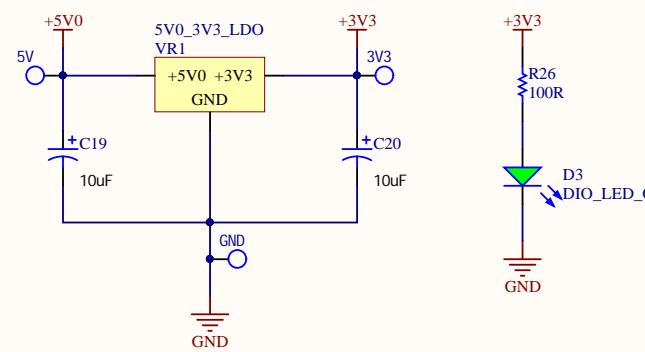
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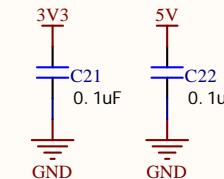
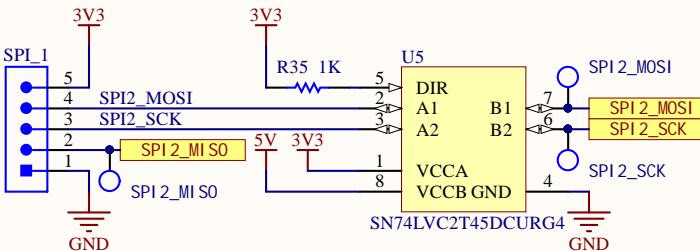
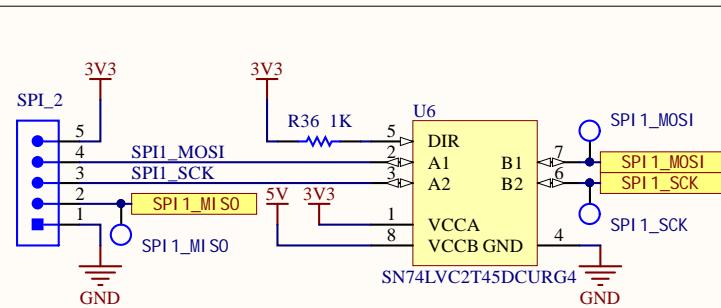
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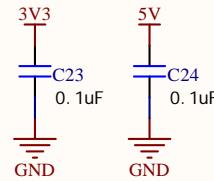


A

SPI Connectors

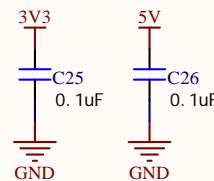
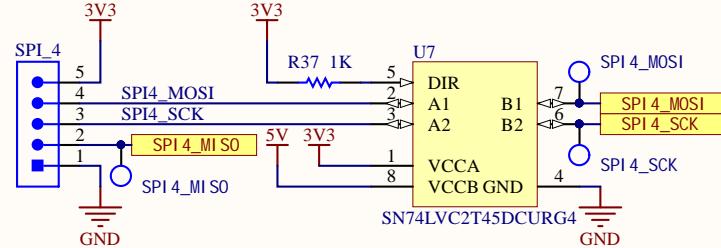


B



Used level shifter because MCU SPI connectors are 5V tolerant and registers voltages greater than 3V3 as high. Didn't shift MISO because SPI can handle that level of input.

C



D

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