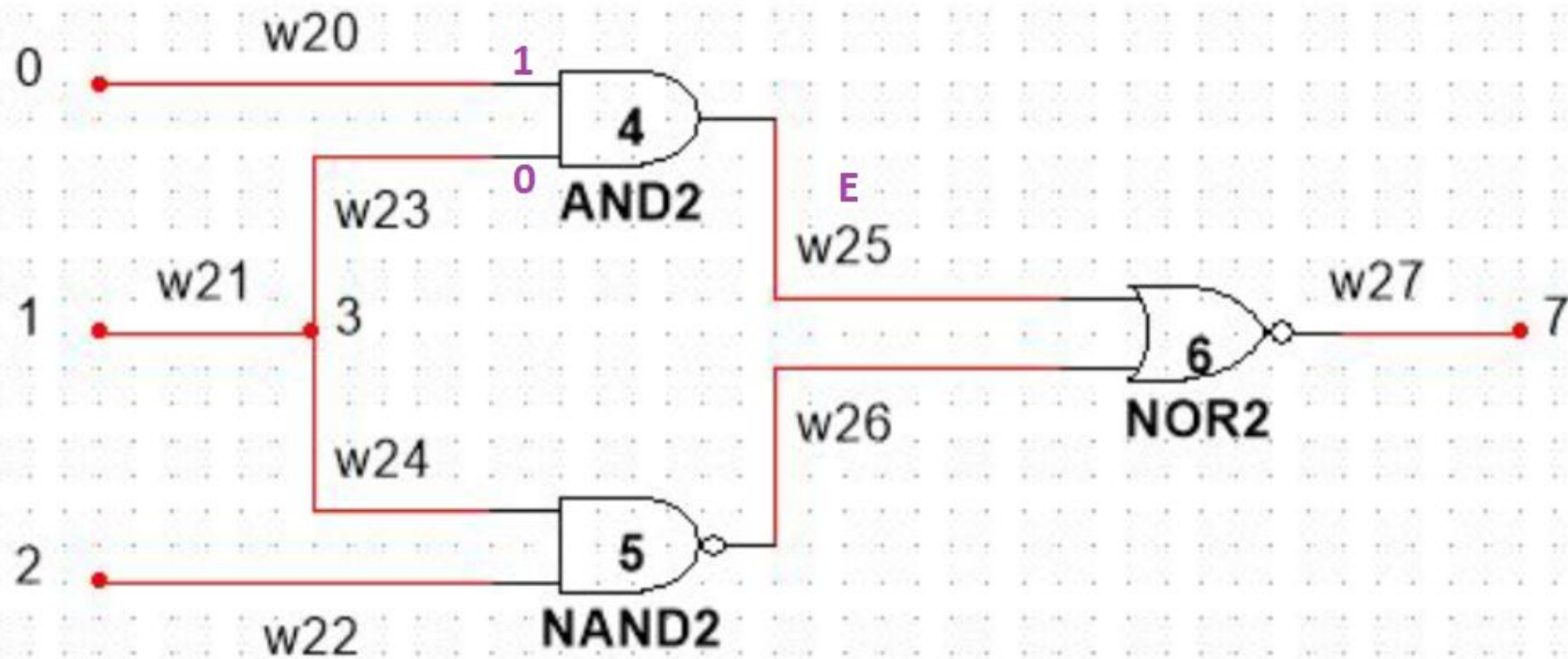
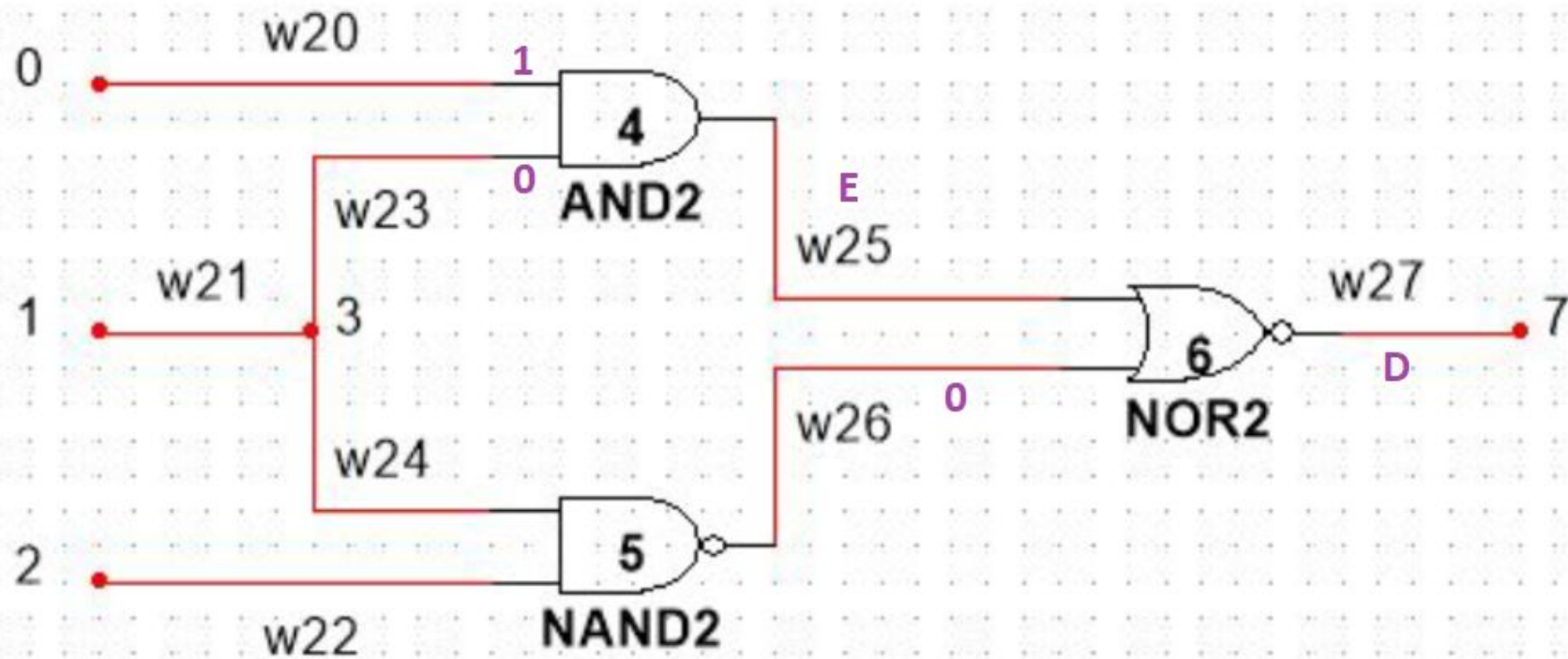
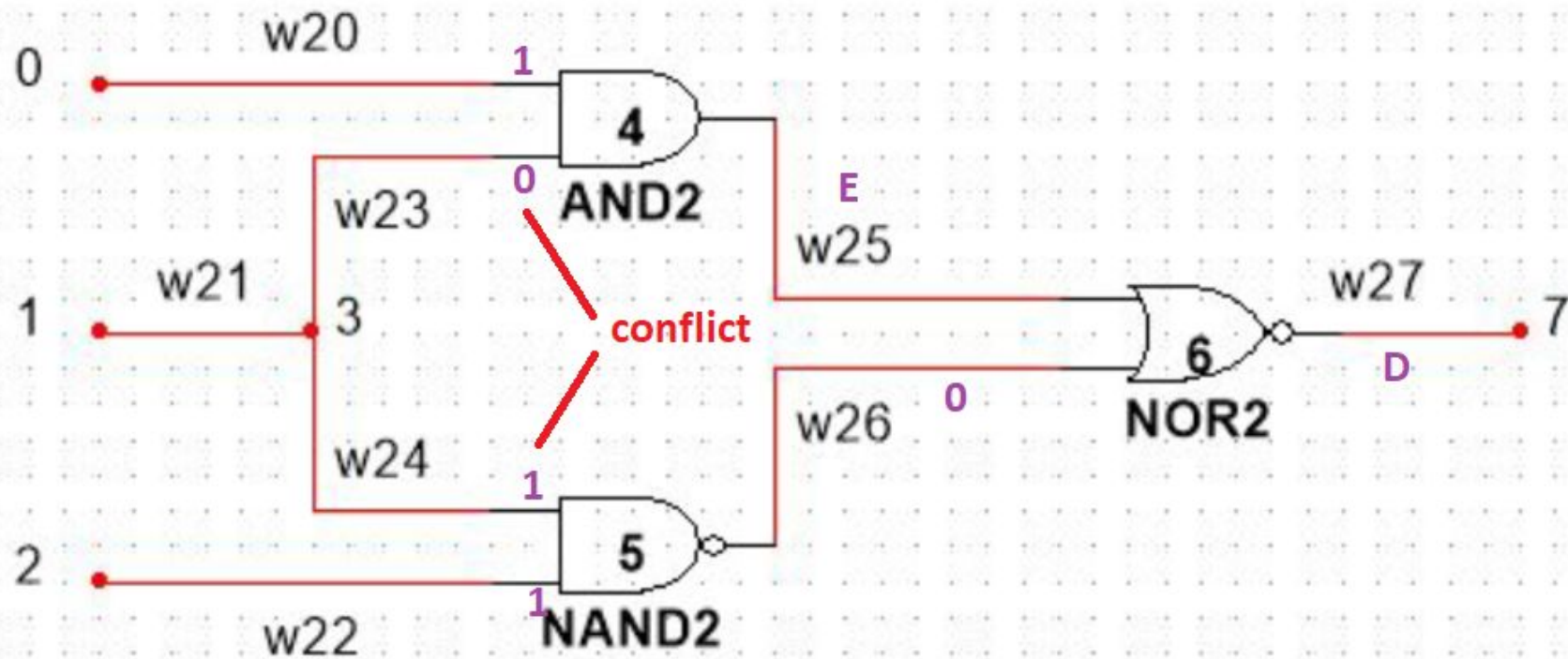


Circuit

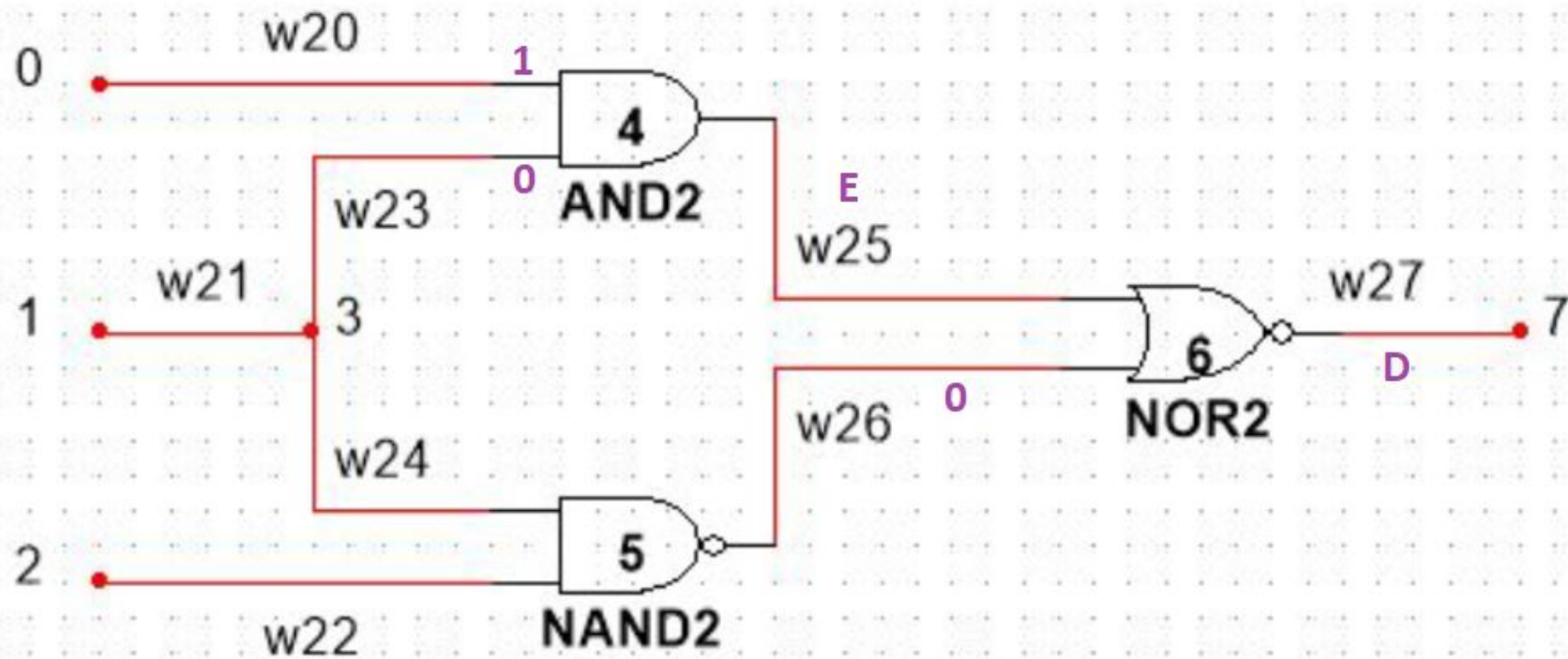


```
char gate_and_pdf[3][3] = {
    {'1', '1', 'D'},
    {'0', 'x', 'E'},
    {'x', '0', 'E'}
};
```

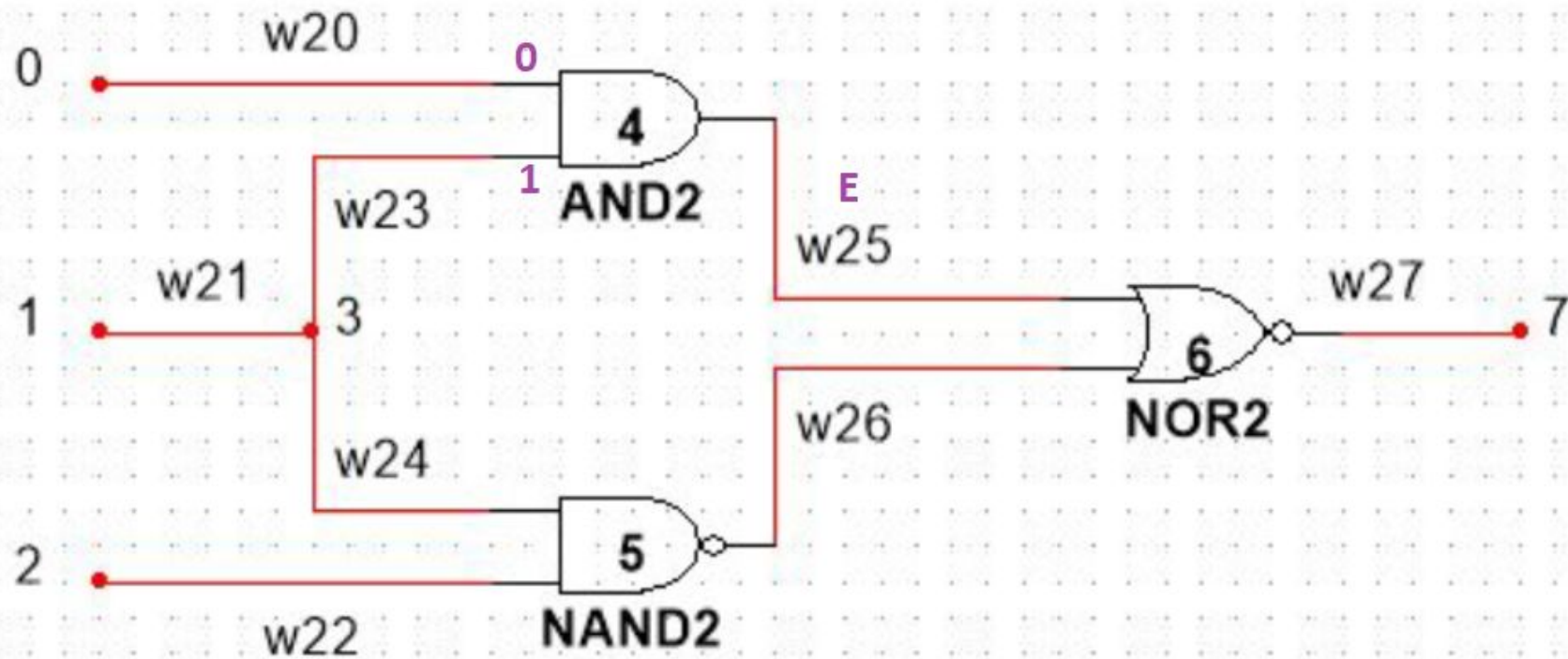




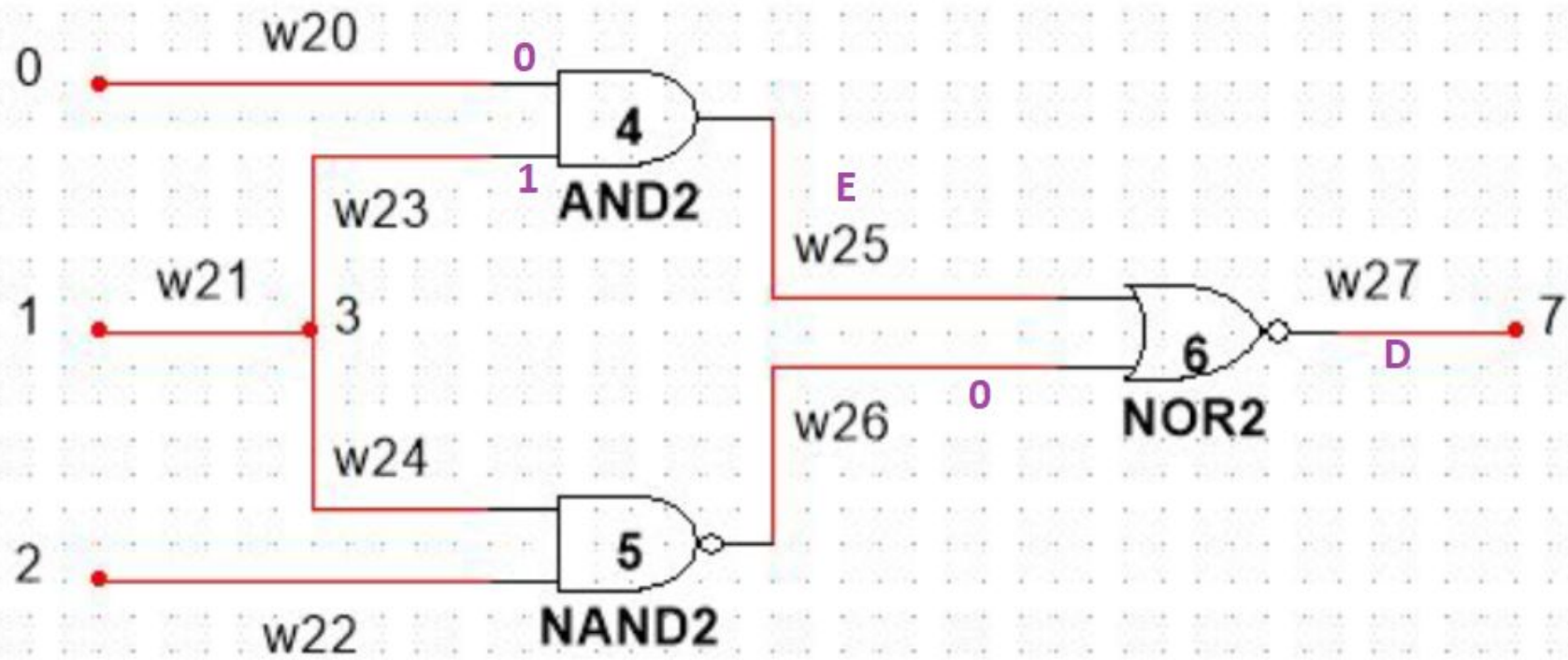
Justify NAND2_5 - Conflict Occurs

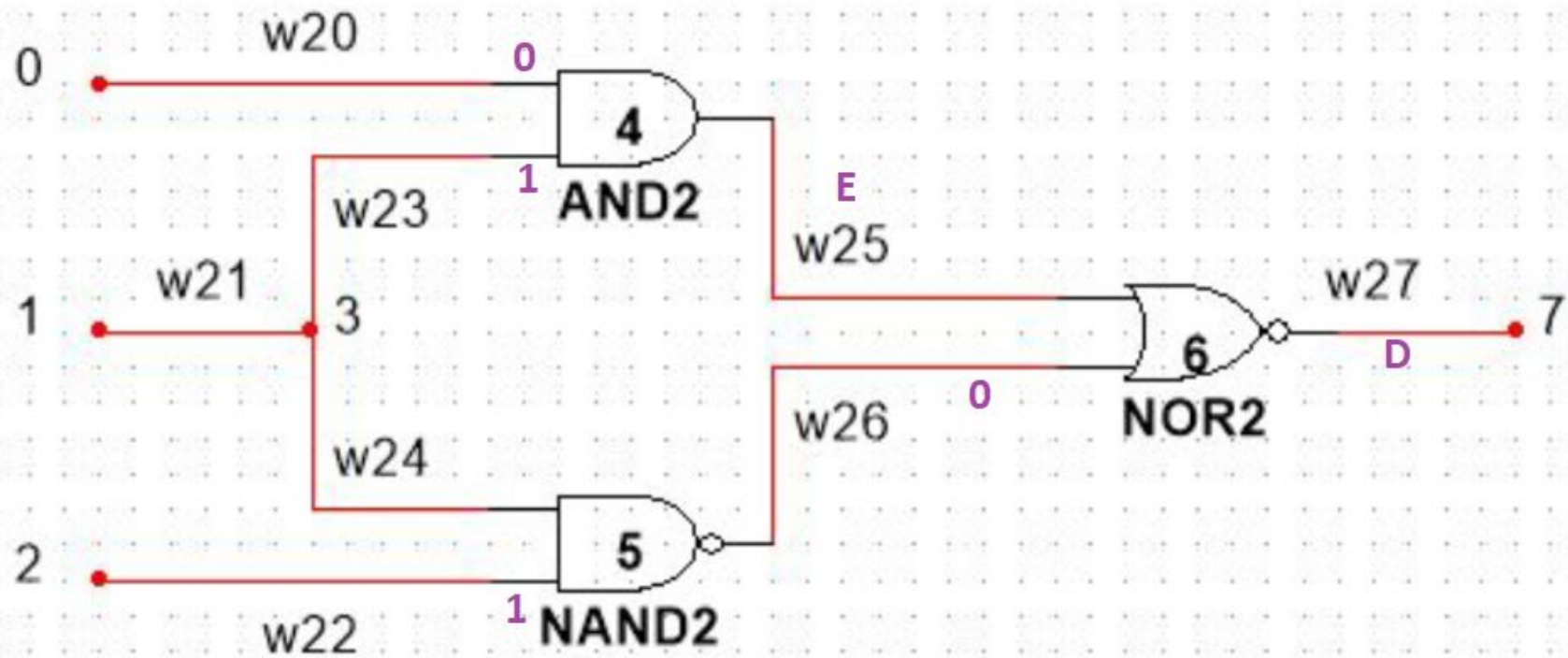


Backtrack to last decision - D-Drive, no other decisions available



Backtrack more - PDF has more decisions





Justify NAND2_5 - Done