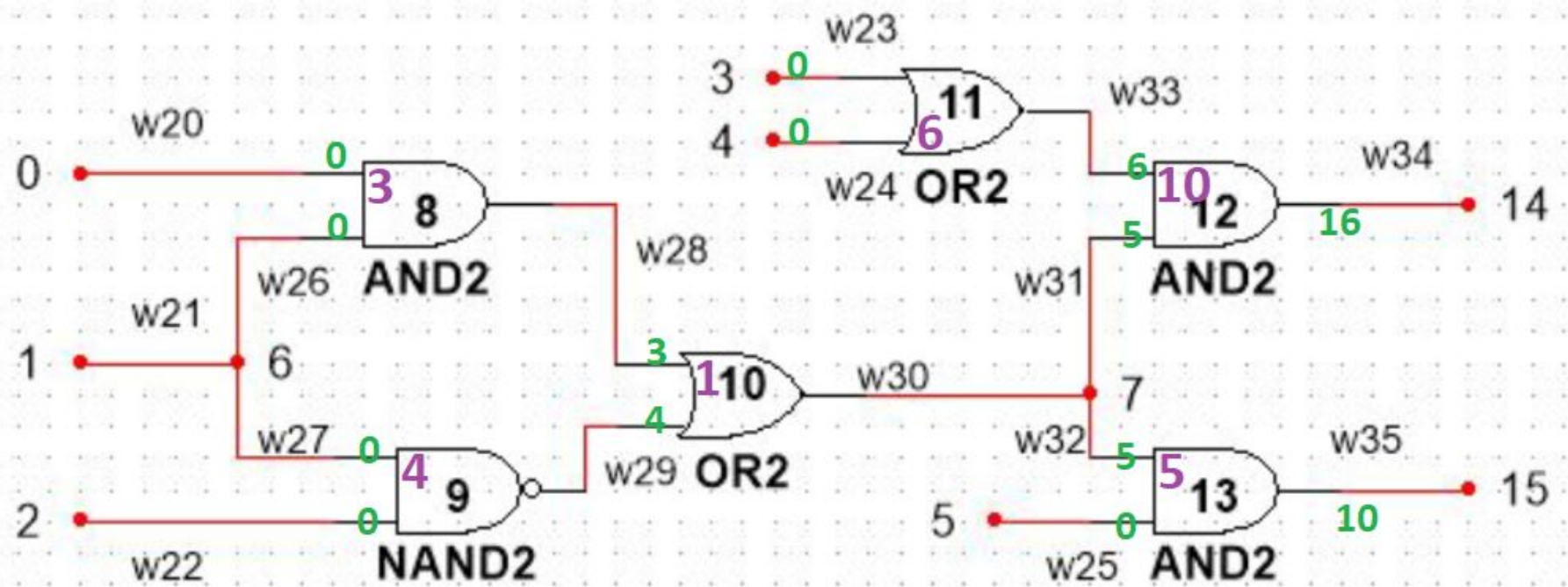
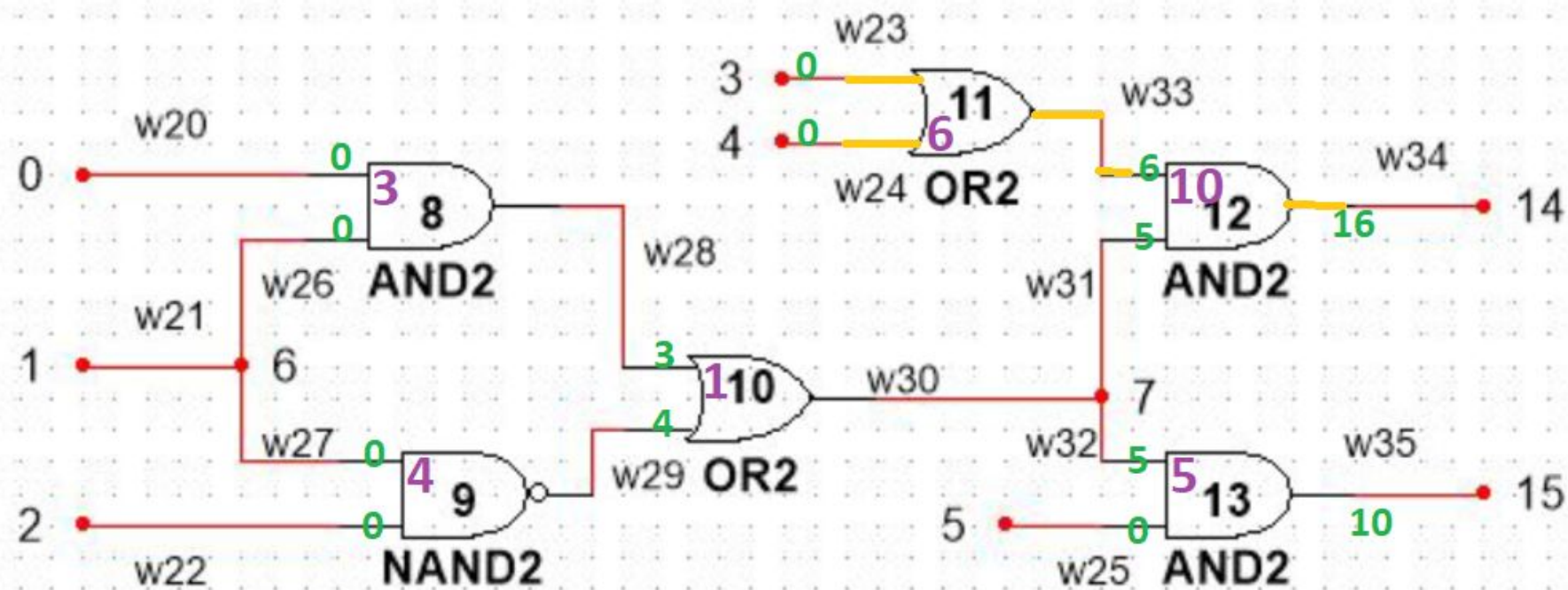


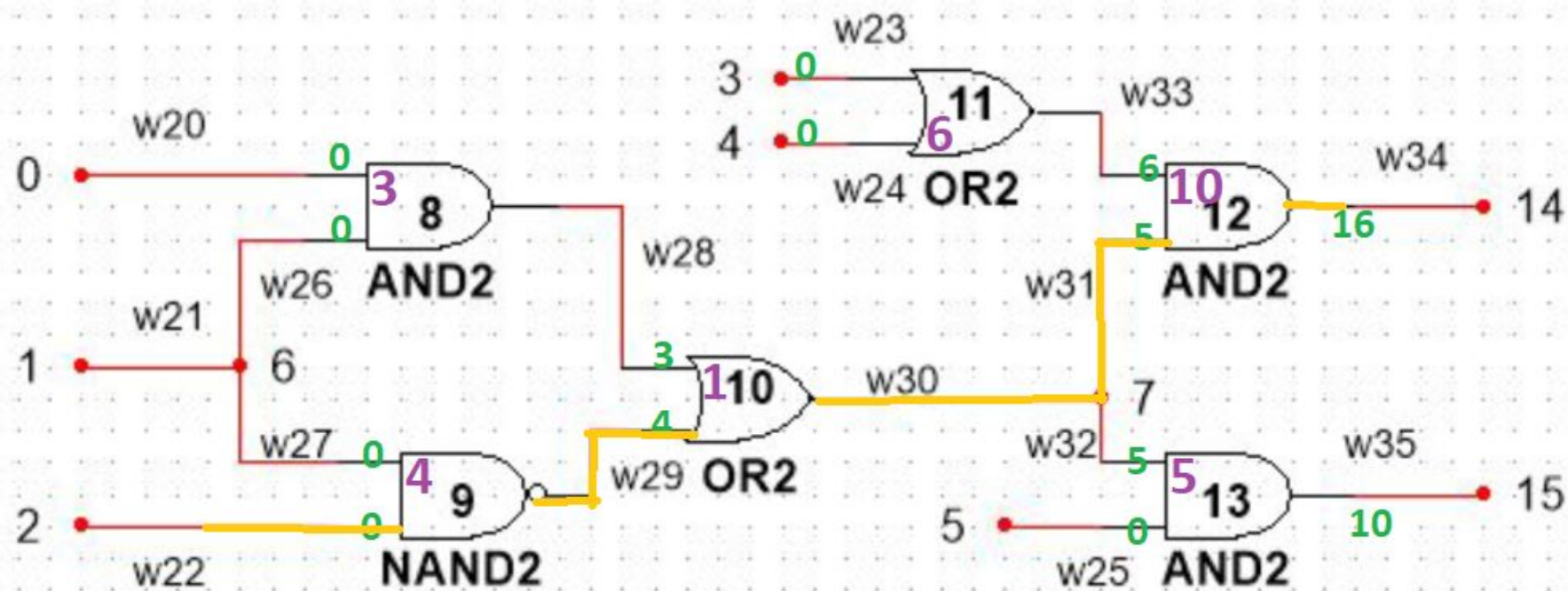
Circuit



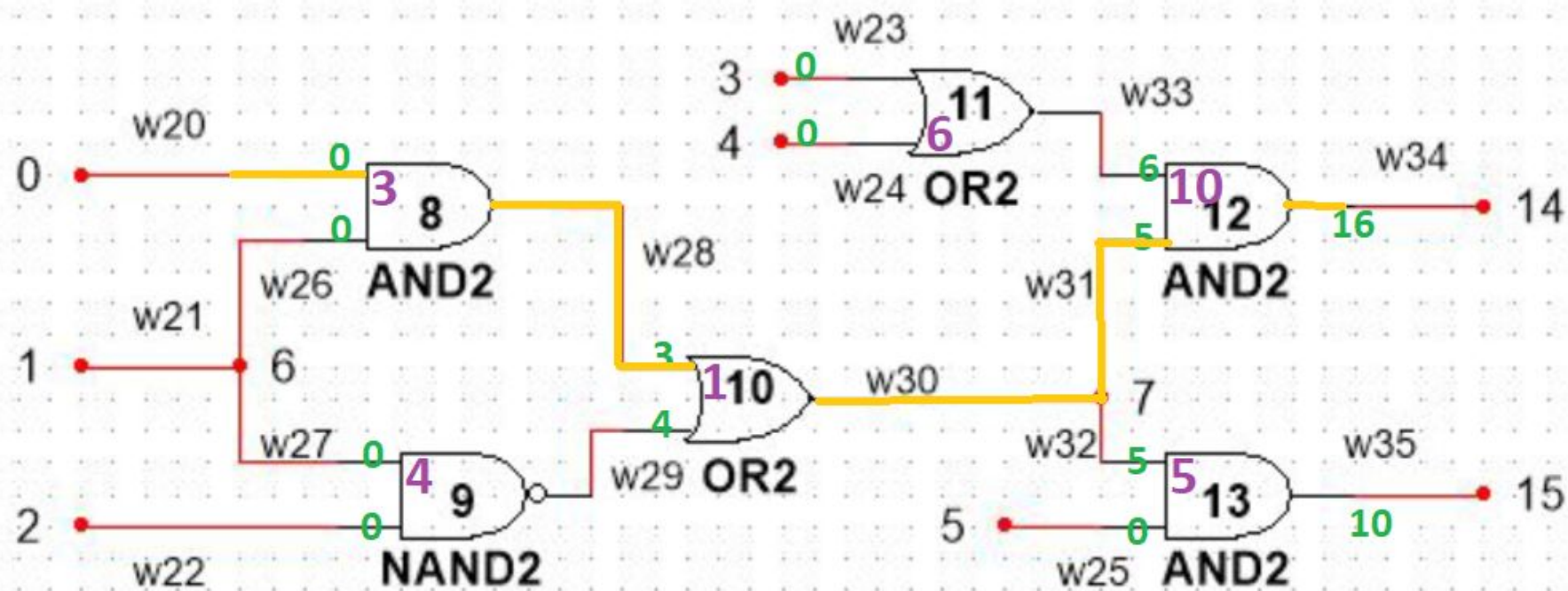
Static Timing Analysis



First Critical Path



Second Critical Path



Third Critical Path