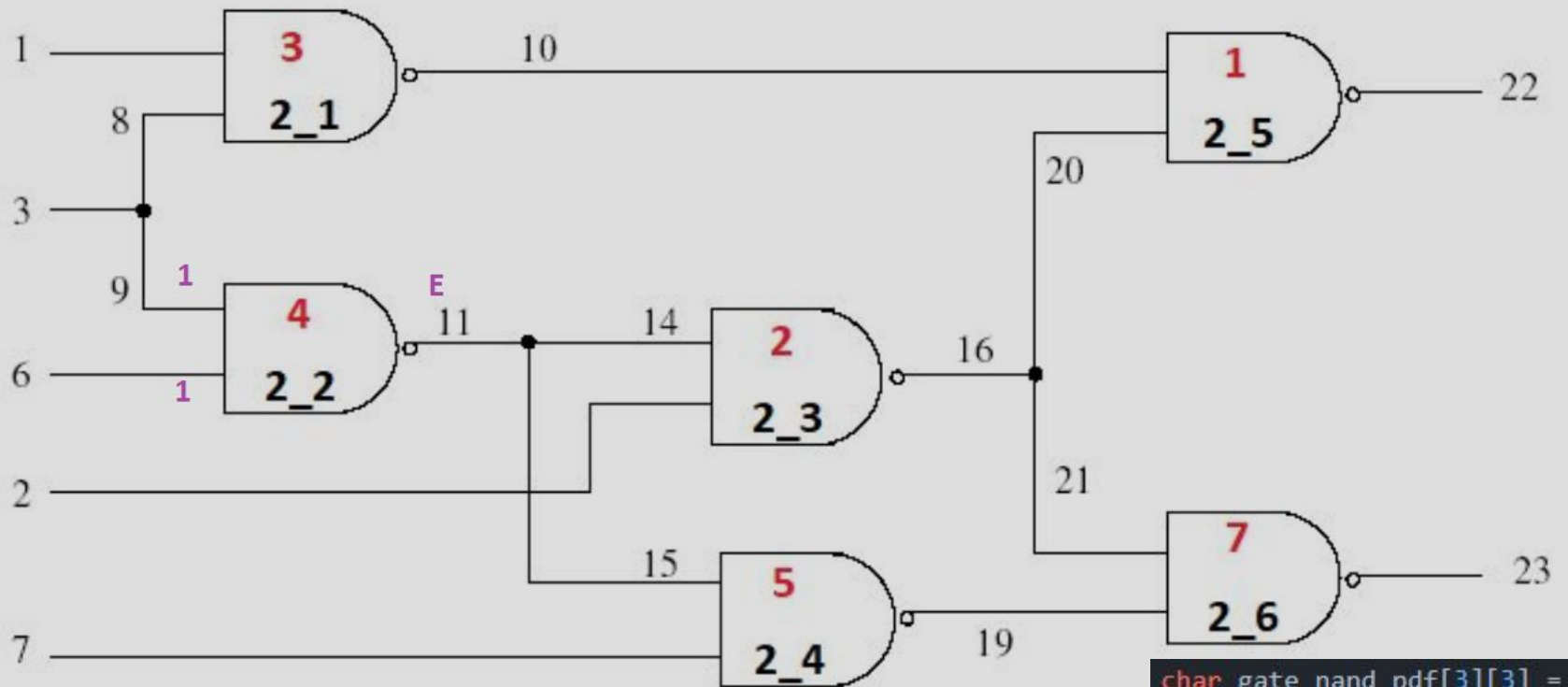
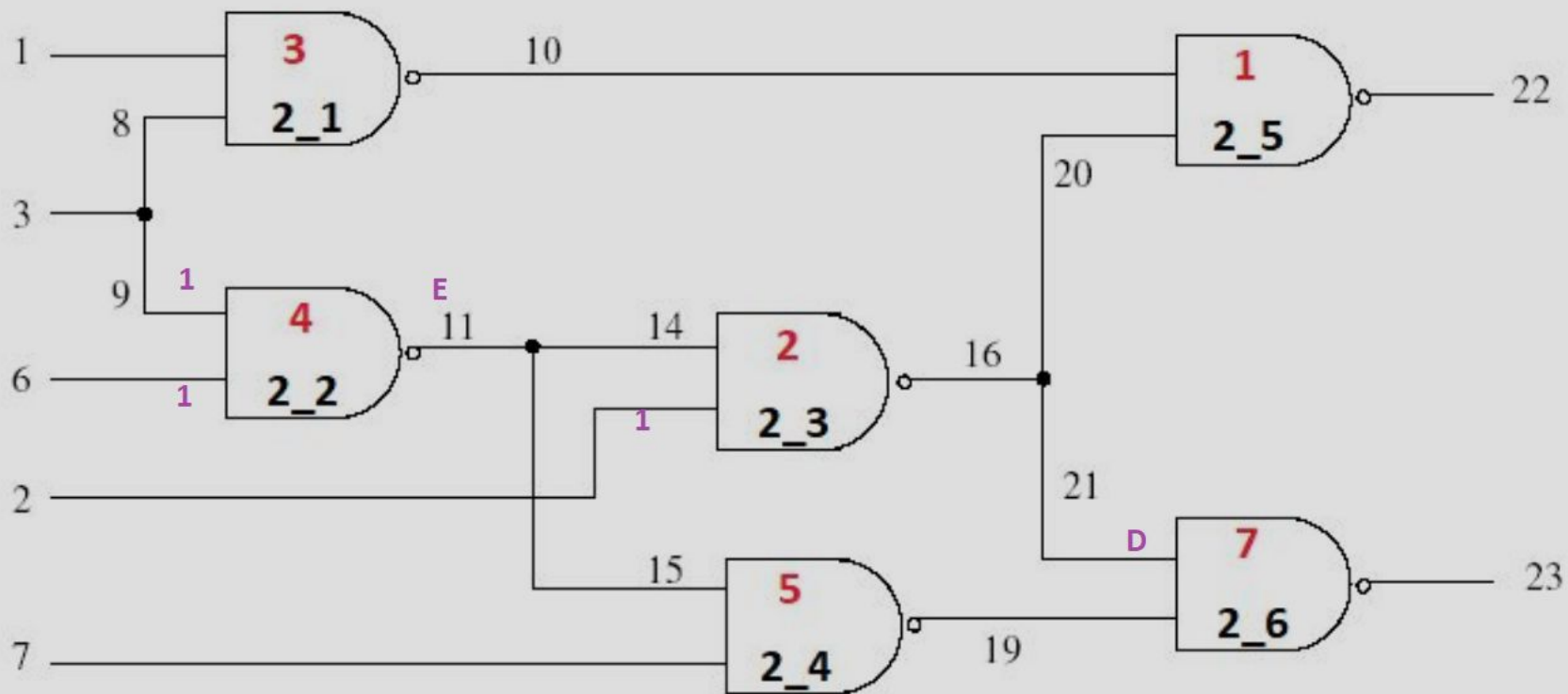


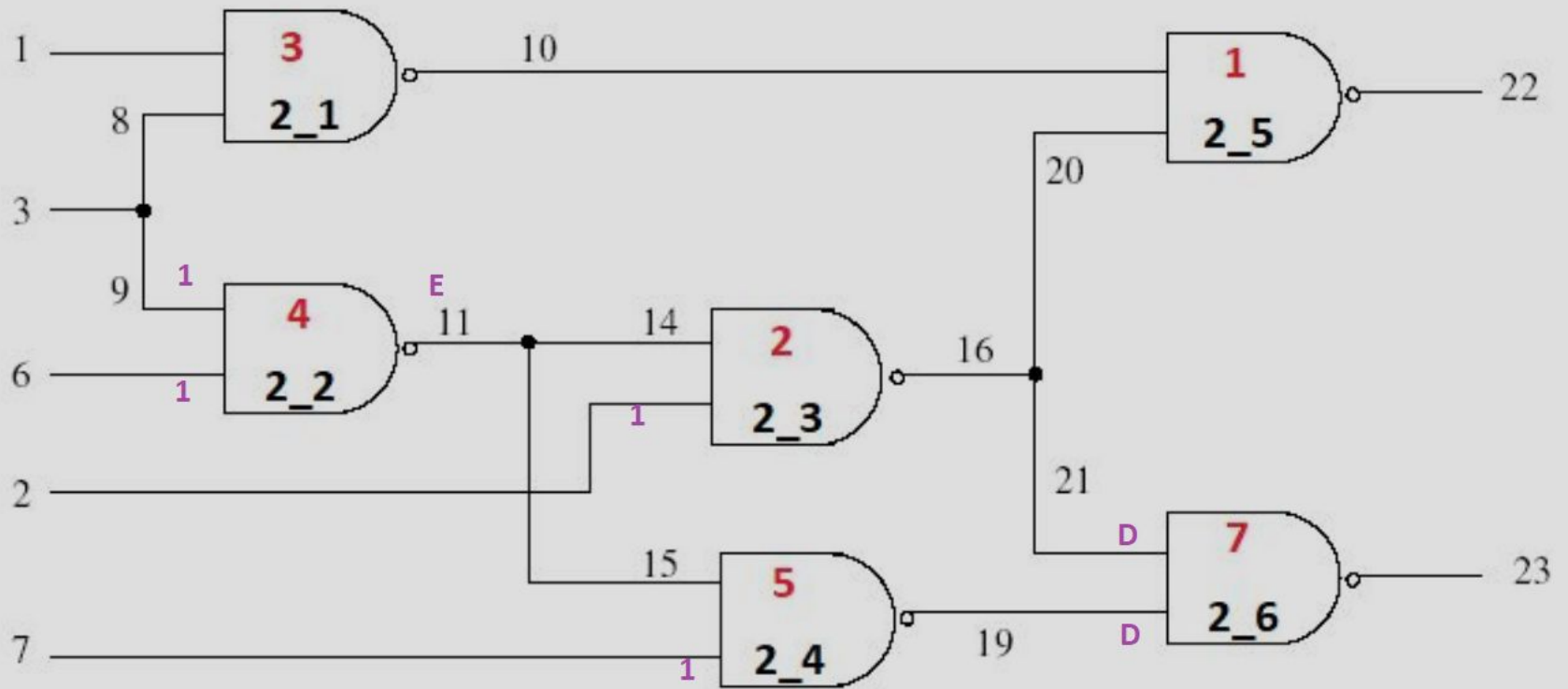
Circuit



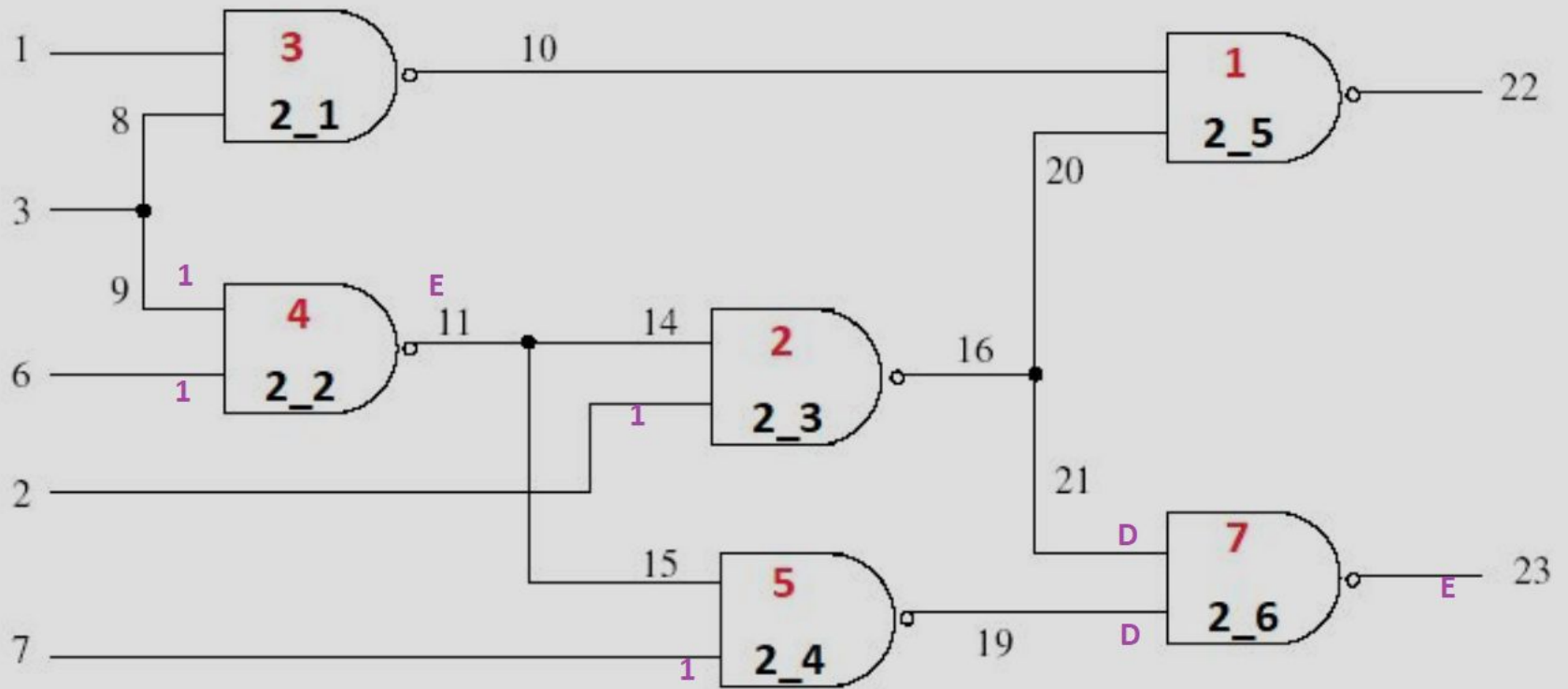
```
char gate_nand_pdf[3][3] = {
    {'1', '1', 'E'},
    {'0', 'x', 'D'},
    {'x', '0', 'D'}
};
```



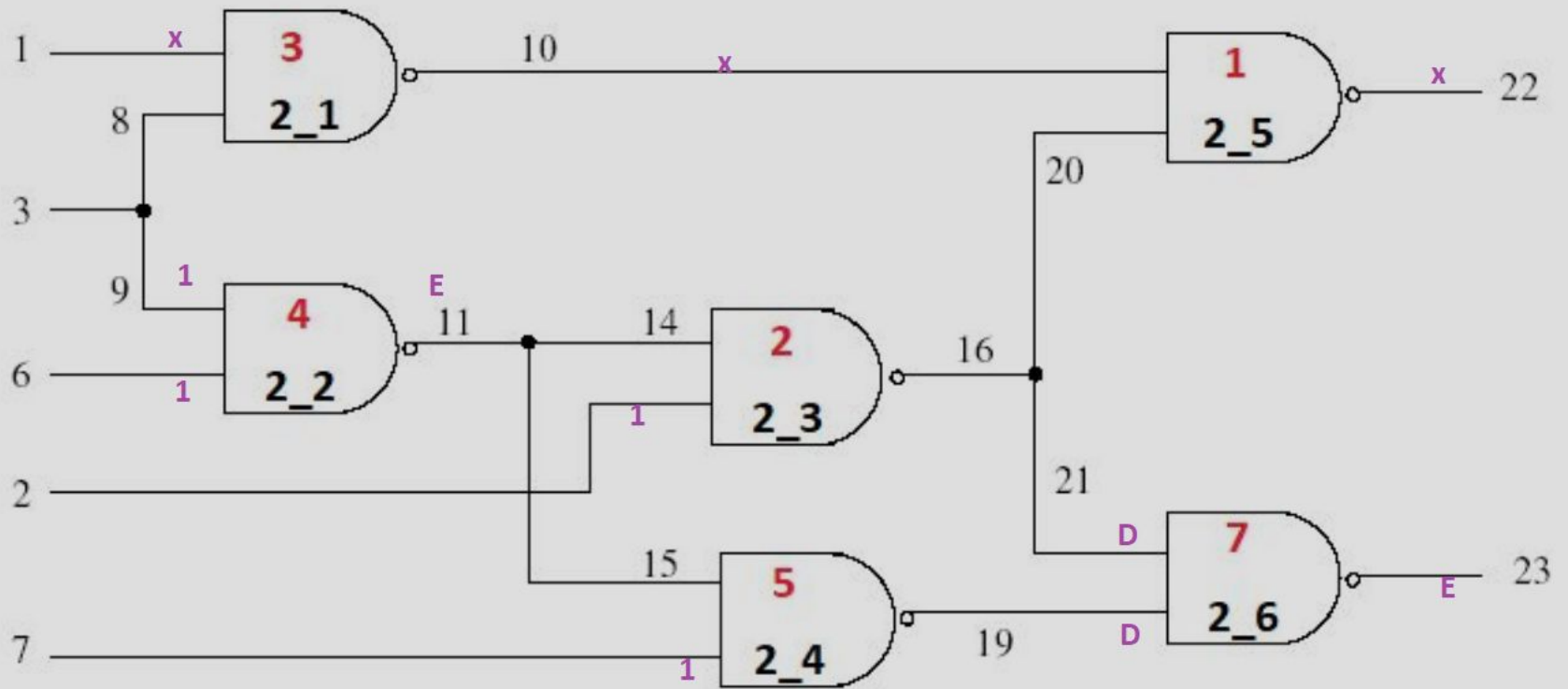
D-Drive NAND2\_3



D-Drive NAND2\_4



D-Drive NAND2\_6 - Implication Occurs



No more J-frontiers, X-Justification - Done