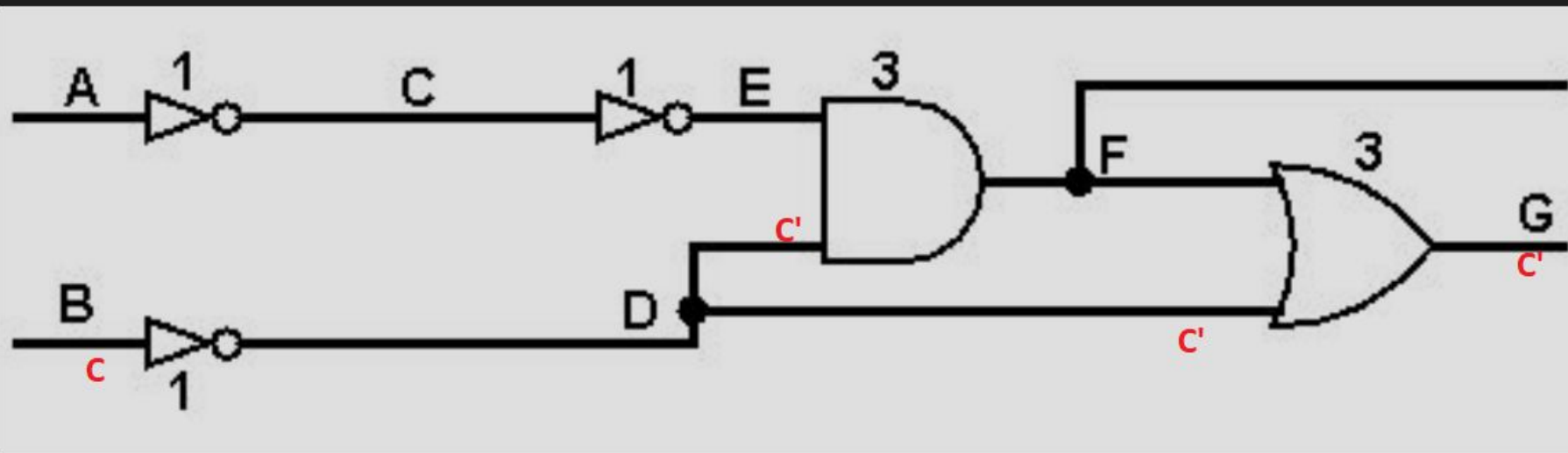
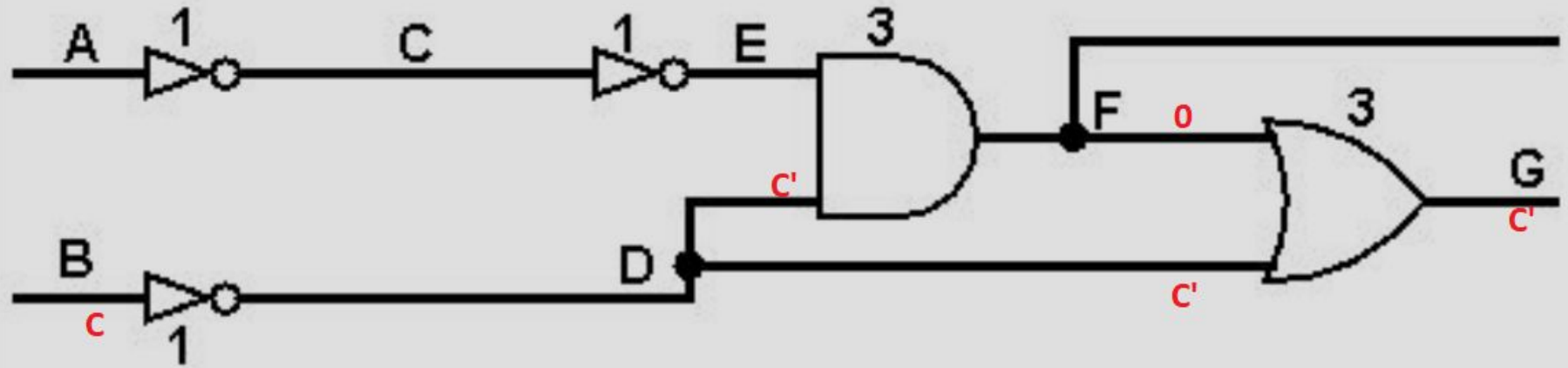


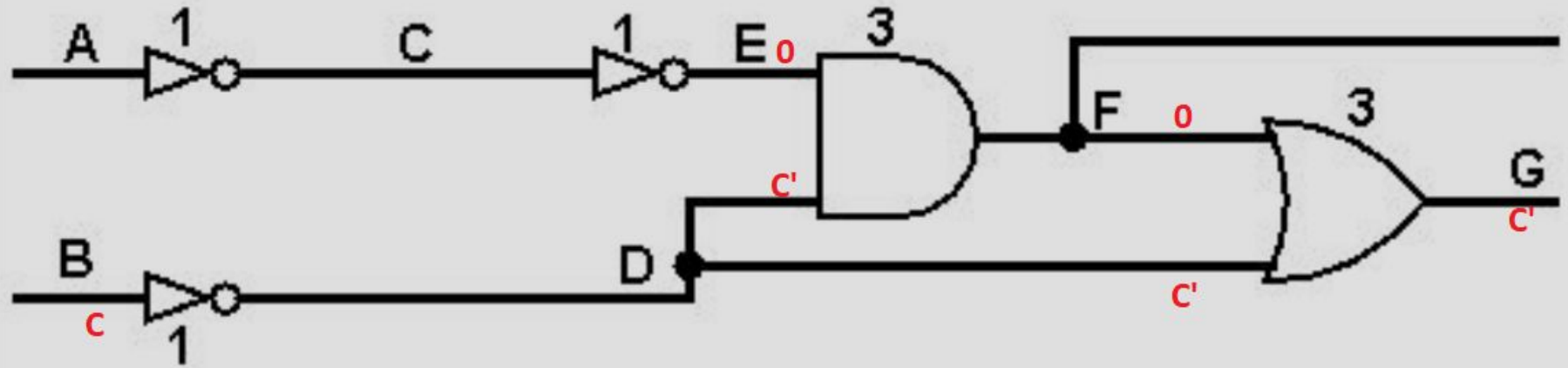
Circuit



Mark Path B OR2\_1 G



Backtrace from PO to PI on path



Justify AND2\_1 - Done