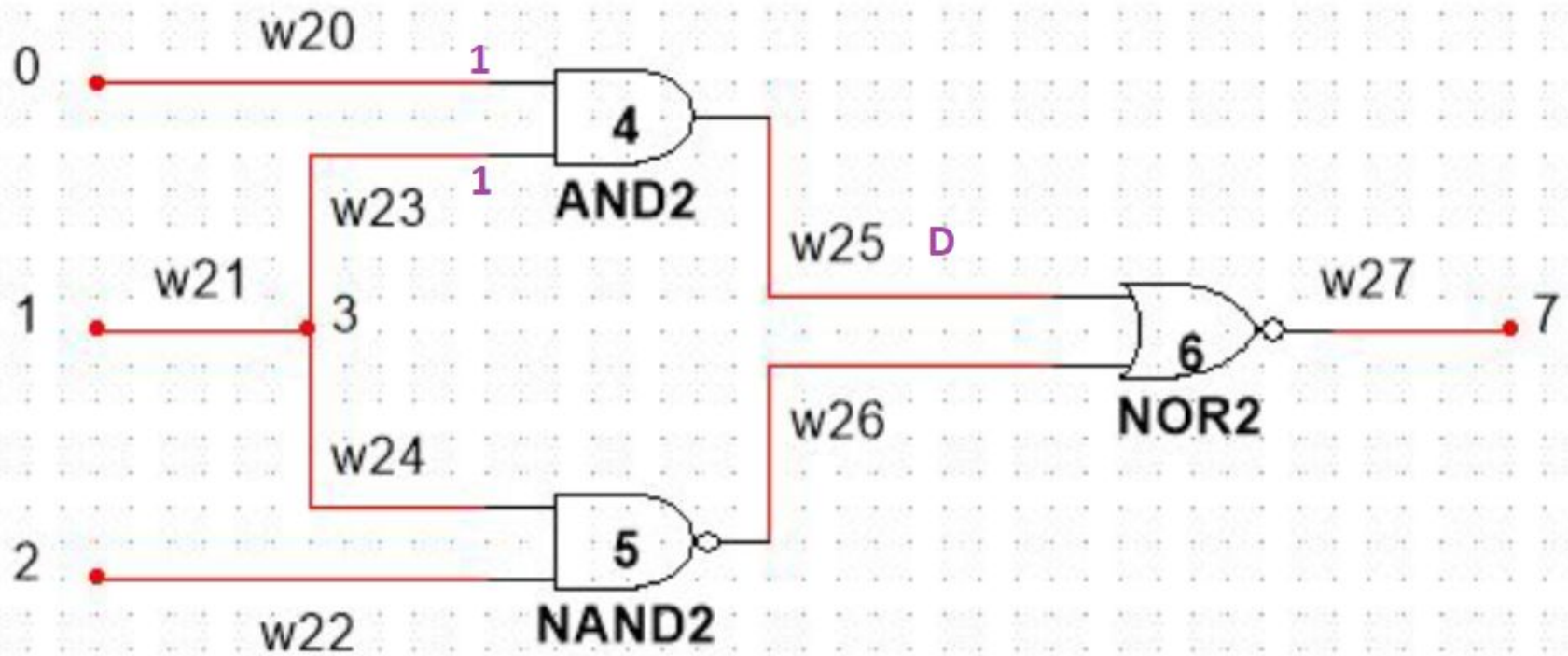
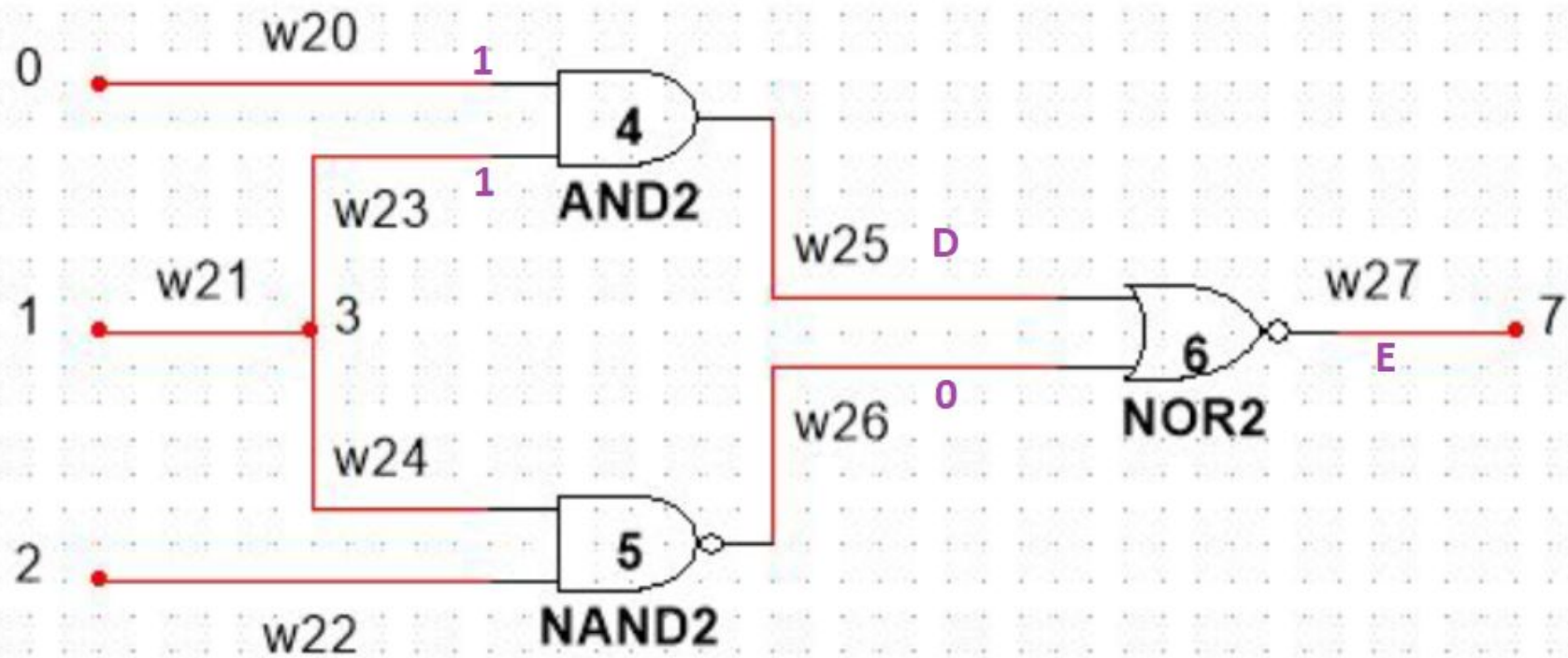
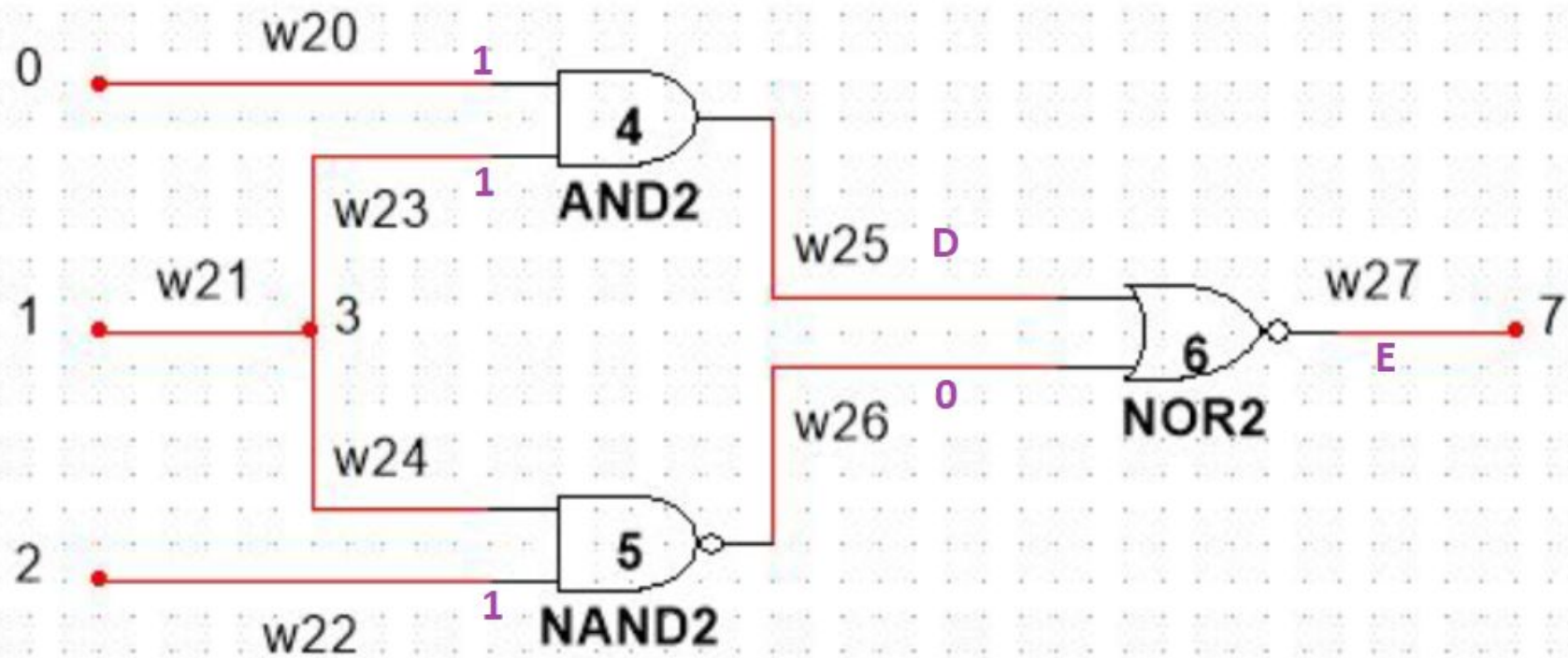


Circuit



```
char gate_and_pdf[3][3] = {
    {'1', '1', 'D'},
    {'0', 'x', 'E'},
    {'x', '0', 'E'}
};
```





Justify NAND2\_5 - Done