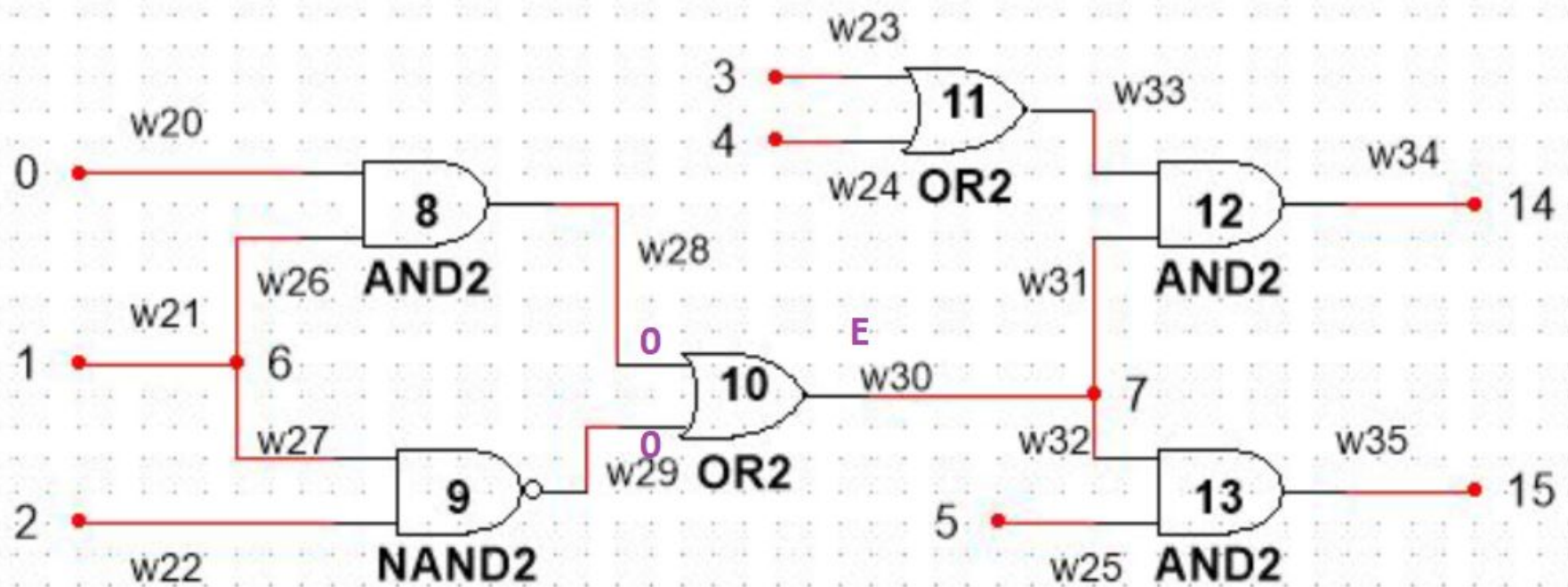
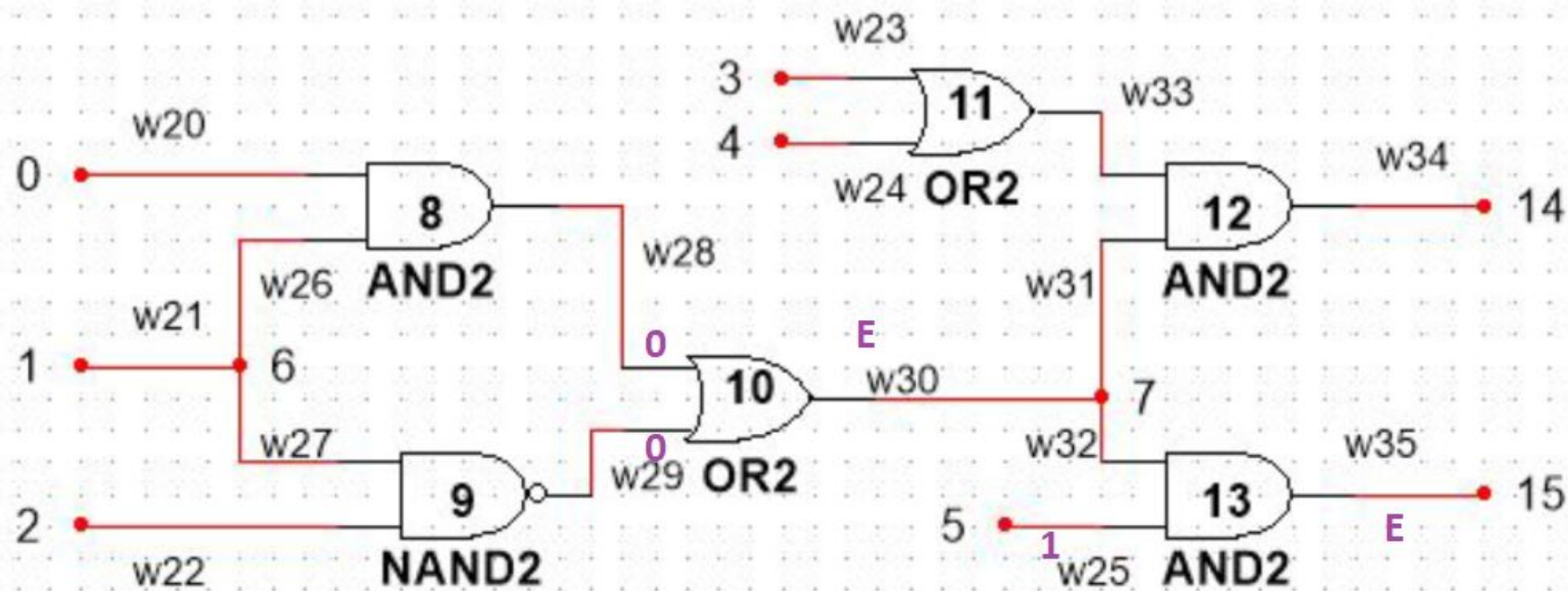


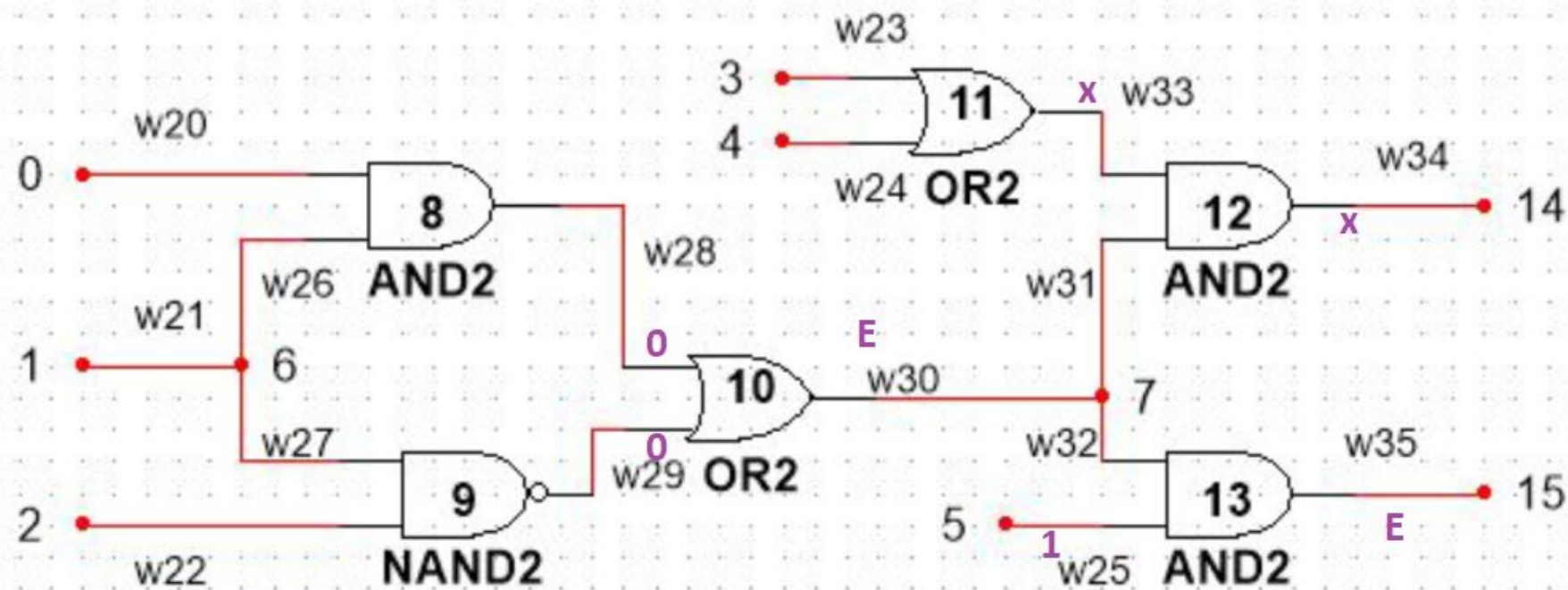
Circuit



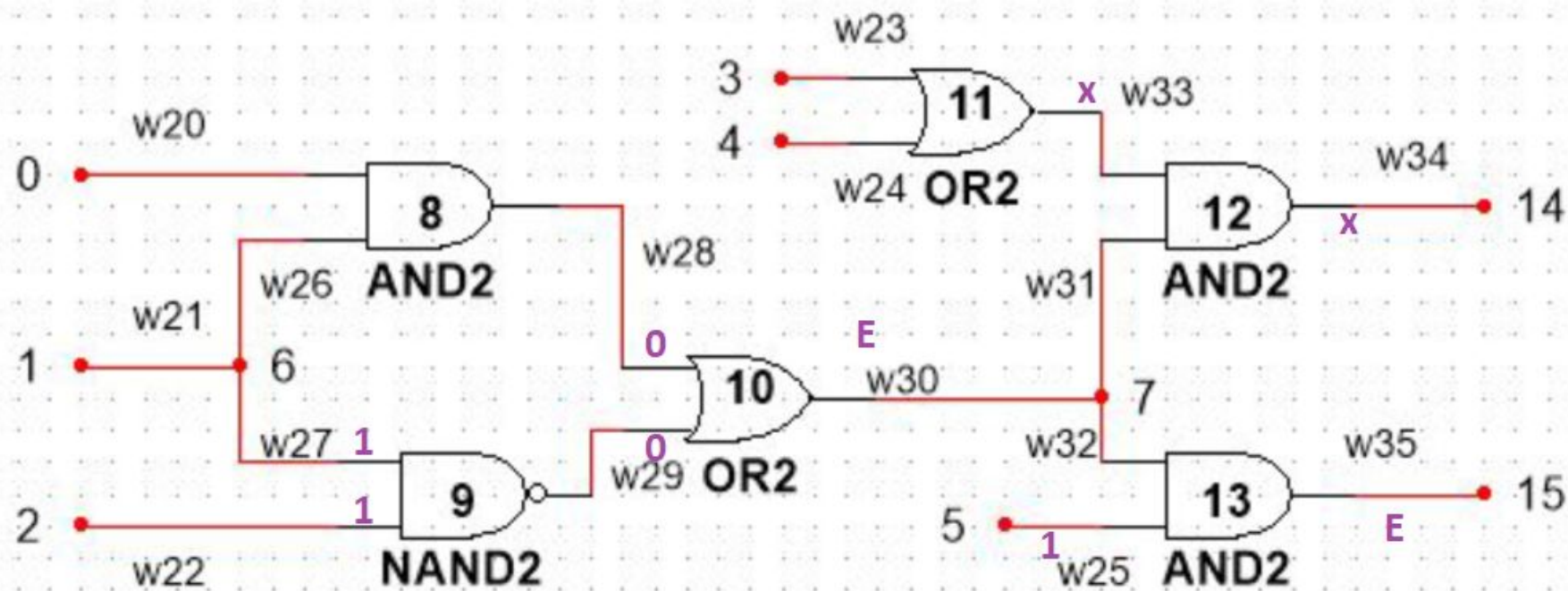
```
char gate_or_pdf[3][3] = {
    {'0', '0', 'E'},
    {'x', '1', 'D'},
    {'1', 'x', 'D'}
};
```

Fault Activation N30 sao

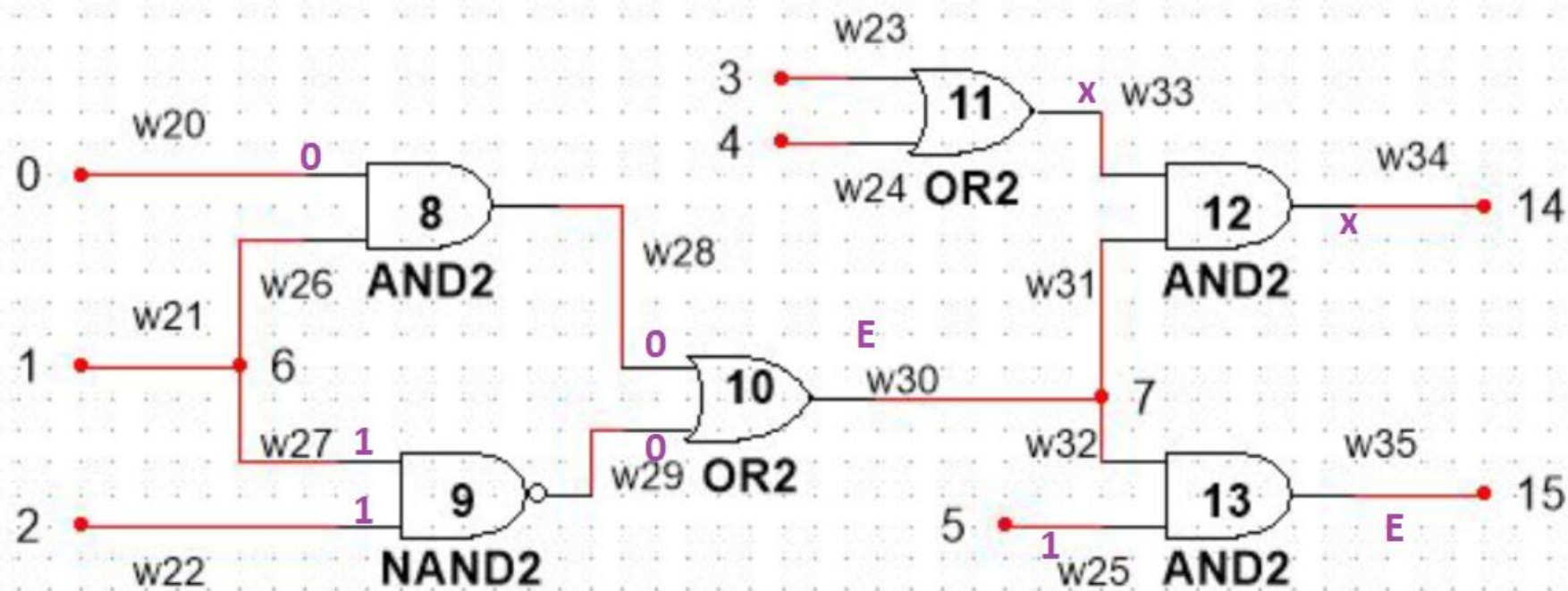




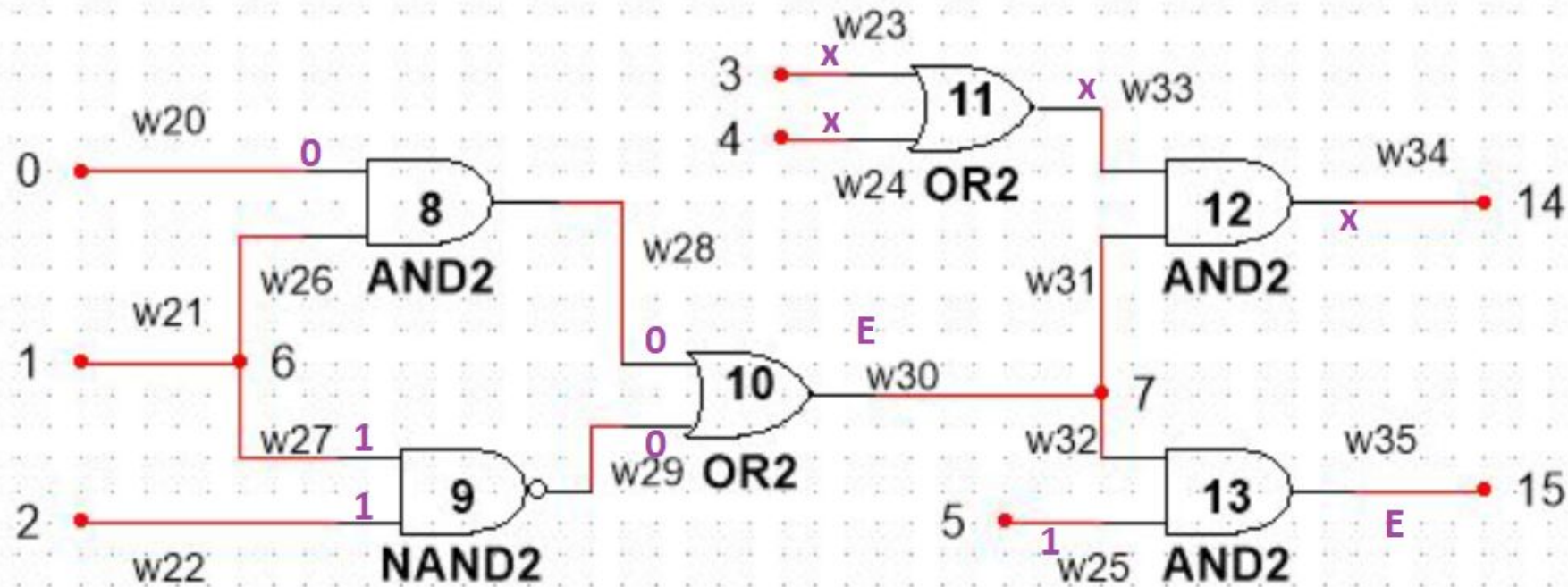
Finishing D-Drive



Justification NAND2_9



Justification AND2_8



Justification OR2_11 - Done