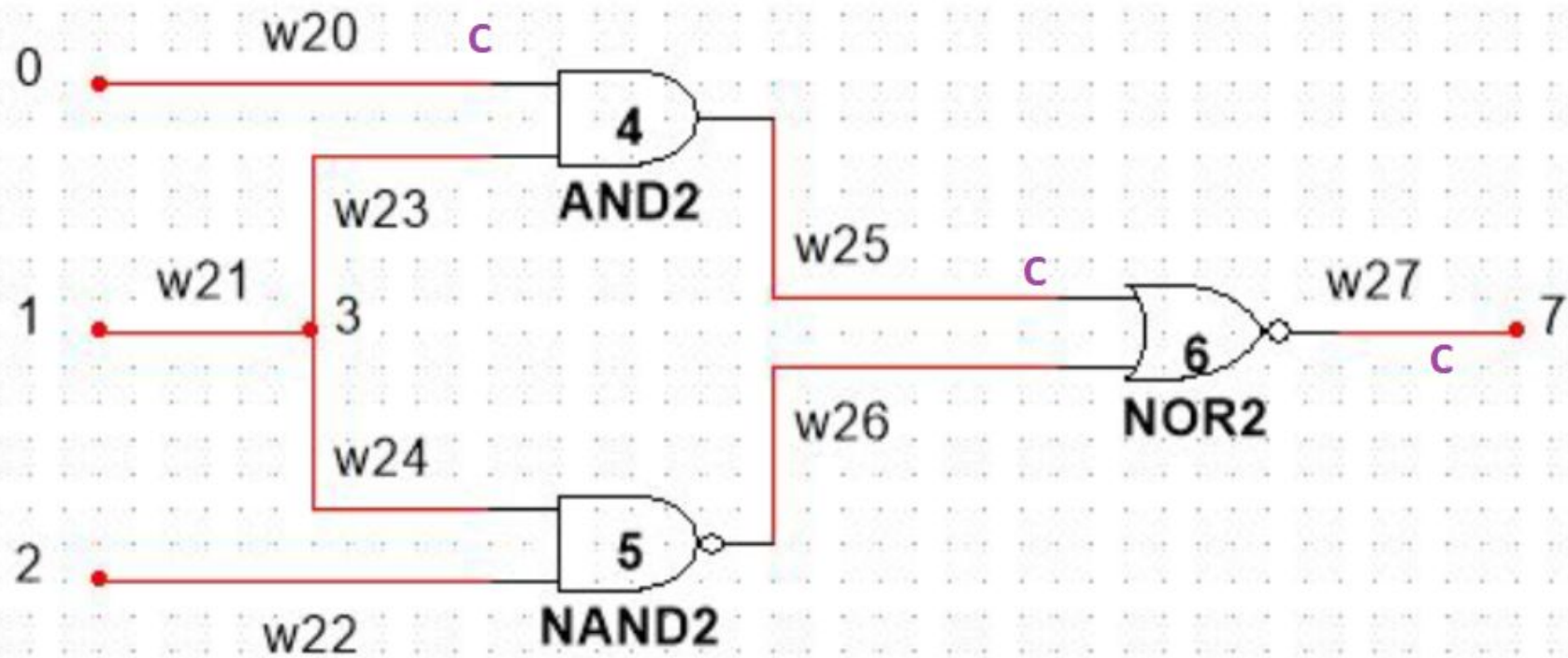
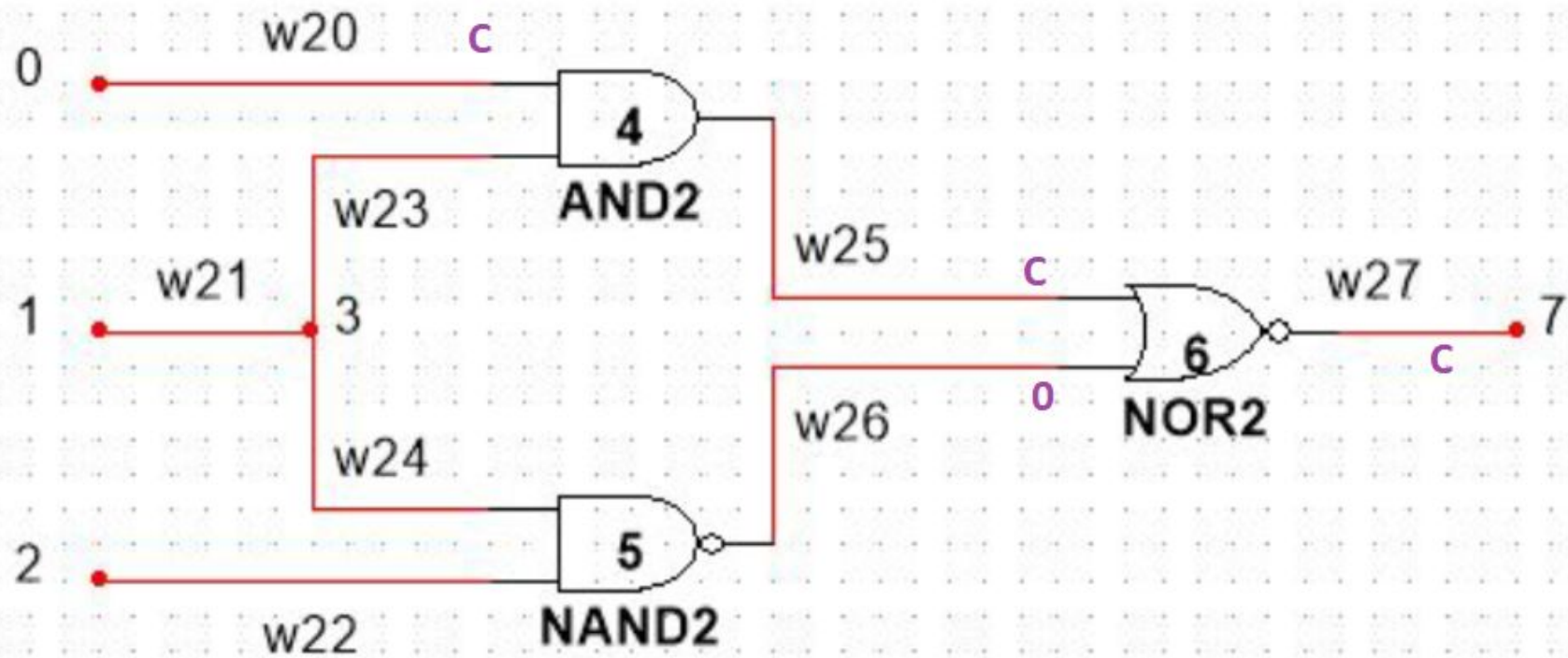


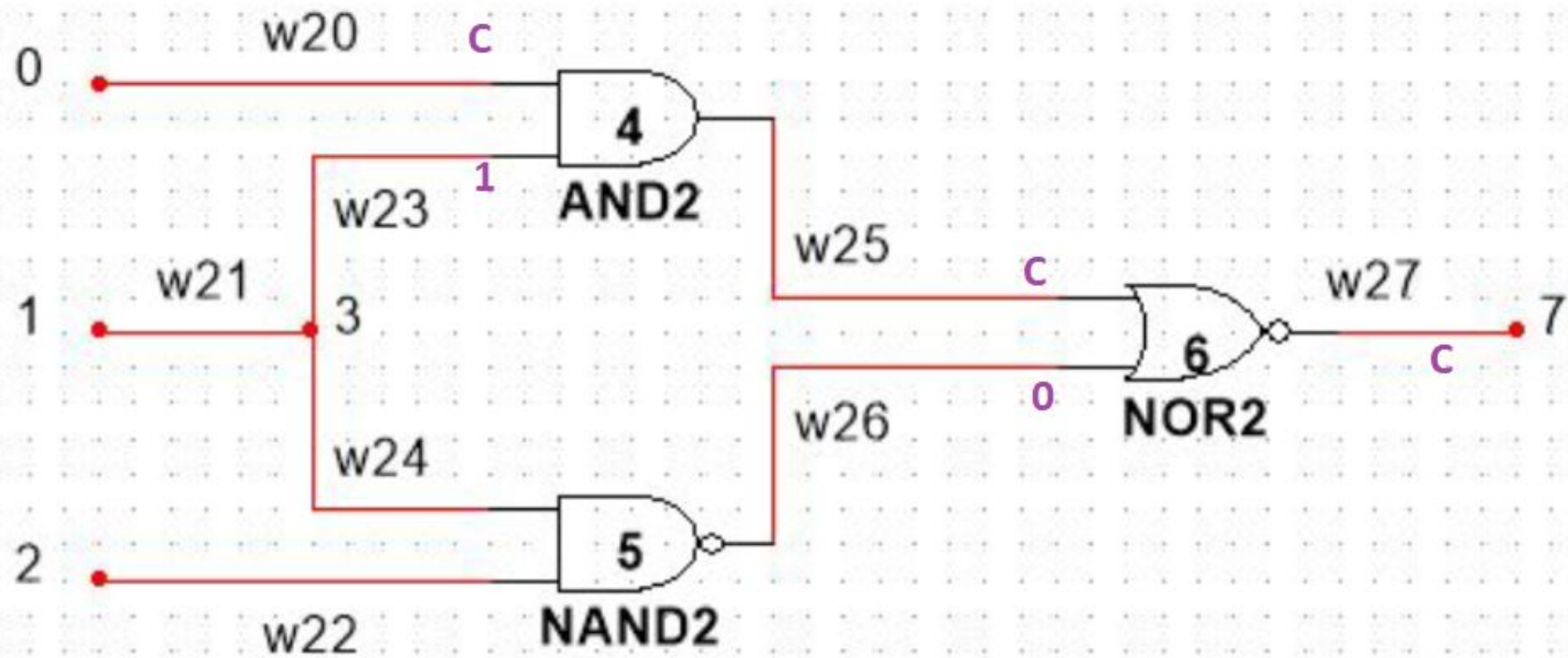
Circuit



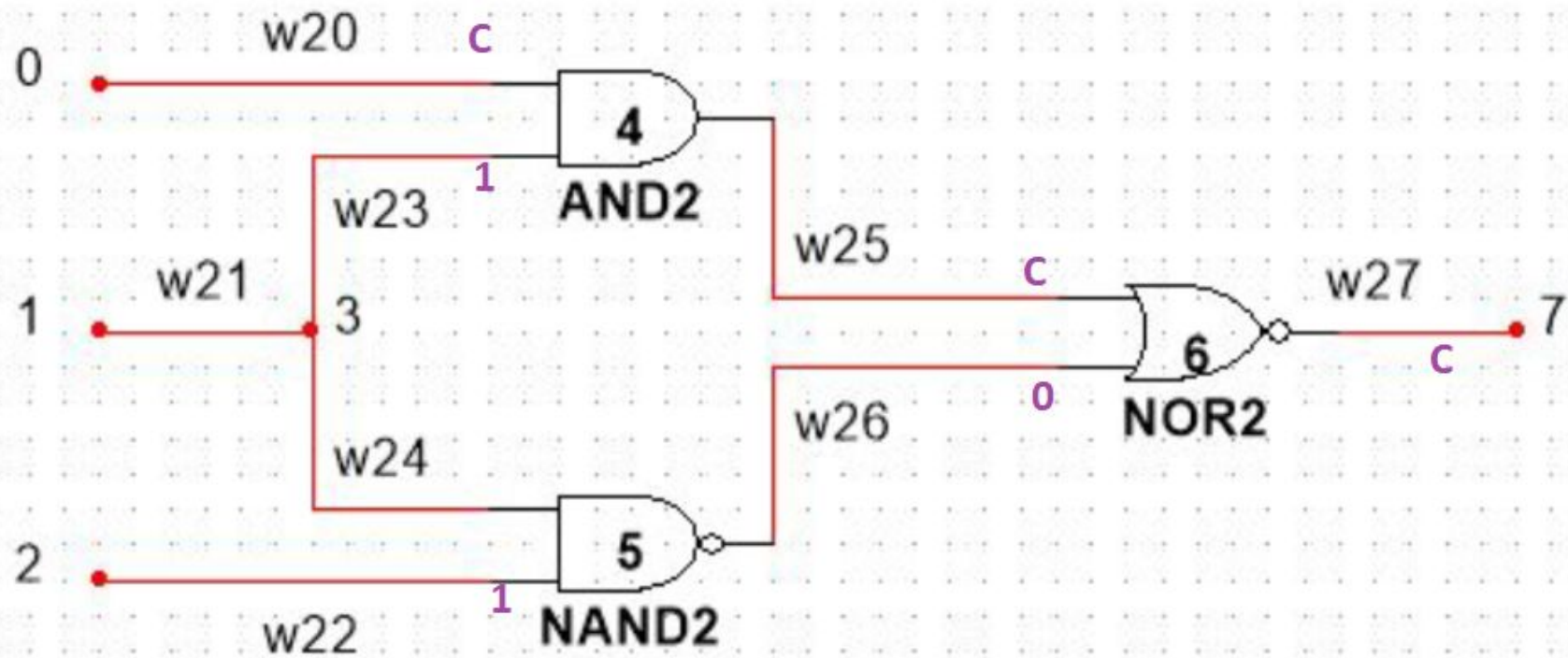
Mark path No AND2\_4 N25 NOR2\_6 N7



Backtrace from PO to PI on path



Backtrace from PO to PI on path



Justify NAND2\_5 - Done