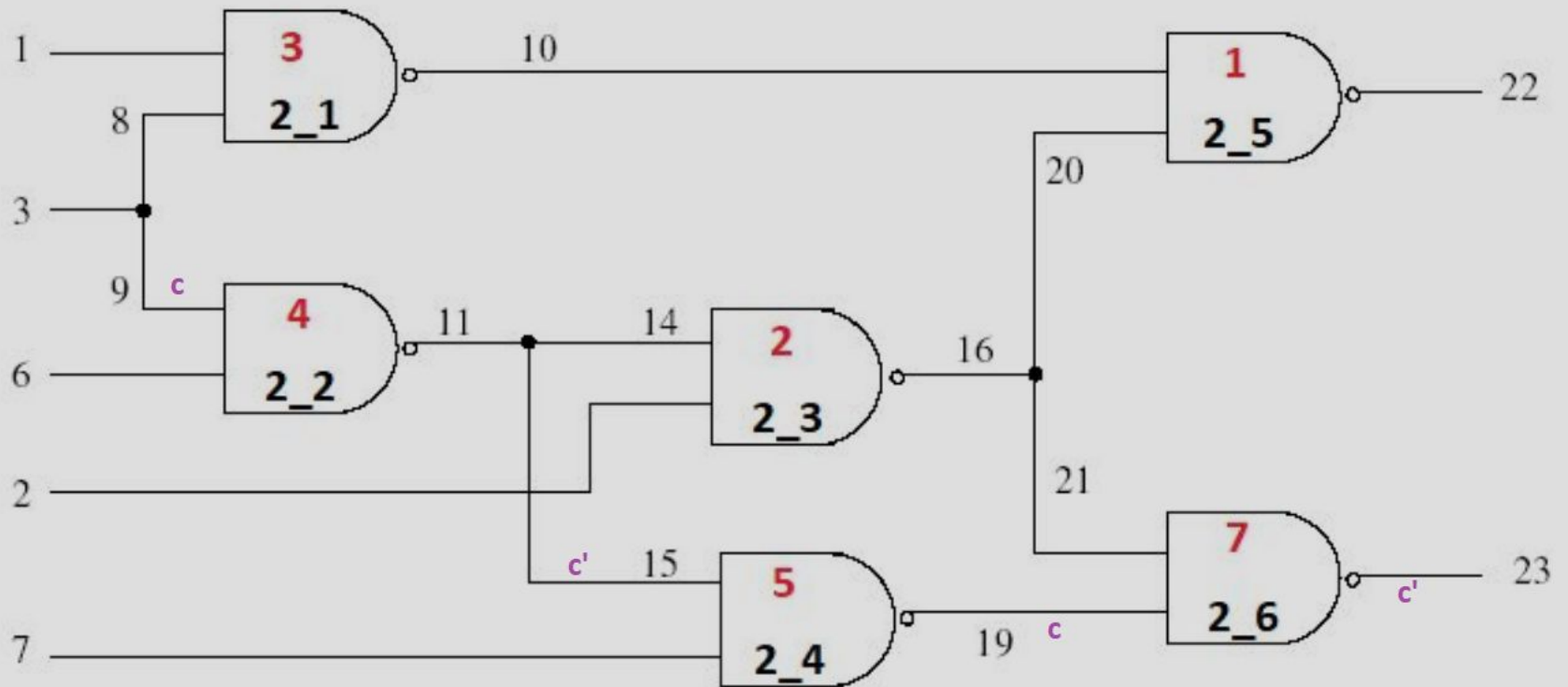
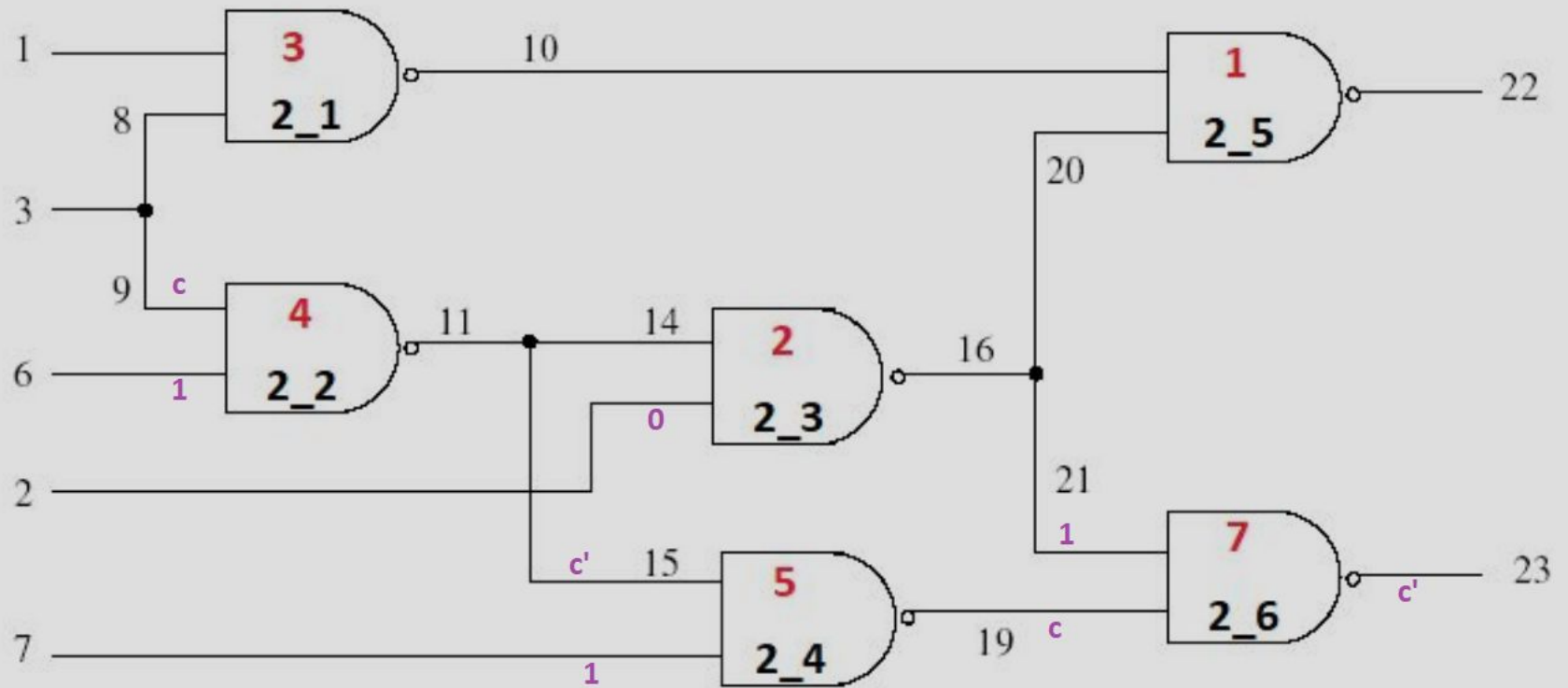


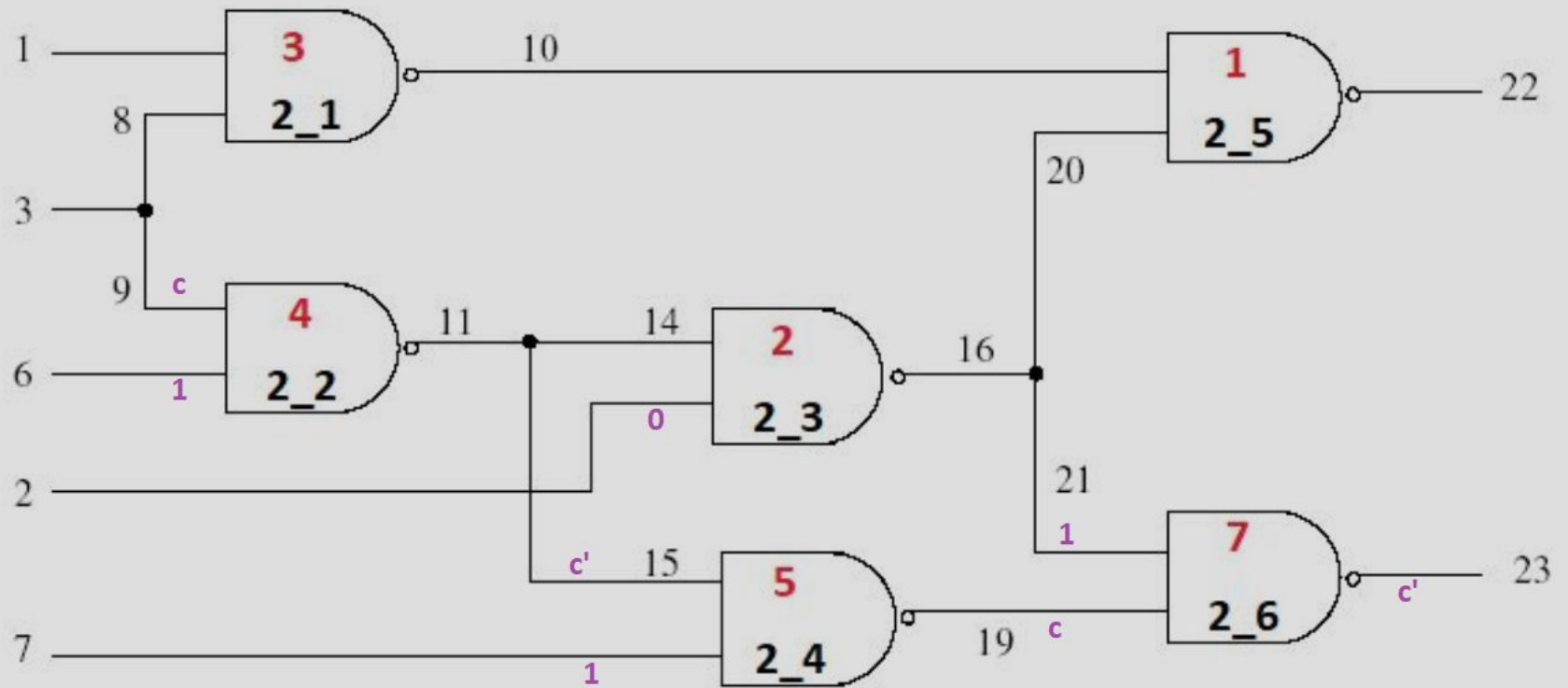
Circuit



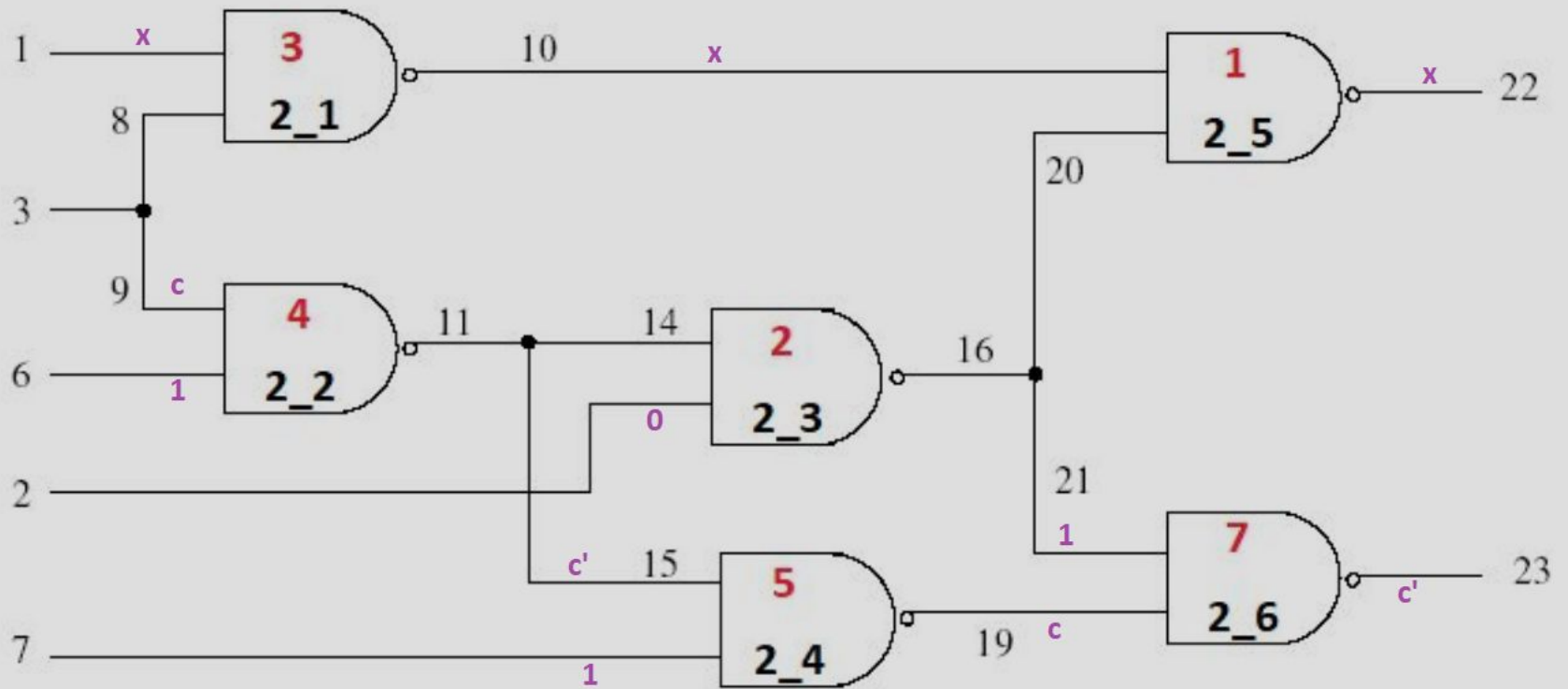
Mark path N3 NAND2_2 N11 NAND2_4 N19 NAND2_6 N23



Backtrace from PO to PI on path



Justify NAND2_3 - Implication Occurs



No more J-frontiers - Done