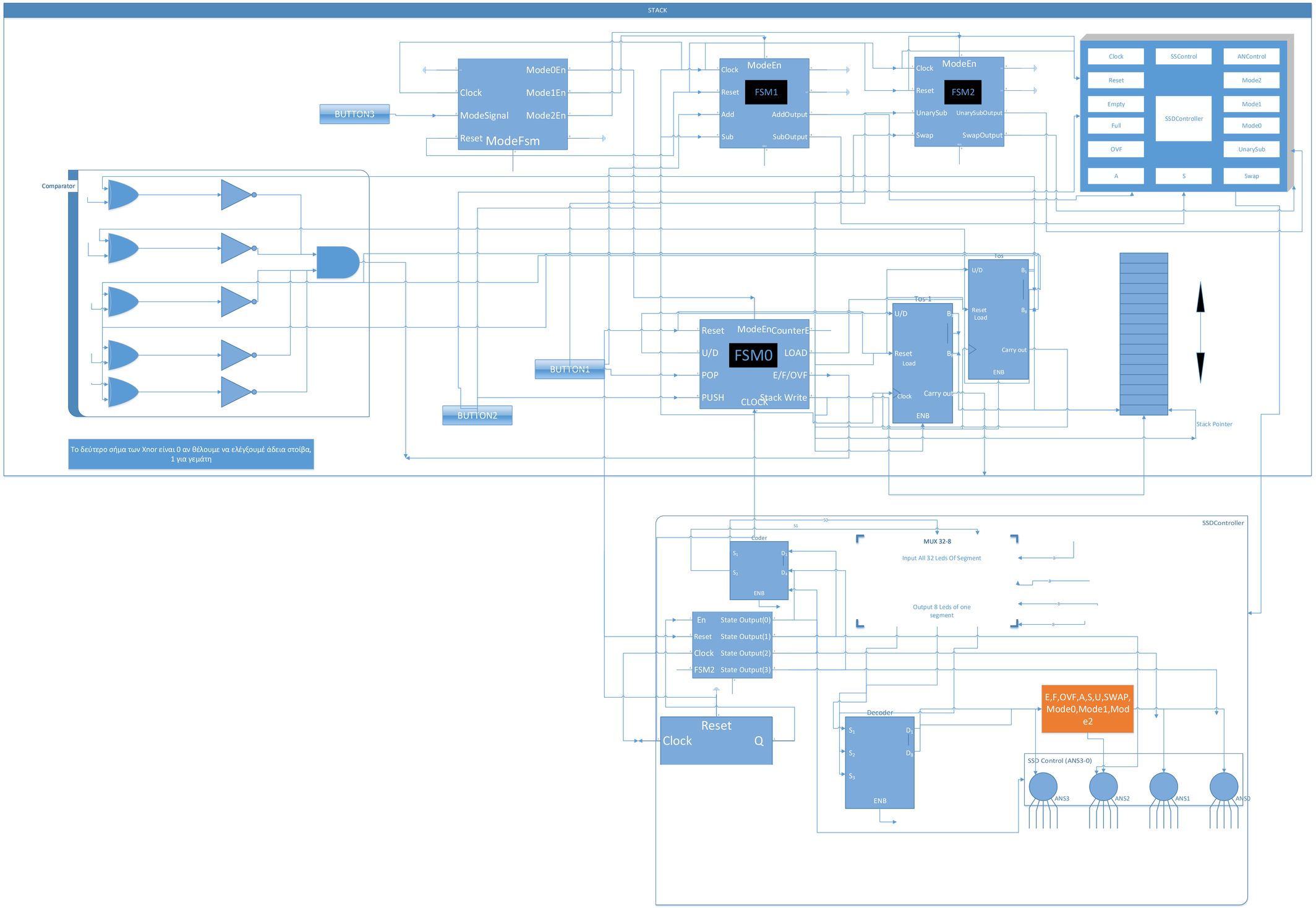
STACK
ModeFsm
Clock
ModeSignal
Reset
Mode0En
Mode1En
Mode2En
BUTTON3
ModeEn
FSM1
Clock
Reset
Add
Sub
AddOutput
SubOutput
ModeEn
FSM2
Clock
Reset
UnarySub
Swap
UnarySubOutput
SwapOutput
Clock
SSControl
ANControl
Reset
Mode2
Empty
SSDController
Mode1
Full
Mode0
OVF
UnarySub
A
S
Swap
Comparator
Tos
U/D
Reset
Load
Carry out
ENB
Tos-1
U/D
Reset
Load
Clock
Carry out
ENB
Reset
ModeEn
CounterE
U/D
FSM0
LOAD
POP
E/F/OVF
PUSH
Stack Write
CLOCK
BUTTON1
BUTTON2
Stack Pointer
Το δεύτερο σήμα των Xnor είναι 0 αν θέλουμε να ελέγξουμέ άδεια στοίβα, 1 για γεμάτη
SSDController
S0
S1
Coder
ENB
MUX 32-8
Input All 32 Leds Of Segment
Output 8 Leds of one segment
En
Reset
Clock
FSM2
State Output(0)
State Output(1)
State Output(2)
State Output(3)
Reset
Clock
Q
Decoder
ENB
E,F,OVF,A,S,U,SWAP, Mode0,Mode1,Mode2
SSD Control (ANS3-0)
ANS3
ANS2
ANS1
ANS0