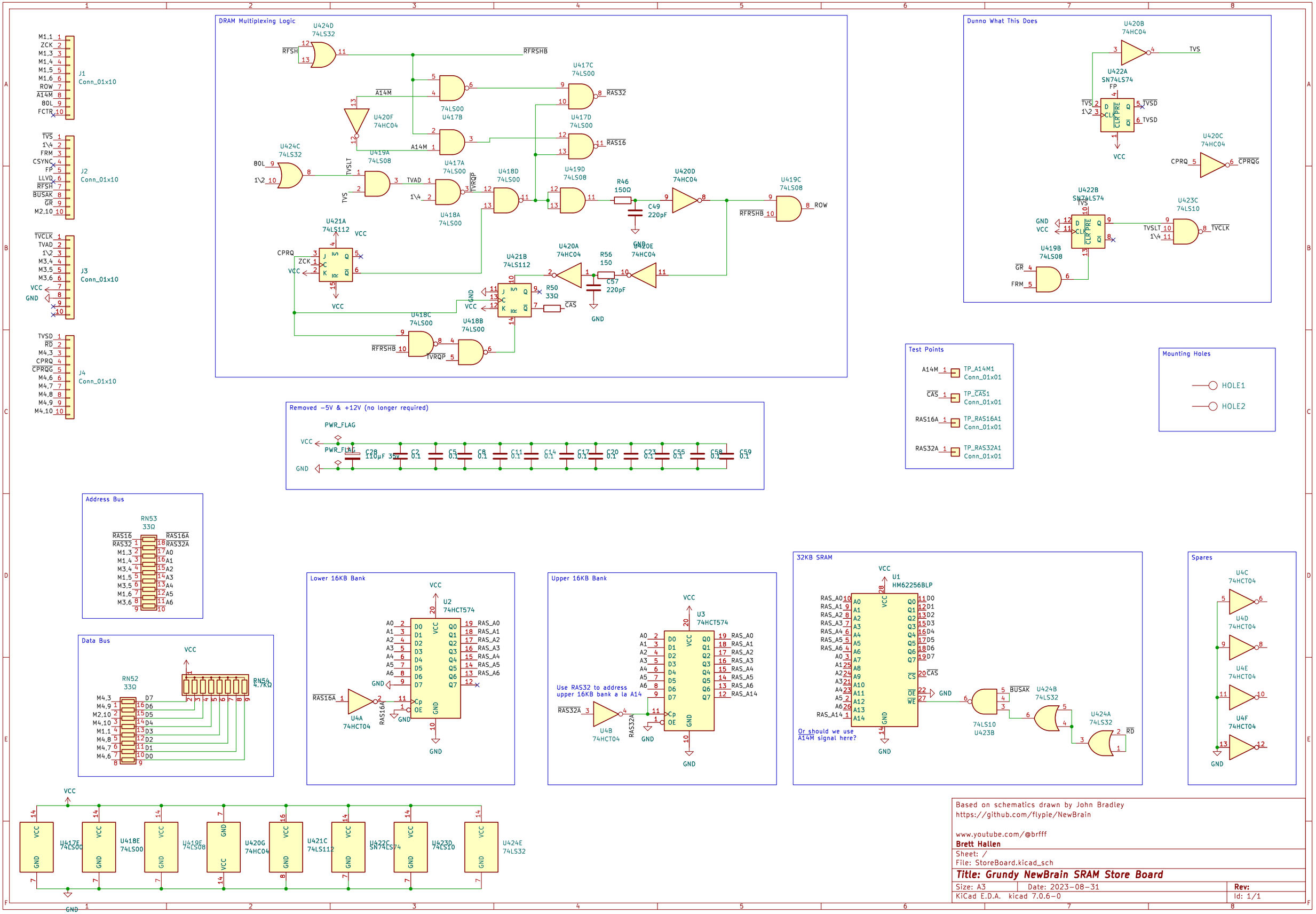
DRAM Multiplexing Logic
Dunno What This Does
Removed −5V & +12V (no longer required)
Test Points
Mounting Holes
Address Bus
Data Bus
Lower 16KB Bank
Upper 16KB Bank
32KB SRAM
Spares
Use RAS32 to address upper 16KB bank a la A14
Or should we use A14M signal here?
Based on schematics drawn by John Bradley
https://github.com/flypie/NewBrain
www.youtube.com/@brfff
Brett Hallen
Sheet: /
File: StoreBoard.kicad_sch
Title: Grundy NewBrain SRAM Store Board
Size: A3
Date: 2023-08-31
Rev:
KiCad E.D.A. kicad 7.0.6-0
Id: 1/1