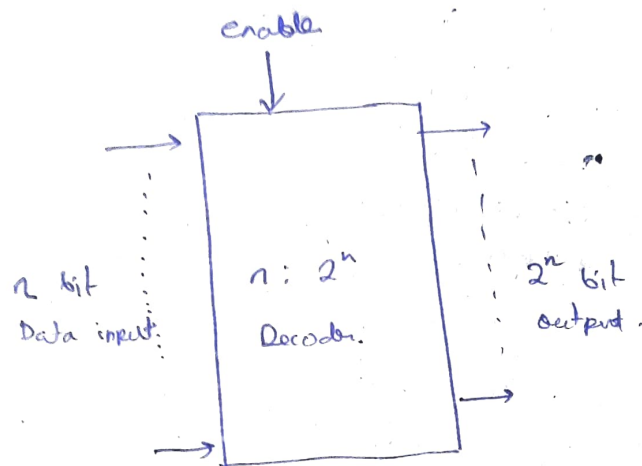


Question:- Design a 4×16 decoder by behavioural modelling, along with test bench.

Block-Diagram:-



A decoder is a combinational circuit that has ' n ' input lines and a maximum of 2^n output lines.

Only one of the output lines is high, based on the input when the decoder is enabled.

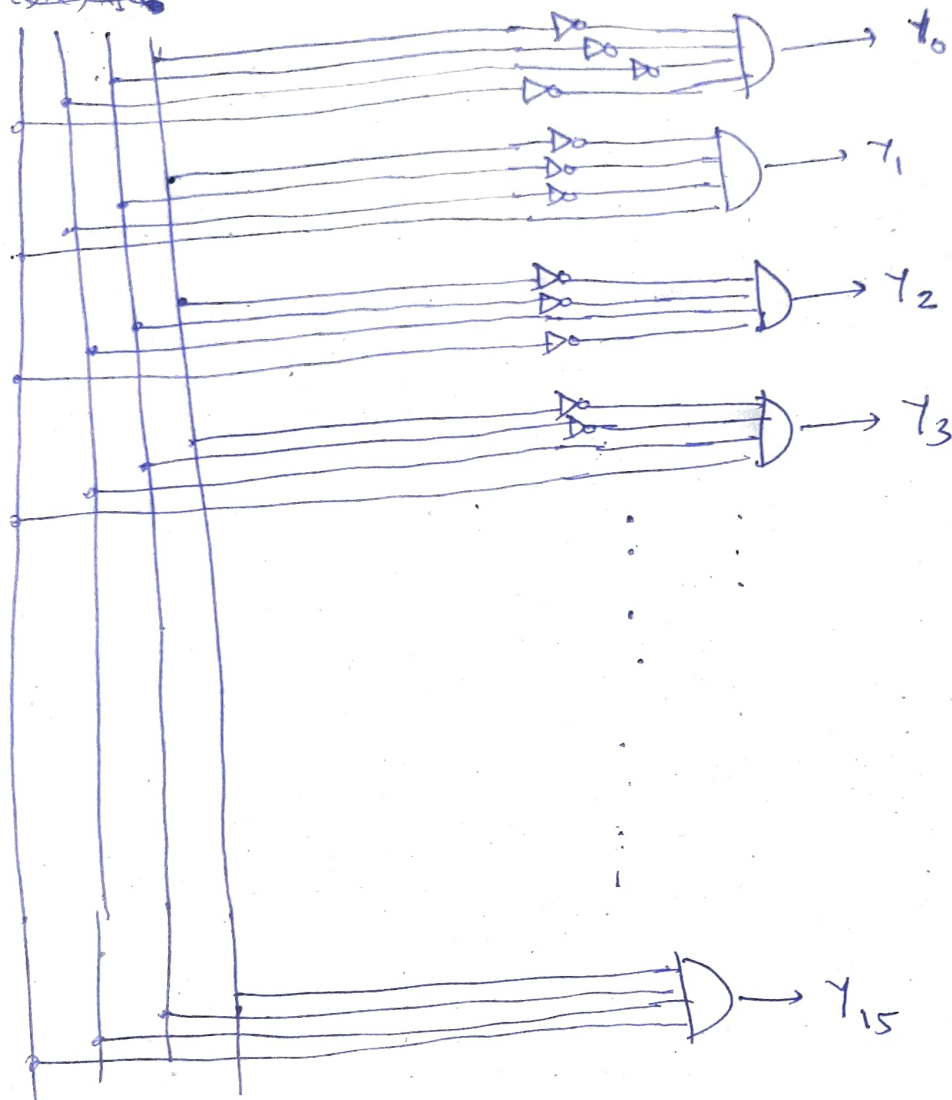
If the decoder is disabled $\&$ all the output lines are low.

Truth table :-

X	Y
0000	0000.00000.00000.00001
0001	000000000000000000010
0010	000000000000000000100
0011	000000000000000001000
0100	000000000000000010000
0101	00000000000000100000
0110	00000000000001000000
0111	00000000000010000000
1000	00000000100000000000
1001	00000000100000000000
1010	00000000100000000000
1011	00000000100000000000
1100	00001000000000000000
1101	00010000000000000000
1110	00100000000000000000
1111	01000000000000000000

Circuit diagram for ~~4x16~~ 4x16 decoder.

x_0, x_1, x_2, x_3
 ~~x_0, x_1, x_2, x_3~~



code:-

entity decoder_4x16 is

Port (x: in std_logic_vector (3 downto 0);

E: in std_logic;

Y: out std_logic_vector (15 downto 0));

end decoder_4x16;

architecture Behavioural of decoder_4x16 is

begin

p1: process (X, E)

begin

if E = '0' then

Y = "0000 0000 0000 0000";

else

if x = "0000" then ~~0000 0000 0000~~ Y <= "0000 0000 0000 0001";

elsif x = "0001" then Y <= "0000 0000 0000 0010";

elsif x = "0010" then Y <= "0000 0000 0000 0100";

elsif x = "0011" then Y <= "0000 0000 0000 1000";

elsif x = "0100" then Y <= "0000 0000 0001 0000";

elsif x = "0101" then Y <= "0000 0000 0010 0000";

elsif x = "0110" then Y <= "0000 0000 0100 0000";

elsif x = "0111" then Y <= "0000 0000 1000 0000";

elsif x = "1000" then Y <= "0000 0001 0000 0000";

elsif x = "1001" then Y <= "0000 0010 0000 0000";

elsif x = "1010" then Y <= "0000 0100 0000 0000";

elsif x = "1011" then Y <= "0000 1000 0000 0000";

elsif x = "1100" then Y <= "0000 0000 0000 0000";

elsif x = "1101" then Y <= "0010 0000 0000 0000";

elsif x = "1110" then Y <= "0100 0000 0000 0000";

elsif x = "1111" then Y <= "1000 0000 0000 0000";

endif;

endif;

end process;

end Behavioural;

Test Bench :-

stim - proc : ~~ps~~ process.

begin

```
x <= "0000"; wait for 1ps;  
x <= "0001"; wait for 1ps;  
x <= "0010"; wait for 1ps;  
x <= "0011"; wait for 1ps;  
x <= "0100"; wait for 1ps;  
x <= "0101"; wait for 1ps;  
x <= "0110"; wait for 1ps;  
x <= "0111"; wait for 1ps;  
x <= "1000"; wait for 1ps;  
x <= "1001"; wait for 1ps;  
x <= "1010"; wait for 1ps;  
x <= "1011"; wait for 1ps;  
x <= "1100"; wait for 1ps;  
x <= "1101"; wait for 1ps;  
x <= "1110"; wait for 1ps;  
x <= "1111"; wait for 1ps;
```

end process;