

# Three Dark Clouds over the Android Kernel

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#### **About me**



- Security researcher in Alpha Lab of 360
- Focus on the Android/Linux kernel
  - ✓ Bug hunting
  - ✓ Writing exploits
  - ✓ Researching Mitigations

#### **Contents**



- **01.** Three Dark Clouds
- **02.** Thunder and Lightning
- 03. Dispel the Clouds and See the Sun
- **04.** Conclusion

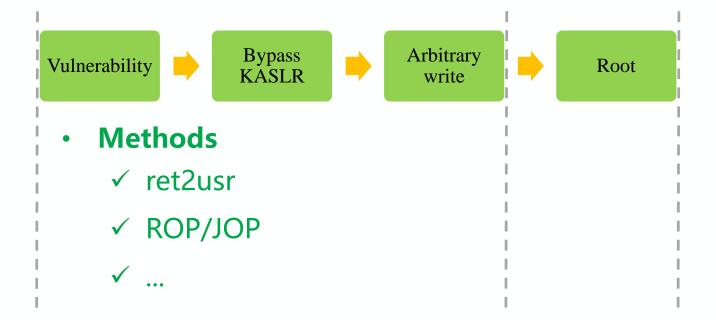
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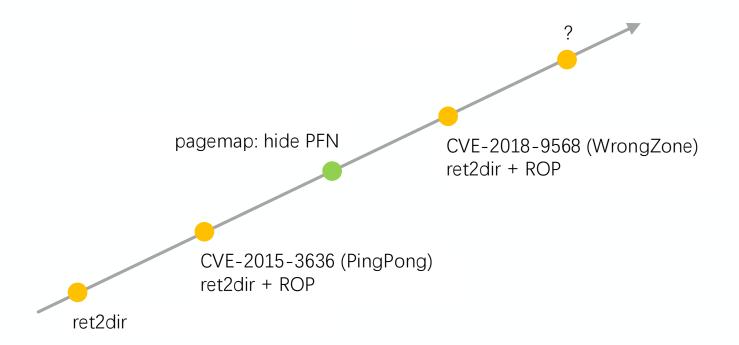
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# Classic attack path



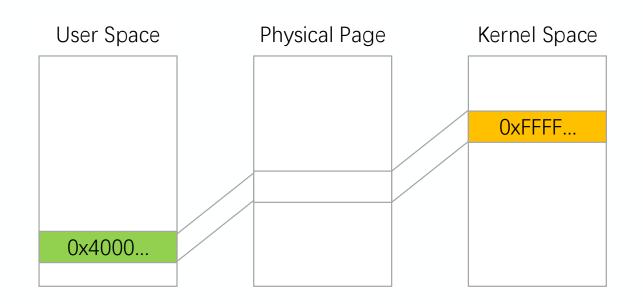








#### Double Mapping



https://cs.brown.edu/~vpk/papers/ret2dir.sec14.pdf



- User can calculate the kernel address
  - 1. Get PFN from /proc/self/pagemap
  - kaddr = (PFN << PAGE\_SHIFT) PHYS\_OFFSET</li>
     + PAGE OFFSET
- The kernel mapping attributes
  - ✓ qcom: RW
  - ✓ mtk: RWX (CVE-2016-0820)



#### Hide PFN in the /proc/self/pagemap

```
+static int pagemap open(struct inode *inode, struct file *file)
+ {
     /* do not disclose physical addresses: attack vector */
      if (!capable(CAP SYS ADMIN))
           return -EPERM;
     return 0;
+
+}
 const struct file operations proc pagemap operations = {
     .llseek = mem lseek, /* borrow this */
     . read = pagemap read,
+
                 = pagemap open,
     . open
 };
```



- It still works
  - ✓ CVE-2015-3636<sup>[1]</sup>
  - ✓ CVE-2018-9568<sup>[2]</sup>
- The key is information leakage

[1] https://www.blackhat.com/docs/us-15/materials/us-15-Xu-Ah-Universal-Android-Rooting-Is-Back.pdf

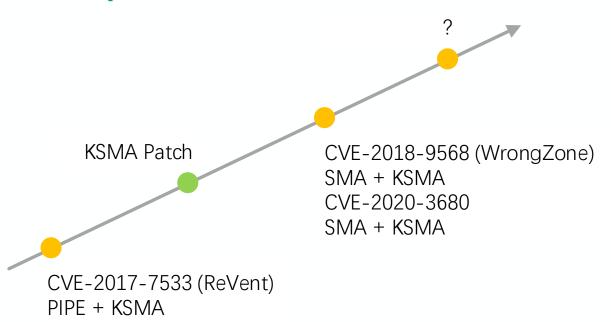
[2] https://github.com/ThomasKing2014/slides/blob/master/Building%20universal%20Android%1920rooting%20with%20a%20type%20confusion%20vulnerability.pdf

# The KSMA





Kernel Space Mirror Attack



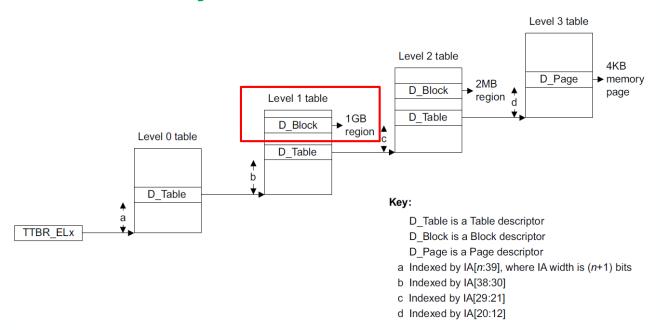
https://i.blackhat.com/briefings/asia/2018/asia-18-WANG-KSMA-Breaking-Android-kernel-isolation-and-Rooting-with-ARM-MMU-features.pdf







#### The block entry

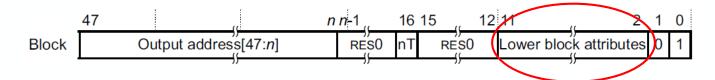


https://documentation-service.arm.com/static/5f20515cbb903e39c84dc459?token=

# The KSMA



#### The AP (Access Permissions) attribute



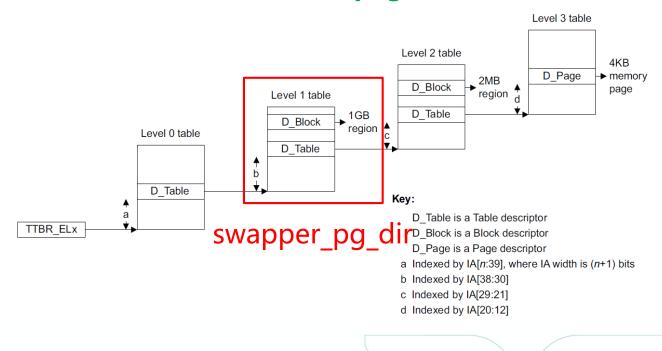
|   | AP[2:1] | Access from higher Exception level | Access from EL0 |
|---|---------|------------------------------------|-----------------|
| _ | 00      | Read/write                         | None            |
|   | 01      | Read/write                         | Read/write      |
|   | 10      | Read-only                          | None            |
|   | 11      | Read-only                          | Read-only       |







Android: 39-bits VA & 4K page



# The KSMA



The position of the swapper\_pg\_dir is fixed

```
arch/arm64/kernel/vmlinux.lds.S

BSS_SECTION(0, 0, 0)

. = ALIGN(PAGE_SIZE);
idmap_pg_dir = .;
. += IDMAP_DIR_SIZE;
swapper_pg_dir = .;
. += SWAPPER_DIR_SIZE;
```

Arbitrary write is needed



- Proposed in 2017
- Has the characteristics of TOCTOU
- Converts heap problems to arbitrary reading and writing



#### Allocate iovec on the heap

```
fs/read write.c
ssize t rw copy check uvector (int type, const struct lovec user * uvector,
                              unsigned long nr segs, unsigned long fast segs,
                              struct iovec *fast pointer, ...) {
     if (nr segs > fast segs) { // UIO FASTIOV == 8
          iov = kmalloc(nr segs*sizeof(struct iovec), GFP KERNEL);
     if (copy from user(iov, uvector, nr segs*sizeof(*uvector))) {
          ret = -EFAULT;
          goto out;
```



#### Check the iovec passed in by the user

```
fs/read write.c
ssize_t rw_copy_check_uvector(int type, const struct iovec __user * uvector,
                            unsigned long nr_segs, unsigned long fast_segs,
                             struct iovec *fast pointer, ...) {
     for (seg = 0; seg < nr_segs; seg++) {
           void user *buf = iov[seg].iov base;
           ssize t len = (ssize t)iov[seg].iov len;
           if (type >= 0 && unlikely(!access ok(vrfy dir(type), buf, len))) {}
           ret += len;
```



- We can block the pipe
  - 1. There is no contents when reading
  - 2. There is no space when writing
- Overwrite the iovec





Does NOT check when copying from/to the user

```
lib/iov iter.c
static size_t copy_page_to_iter_iovec(..) {
     left = copy to user inatomic(buf, from, copy);
     copy -= left:
     skip += copy;
     from += copy;
     bytes -= copy;
```



- Two mitigations
  - 1. UAO (User Access Override)
  - 2. uaccess\_mask\_ptr()

```
arch/arm64/include/asm/uaccess.h

static inline void __user *__uaccess_mask_ptr(const void __user *ptr)
{
    asm volatile(
        " bics xzr, %1, %2\n"
        " csel %0, %1, xzr, eq\n"
        : "=&r" (safe_ptr)
        : "r" (ptr), "r" (current_thread_info()->addr_limit)
        : "cc");
    return safe_ptr;
}
```

https://developer.arm.com/docs/ddi0595/h/aarch64-system-registers/uao

### The SMA



- SLAB Mirror Attack
  - ✓ Two objects share the same SLAB
  - ✓ Bypass KASLR
  - ✓ Write kernel arbitrary (+KSMA = ROOT)

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#### CVE-2020-3680



#### Timeline



2020-4-22 The CVE ID is CVE-2020-3680

2020-5-4 Public disclosure on the Qualcomm Security Bulletin

#### CVE-2020-3680



#### Qualcomm ADSPRPC driver

```
drivers/char/adsprpc.c
int fastrpc internal mmap(...) {
                                                       int fastrpc internal munmap fd(...) {
     mutex lock(&fl->map mutex);
                                                          mutex lock(&fl->fl map mutex);// 2
                                                          if (fastrpc_mmap_find(..., &map))
     mutex_lock(&fl->fl_map_mutex);
     VERIFY(err, !fastrpc_mmap_create(..., &map));
                                                          if (map)
     mutex unlock(&fl->fl map mutex); // 1
                                                                fastrpc mmap free (map. 0)://3
     VERIFY(err, !fastrpc mmap on dsp());
                                                          mutex unlock(&fl->fl map mutex):
bail:
                                                          return err;
      if (err && map) {
           mutex_lock(&fl->fl_map_mutex);
           fastrpc_mmap_free(map, 0); // 4
           mutex unlock(&fl->fl map mutex);
     mutex unlock(&fl->map mutex);
     return err;
```

# **How to exploit**



- ROP/JOP (a)
  - ✓ PAN (Privileged Access Never)
  - ✓ CFI (Control Flow Integrity)
- SMA + KSMA @
  - Convert UAF to Double Free
  - 2. Bypass KASLR
  - 3. Apply KSMA



```
drivers/char/adsprpc.c
void fastrpc mmap free(struct fastrpc mmap *map, uint32 t flags) {
     if (map->flags == ADSP_MMAP_HEAP_ADDR || ...) {
      else
          map->refs--:
          if (!map->refs)
               hlist del init(&map->hn); // 1
          if (map->refs > 0 && !flags)
               return:
     if (map->flags == ADSP MMAP HEAP ADDR | | ...) {
      else if (map->flags == FASTRPC_DMAHANDLE NOMAP) {
          if (!IS ERR OR NULL(map->handle))
               ion free(fl->apps->client, map->handle); // 2
     kfree(map); // 3
```



How to bypass hlist\_del\_init()

```
include/linux/list.h

static inline void hlist_del_init(struct hlist_node *n) {
    if (!hlist_unhashed(n)) {
        __hlist_del(n);
        INIT_HLIST_NODE(n);
    }
}

static inline int hlist_unhashed(const struct hlist_node *h {
    return !h->pprev;
}
```



- The object used for heap spray
  - 1. It uses the kmalloc-256 SLAB
  - 2. I can control the map->refs and map->flags
  - 3. The map->hn->pprev is zero

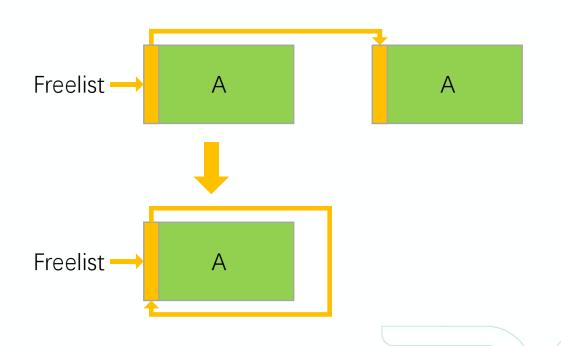


The object used for heap spray

```
fs/xattr.c
setxattr(..., const void user *value, size t size, int flags) {
      [...]
     if (size) {
           kvalue = kmalloc(size, GFP_KERNEL | __GFP_NOWARN);
           if (copy from user (kvalue, value, size)) {
                 error = -EFAULT;
                 goto out;
```



The SLAB appears twice in the freelist





Use the freelist to modify the object

```
struct seq_file {
    char *buf; // is modified by the freelist
    size_t size;
    [...]
    struct mutex lock;
    const struct seq_operations *op; // is leaked
    int poll_event;
    const struct file *file;
    void *private;
}
```

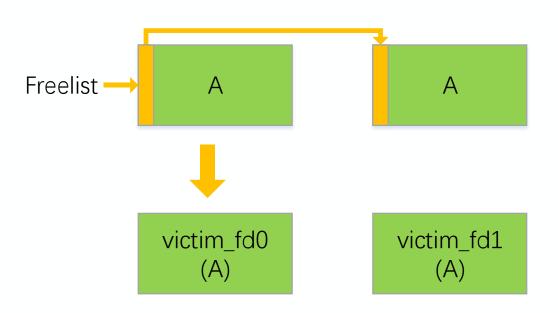


Users can read the seq\_file->buf

```
flame:/ $ cat /proc/cpuinfo
Processor : AArch64 Processor rev 14 (aarch64)
processor : 0
BogoMIPS : 38.00
Features : fp asimd evtstrm aes pmull sha1 sha2 crc32 atomics
CPU implementer : 0x51
CPU architecture: 8
CPU variant : 0xd
CPU part : 0x805
CPU revision : 14
```

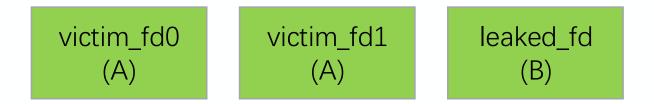


Allocate victim\_fd0 & victim\_fd1

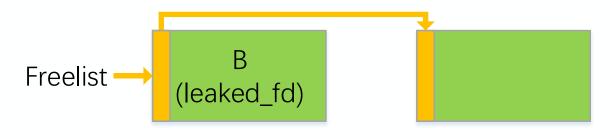




Allocate the leaked\_fd

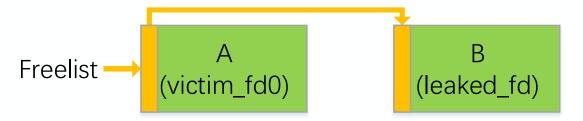


Free the leaked fd





Free the victim fd0



Read seq\_file->op from victim\_fd1



- Use the object to modify the freelist
  - 1. It uses the kmalloc-256 SLAB
  - 2. I can control the first 8 bytes



Use the object to modify the freelist

```
struct ipv6_mc_socklist {
    struct in6_addr addr;
    int ifindex;
    struct ipv6_mc_socklist __rcu *next;
    rwlock_t sflock;
    unsigned int sfmode;
    struct ip6_sf_socklist *sflist;
    struct rcu_head rcu;
}
```



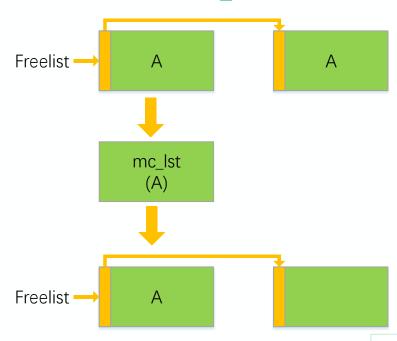
#### Use the object to modify the freelist

```
net/ipv6/mcast.c

int ipv6_sock_mc_join(..., const struct in6_addr *addr) {
    [...]
    mc_lst = sock_kmalloc(sizeof(struct ipv6_mc_socklist));
    if (!mc_lst)
        return - ENOMEM;
    mc_lst->next = NULL;
    mc_lst->addr = *addr;
    [...]
}
```

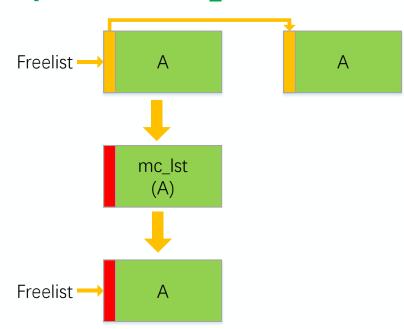


Allocate the mc\_lst





Update the mc\_lst->addr



**Demo** 







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### The ret2dir mitigation



- Exclusive Page-Frame Ownership
  - ✓ Unmap the user page in the kernel
  - ✓ Not yet merged into the mainline

### The KSMA mitigation



- The KSMA patch
  - ✓ I submitted it in May 2018
  - ✓ Was merged into the mainline in September 2018
  - ✓ Android has not yet been backported ☺

### The KSMA patch



#### Move the page table to the rodata section

```
arch/arm64/kernel/vmlinux.lds.S
+#define KERNEL PG TABLES \
+ . = ALIGN(PAGE SIZE); \
+ idmap_pg_dir = .; \
+ . += IDMAP DIR SIZE; \
+ TRAMP PG TABLE \
+ RESERVED PG TABLE \
+ swapper pg dir = .; \
+ . += PAGE SIZE; \
+ swapper pg end = .;
RO DATA (PAGE SIZE)
EXCEPTION TABLE (8)
NOTES
+ KERNEL PG TABLES
```

### The KSMA patch



The kernel assumes the relative offset between the swapper\_pg\_dir and the tramp\_pg\_dir

```
arch/arm64/kernel/entry. S
. macro tramp_map_kernel, tmp
mrs \tmp, ttbr1_el1
sub \tmp, \tmp, #(SWAPPER_DIR_SIZE + RESERVED_TTBRO_SIZE)
bic \tmp, \tmp, #USER_ASID_FLAG
msr ttbr1_el1, \tmp
[...]
. endm
```

### The KSMA patch



Update the page table through fixmap

```
void set_swapper_pgd(pgd_t *pgdp, pgd_t pgd)
{
    pgd_t *fixmap_pgdp;
    spin_lock(&swapper_pgdir_lock);
    fixmap_pgdp = pgd_set_fixmap(pa(pgdp));
    WRITE_ONCE(*fixmap_pgdp, pgd);
    pgd_clear_fixmap();
    spin_unlock(&swapper_pgdir_lock);
}
```

### The SMA mitigation



- CONFIG\_SLAB\_FREELIST\_HARDENED[1]
  - ✓ Not enabled in the kernel 4.9
  - ✓ Not enabled on the Google Pixel (4.14)
- CONFIG\_INIT\_ON\_FREE\_DEFAULT\_ON<sup>[2]</sup>
  - ✓ Not enabled in the Android kernel

[1] https://hardenedlinux.github.io/system-security/2017/12/02/linux\_kernel\_4.14%E7%9A%84SLAB\_FREELIS

T\_HARDENED%E7%9A%84%E7%AE%80%E8%A6%81%E5%88%86%E6%9E%90.html





#### Generate a random number

```
mm/slub.c

int kmem_cache_open(struct kmem_cache *s, unsigned long flags)
{
    s->flags = kmem_cache_flags(s->size, flags, s->name, s->ctor);
    s->reserved = 0;

#ifdef CONFIG_SLAB_FREELIST_HARDENED
    s->random = get_random_long();

#endif
}
```



#### Obfuscate the address of SLAB





#### The obfuscation has weaknesses

kmalloc-32 freelist walk, before:

```
ptr ptr_addr stored value random number ffff90c22e019020@ffff90c22e019000 is 86528eb656b3b5bd (86528eb656b3b59d) ffff90c22e019060@ffff90c22e019040 is 86528eb656b3b5bd (86528eb656b3b59d) ffff90c22e019060@ffff90c22e019040 is 86528eb656b3b5bd (86528eb656b3b59d)
```

https://git.kernel.org/pub/scm/linux/kernel/git/torvalds/linux.git/commit/?id=1ad53d9fa3f6168ebcf48a50e08b170432da2257



#### Improve the obfuscation



#### Check the double free

```
mm/slub.c

void set_freepointer(struct kmem_cache *s, void *object, void *fp)
{
    unsigned long freeptr_addr = (unsigned long)object + s->offset;
#ifdef CONFIG_SLAB_FREELIST_HARDENED
    BUG_ON(object == fp); // Double free
#endif
    *(void **)freeptr_addr = freelist_ptr(s, fp, freeptr_addr);
}
```

### INIT ON FREE DEFAULT ON



#### Clear the data in the released SLAB

```
mm/slub.c
bool slab free freelist hook(struct kmem cache *s, void **head, void **tail)
     do {
          object = next;
          next = get freepointer(s, object);
          if (slab want init on free(s)) {
               memset(object, 0, s->object size);
               rsize = (s-)flags & SLAB RED ZONE) ? s-)red left pad : 0;
               memset ((char *) object + s->inuse, 0, s->size - s->inuse - rsize);
     } while (object != old tail);
```

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### Conclusion



- The combination of the SMA and the KSMA is powerful
- In order to harden the kernel, we need:
  - 1. Mitigate known attacks (the ret2dir)
  - 2. Fill the gap between Android and the mainline (the KSMA patch)
  - 3. Prevent kernel fragmentation (the SMA)



### Thank You

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