

Node Name	Customize Column S...	Direction	Location	I/O Bank	VREF Group	Fitter Location	I/O Standard	Reserved	Current Strength	Slew Rate	Differential Pair	Strict Preservation
dadosIN[8]		Input	PIN_B14	7	B7_N0	PIN_B14	2.5 V		12mA (default)			
dadosIN[7]		Input	PIN_A14	7	B7_N0	PIN_A14	2.5 V		12mA (default)			
dadosIN[6]		Input	PIN_A13	7	B7_N0	PIN_A13	2.5 V		12mA (default)			
dadosIN[5]		Input	PIN_B12	7	B7_N0	PIN_B12	2.5 V		12mA (default)			
dadosIN[4]		Input	PIN_A12	7	B7_N0	PIN_A12	2.5 V		12mA (default)			
dadosIN[3]		Input	PIN_C12	7	B7_N0	PIN_C12	2.5 V		12mA (default)			
dadosIN[2]		Input	PIN_D12	7	B7_N0	PIN_D12	2.5 V		12mA (default)			
dadosIN[1]		Input	PIN_C11	7	B7_N0	PIN_C11	2.5 V		12mA (default)			
dadosIN[0]		Input	PIN_C10	7	B7_N0	PIN_C10	2.5 V		12mA (default)			
disp1[0]		Output	PIN_C14	7	B7_N0	PIN_C14	2.5 V		12mA (default)	2 (default)		
disp1[1]		Output	PIN_E15	7	B7_N0	PIN_E15	2.5 V		12mA (default)	2 (default)		
disp1[2]		Output	PIN_C15	7	B7_N0	PIN_C15	2.5 V		12mA (default)	2 (default)		
disp1[3]		Output	PIN_C16	7	B7_N0	PIN_C16	2.5 V		12mA (default)	2 (default)		
disp1[4]		Output	PIN_E16	7	B7_N0	PIN_E16	2.5 V		12mA (default)	2 (default)		
disp1[5]		Output	PIN_D17	7	B7_N0	PIN_D17	2.5 V		12mA (default)	2 (default)		
disp1[6]		Output	PIN_C17	7	B7_N0	PIN_C17	2.5 V		12mA (default)	2 (default)		
disp2[0]		Output	PIN_C18	7	B7_N0	PIN_C18	2.5 V		12mA (default)	2 (default)		
disp2[1]		Output	PIN_D18	6	B6_N0	PIN_D18	2.5 V		12mA (default)	2 (default)		
disp2[2]		Output	PIN_E18	6	B6_N0	PIN_E18	2.5 V		12mA (default)	2 (default)		

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disp2[3]		Output	PIN_B16	7	B7_N0	PIN_B16	2.5 V		12mA (default)	2 (default)		
disp2[4]		Output	PIN_A17	7	B7_N0	PIN_A17	2.5 V		12mA (default)	2 (default)		
disp2[5]		Output	PIN_A18	7	B7_N0	PIN_A18	2.5 V		12mA (default)	2 (default)		
disp2[6]		Output	PIN_B17	7	B7_N0	PIN_B17	2.5 V		12mA (default)	2 (default)		
disp3[0]		Output	PIN_B20	6	B6_N0	PIN_B20	2.5 V		12mA (default)	2 (default)		
disp3[1]		Output	PIN_A20	7	B7_N0	PIN_A20	2.5 V		12mA (default)	2 (default)		
disp3[2]		Output	PIN_B19	7	B7_N0	PIN_B19	2.5 V		12mA (default)	2 (default)		
disp3[3]		Output	PIN_A21	6	B6_N0	PIN_A21	2.5 V		12mA (default)	2 (default)		
disp3[4]		Output	PIN_B21	6	B6_N0	PIN_B21	2.5 V		12mA (default)	2 (default)		
disp3[5]		Output	PIN_C22	6	B6_N0	PIN_C22	2.5 V		12mA (default)	2 (default)		
disp3[6]		Output	PIN_B22	6	B6_N0	PIN_B22	2.5 V		12mA (default)	2 (default)		
disp4[0]		Output	PIN_F21	6	B6_N0	PIN_F21	2.5 V		12mA (default)	2 (default)		
disp4[1]		Output	PIN_E22	6	B6_N0	PIN_E22	2.5 V		12mA (default)	2 (default)		
disp4[2]		Output	PIN_E21	6	B6_N0	PIN_E21	2.5 V		12mA (default)	2 (default)		
disp4[3]		Output	PIN_C19	7	B7_N0	PIN_C19	2.5 V		12mA (default)	2 (default)		
disp4[4]		Output	PIN_C20	6	B6_N0	PIN_C20	2.5 V		12mA (default)	2 (default)		
disp4[5]		Output	PIN_D19	6	B6_N0	PIN_D19	2.5 V		12mA (default)	2 (default)		
disp4[6]		Output	PIN_E17	6	B6_N0	PIN_E17	2.5 V		12mA (default)	2 (default)		
enter		Input	PIN_F15	7	B7_N0	PIN_F15	2.5 V		12mA (default)			

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estado[3]		Output	PIN_B10	7	B7_N0	PIN_B10	2.5 V		12mA (default)	2 (default)		
estado[2]		Output	PIN_A10	7	B7_N0	PIN_A10	2.5 V		12mA (default)	2 (default)		
estado[1]		Output	PIN_A9	7	B7_N0	PIN_A9	2.5 V		12mA (default)	2 (default)		
estado[0]		Output	PIN_A8	7	B7_N0	PIN_A8	2.5 V		12mA (default)	2 (default)		
realClk		Input	PIN_P11	3	B3_N0	PIN_P11	2.5 V		12mA (default)			
<<new node>>												