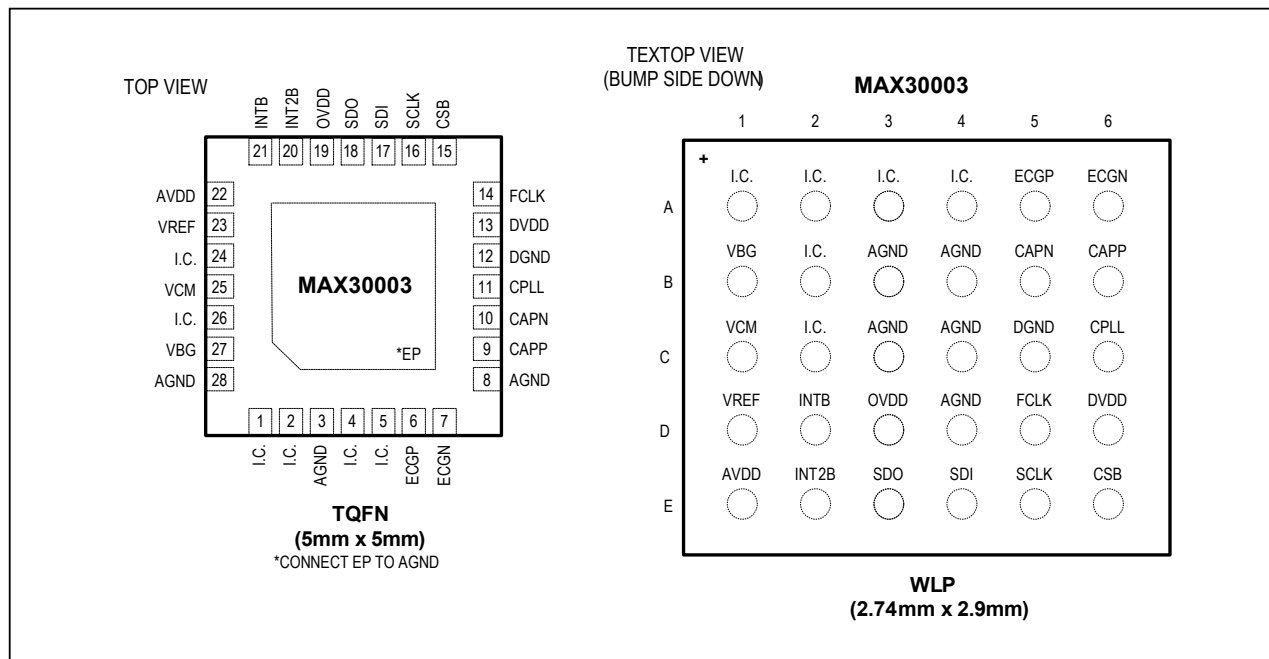


## Pin Configurations



## Pin Description

PIN	BUMP	NAME	FUNCTION
TQFN	WLP		
1, 2, 4, 5, 24, 26	A1, A2, A3, A4, B2, C2	I.C.	Internally Connected. Connect to AGND.
3, 8, 28	B3, B4, C3, C4, D4	AGND	Analog Power and Reference Ground. Connect into the printed circuit board ground plane.
6	A5	ECGP	ECG Positive Input
7	A6	ECGN	ECG Negative Input
9	B6	CAPP	Analog High-Pass Filter Input. Connect a 1μF X7R capacitor (CHPF) between CAPP and CAPN to form a 0.5Hz high-pass response in the ECG channel.
10	B5	CAPN	Analog High-Pass Filter Input. Connect a 1μF X7R capacitor (CHPF) between CAPP and CAPN to form a 0.5Hz high-pass response in the ECG channel.
11	C6	CPLL	PLL Loop Filter Input. Connect 1nF COG cap between CPLL and AGND.
12	C5	DGND	Digital Ground for Both Digital Core and I/O Pad Drivers. Recommended to connect to AGND plane.
13	D6	DVDD	Digital Core Supply Voltage. Connect to AVDD

## Pin Description (continued)

PIN	BUMP	NAME	FUNCTION
TQFN	WLP		
14	D5	FCLK	External 32.768kHz Clock that Controls the Sampling of the Internal Sigma-Delta Converters and Decimator.
15	E6	CSB	Active-Low Chip-Select Input. Enables the serial interface.
16	E5	SCLK	Serial Clock Input. Clocks data in and out of the serial interface when CSB is low.
17	E4	SDI	Serial Data Input. SDI is sampled into the device on the rising edge of SCLK when CSB is low.
18	E3	SDO	Serial Data Output. SDO will change state on the falling edge of SCLK when CSB is low. SDO is three-stated when CSB is high.
19	D3	OVDD	Logic Interface Supply Voltage
20	E2	INT2B	Interrupt 2 Output. INT2B is an active-low status output. It can be used to interrupt an external device.
21	D2	INTB	Interrupt Output. INTB is an active low status output. It can be used to interrupt an external device.
22	E1	AVDD	Analog Core Supply Voltage. Connect to DVDD.
23	D1	V <sub>REF</sub>	ADC Reference Buffer Output. Connect a 10μF X5R ceramic capacitor between V <sub>REF</sub> and AGND.
25	C1	V <sub>CM</sub>	Common Mode Buffer Output. Connect a 10μF X5R ceramic capacitor between V <sub>CM</sub> and AGND.
27	B1	V <sub>BG</sub>	Bandgap Noise Filter Output. Connect a 1.0μF X7R ceramic capacitor between V <sub>BG</sub> and AGND.
EP	—	—	Exposed Paddle. Connect to AGND.