

CS2310: Foundations of Computer System Design Lab

August-November 2021 Semester

Assignment 7

Date: 26th October, 2021

Deadline for Submission: 11.59PM on 1st November, 2021

Implementation of HACK ALU

Details of block diagram of HACK ALU, and control signals are given in a separate file.

Test case 1: x and y are positive, and $x > y$

Test case 2: x is positive and y is negative, and $|x| > |y|$

Each test case: Generate the outputs of ALU, i.e., Out, zr and ng, for each of the 18 combinations of control signals, i.e., zx, nx, zy, ny, f, no.