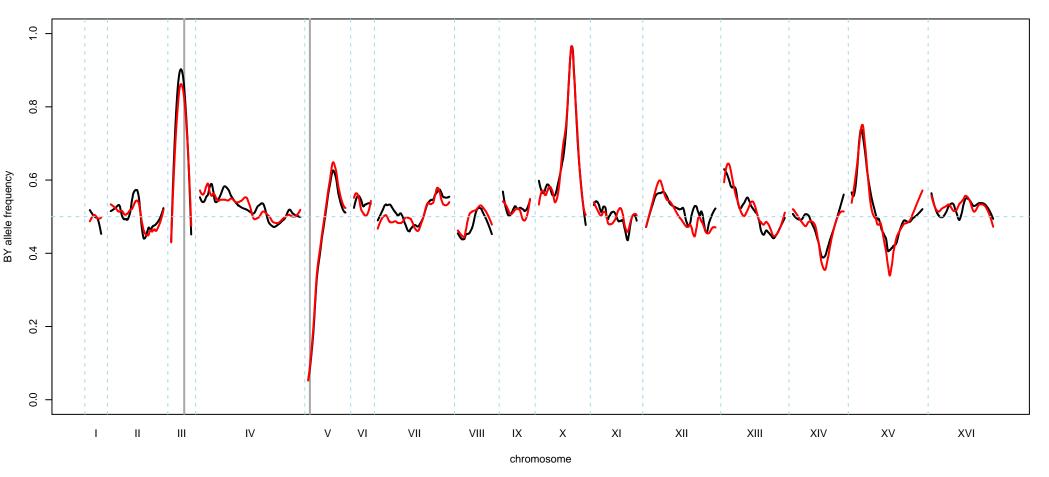
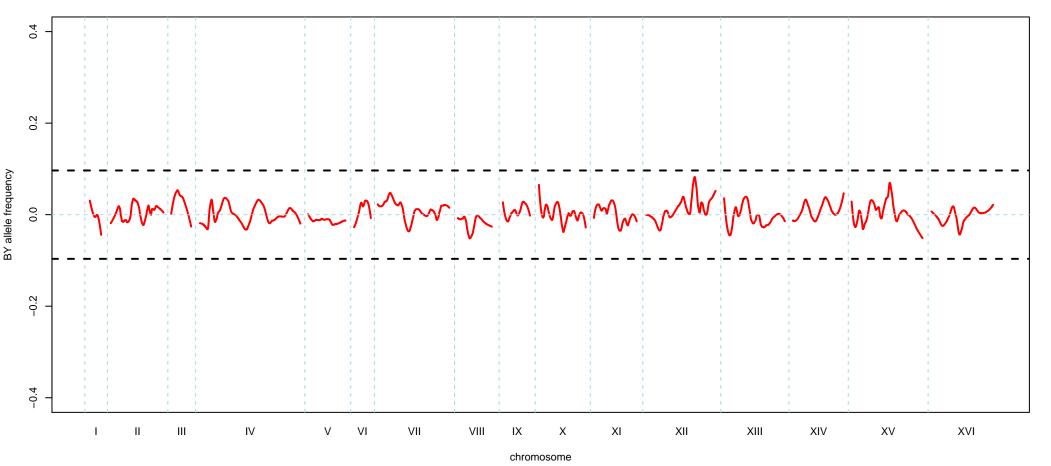
CYC1_a_1_1_1 CYC1_a_2_1_1



CYC1_a_1_1_1 CYC1_a_2_1_1



CYC1_a_1_1_1 CYC1_a_2_1_1

