

# Plataformas Comerciais

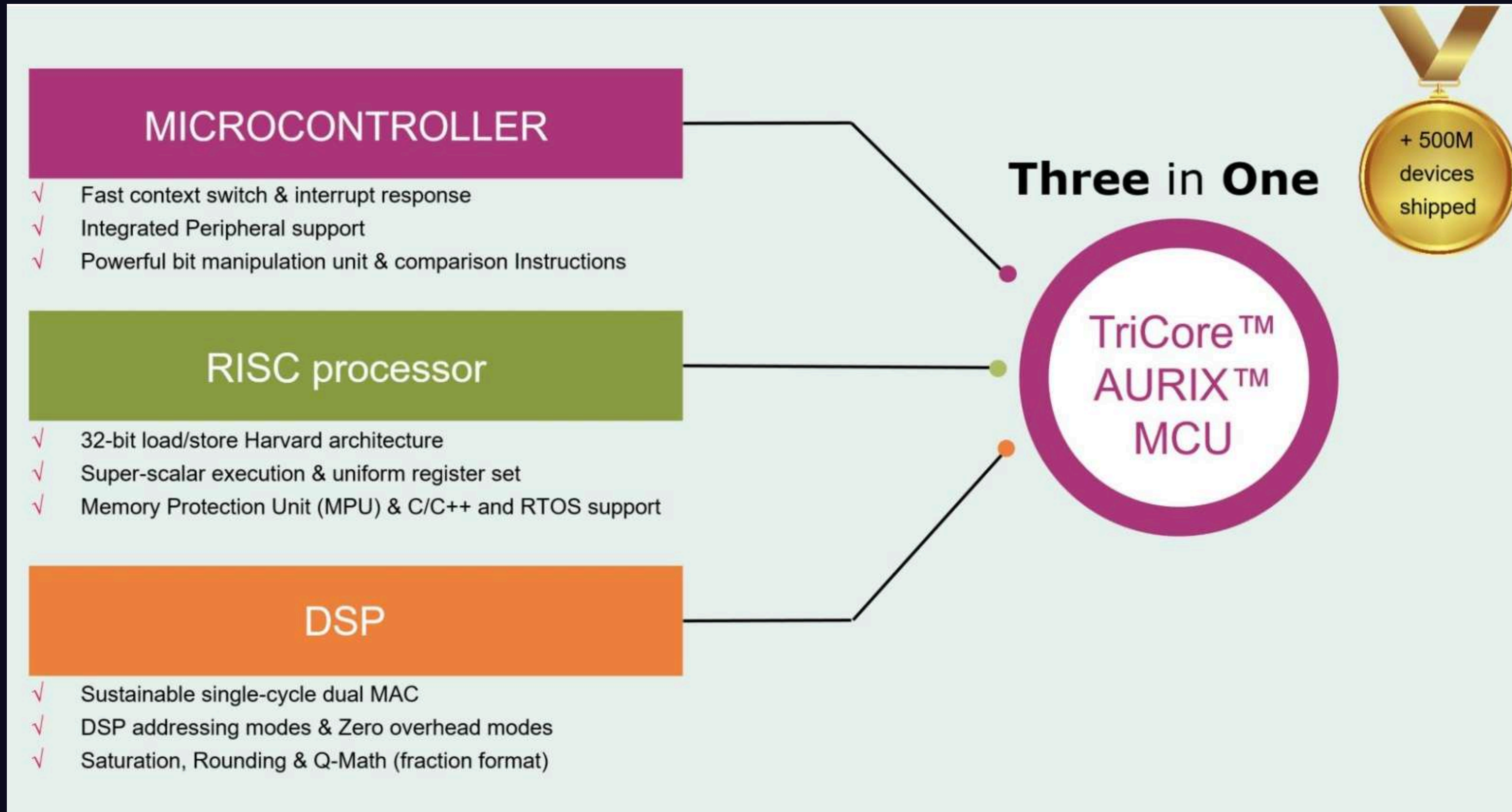
Tricore AURIX TC2xx (Infineon)



Paulo Diego De Meneses  
Davi Coelho Maciel

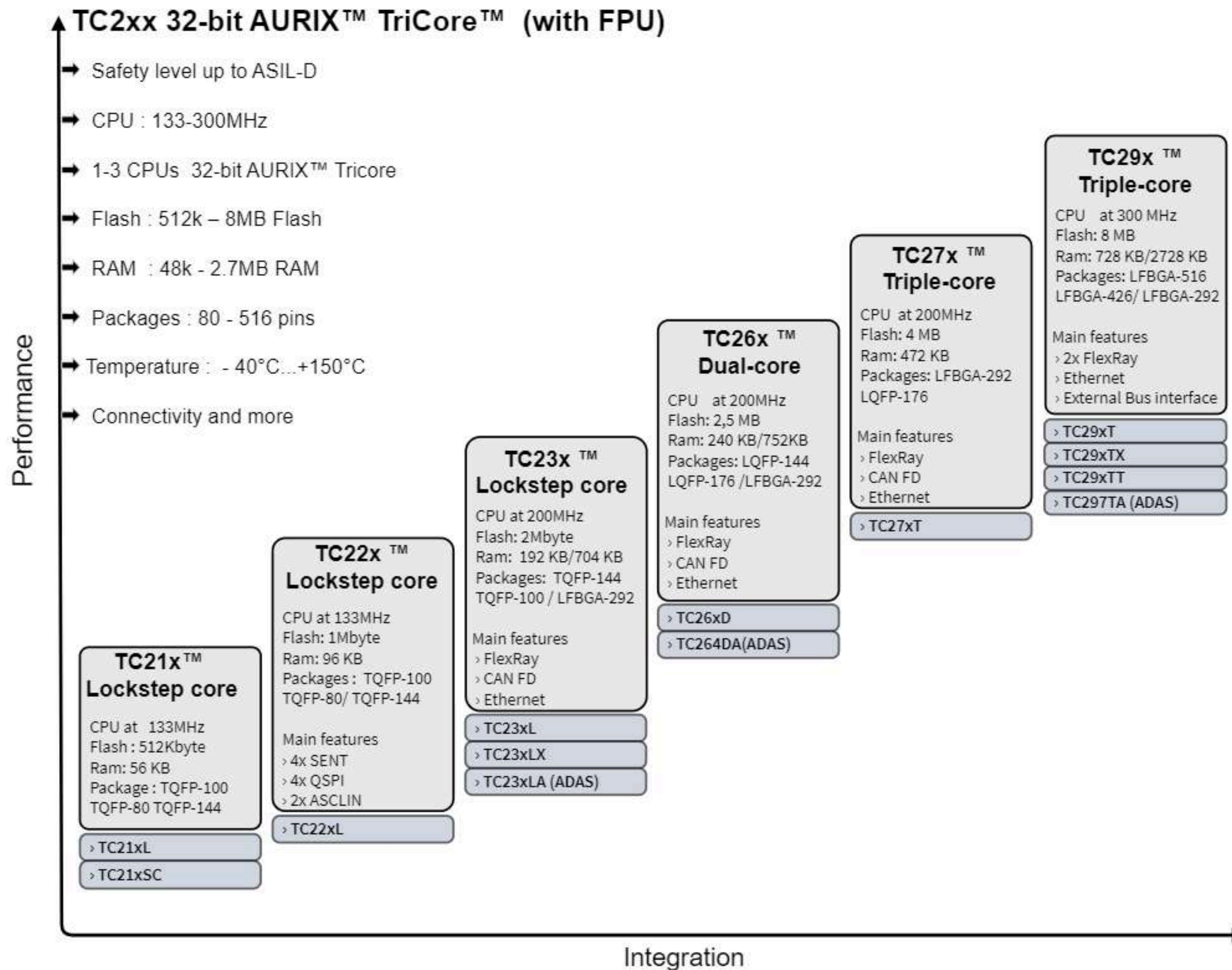


# O que é o TC2xx ?



# Sobre o TC2xx





9x series up to 8 MB					TC297 300 MHz	TC298 300 MHz	TC299 300 MHz
7x series up to 4 MB				TC275 200 MHz	TC277 200 MHz		
6x series up to 2.5 MB			TC264 200 MHz	TC265 200 MHz	TC267 200 MHz		
3x series up to 2 MB		TC233 200 MHz	TC234 200 MHz		TC237 200 MHz		
2x series up to 1 MB	TC222 133 MHz	TC223 133 MHz	TC224 133 MHz				
1x series up to 512 KB	TC212 133 MHz	TC213 133 MHz	TC214 133 MHz				
Flash Package	TQFP-80	TQFP-100	LQFP-144 TQFP-144	LQFP-176	LFBGA-292	BGA-416	LFBGA-516

# Características Principais



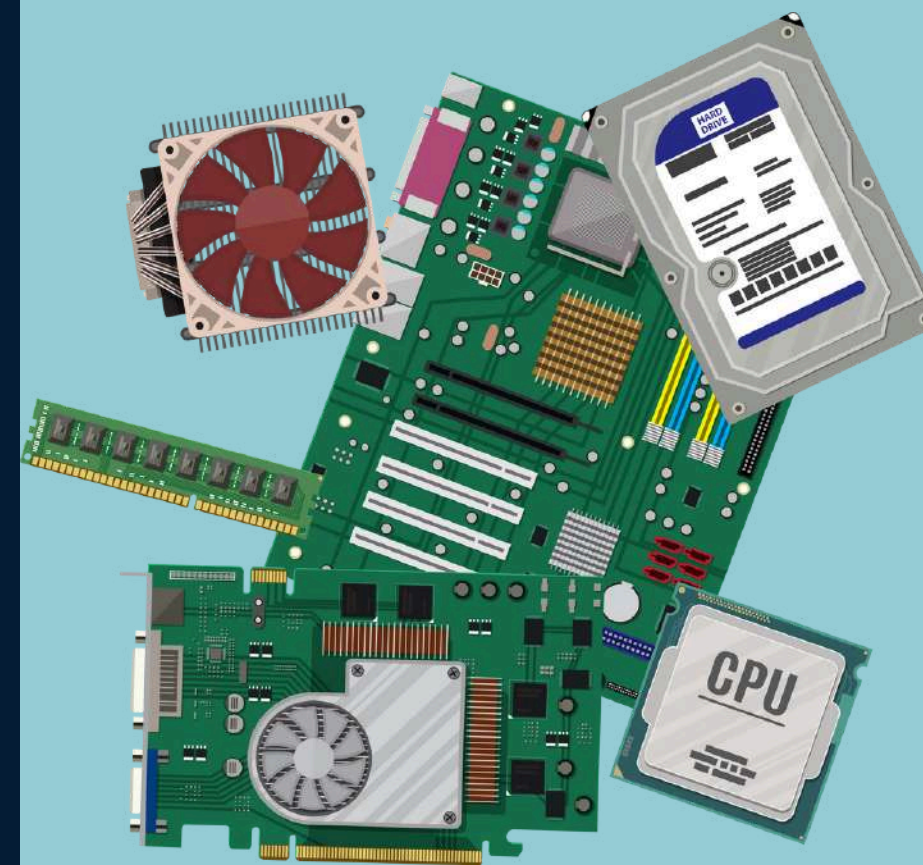
# ISA

- 4 GBytes de espaço de endereçamento
- Instruções de 16 e 32 bits para redução do tamanho do código
- Instruções de desvio (com previsão de desvio)
- Unidade de Ponto Flutuante (FPU) e Unidade de Gerenciamento de Memória (MMU)
- Operações SIMD (Single Instruction Multiple Data)
- Esquema flexível de priorização de interrupções
- Proteção de memória e suporte a depuração



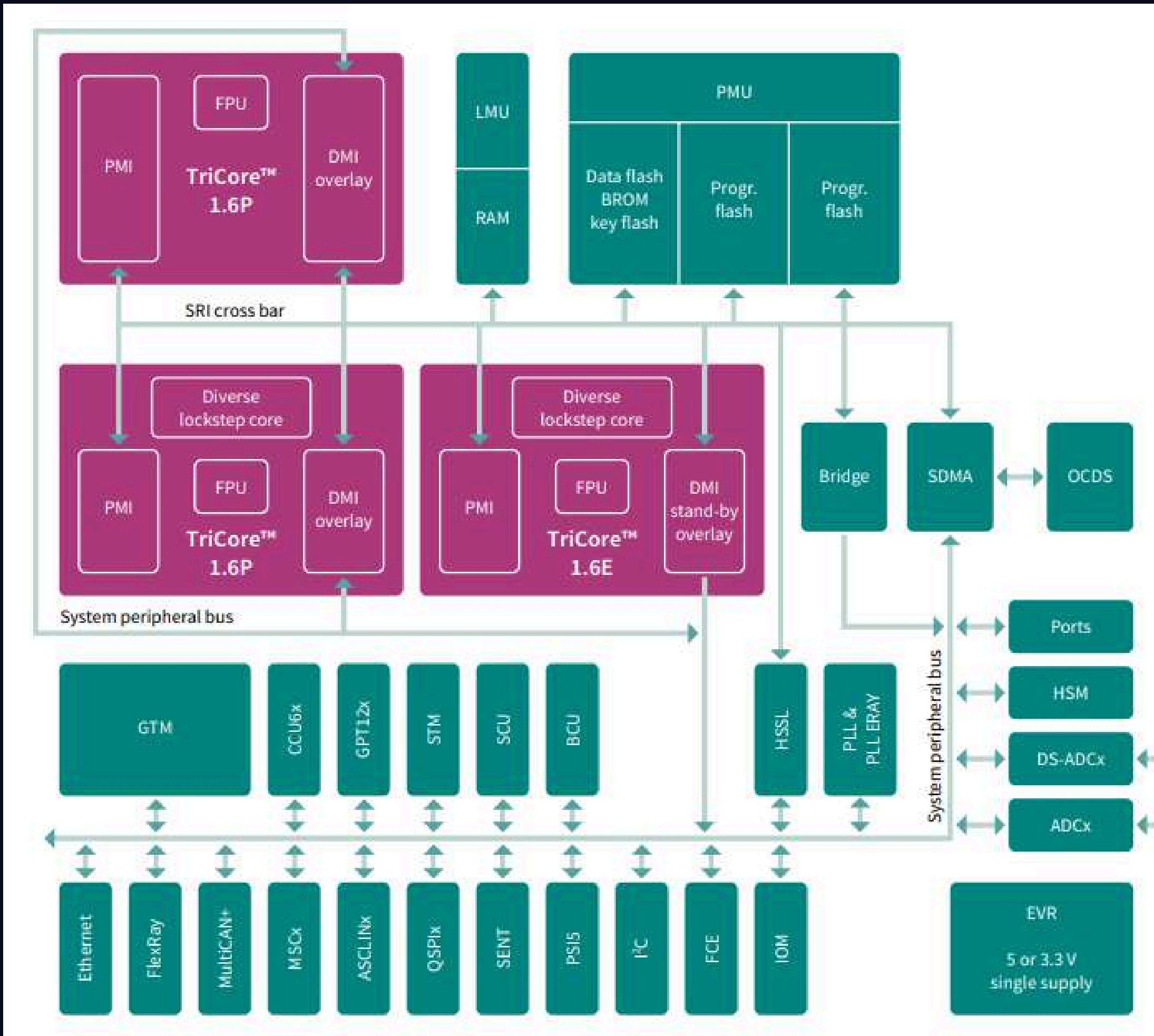
# Hardware

- 1-3 CPUs de 32 bits AURIX™ TriCore™
- CPUs: 133-300 MHz
- Nível de segurança até ASIL-D
- Flash: 512 KB – 8 MB de memória flash
- RAM: 48 KB - 2,7 MB de RAM
- Temperatura: -40°C a +150°C



# Organização





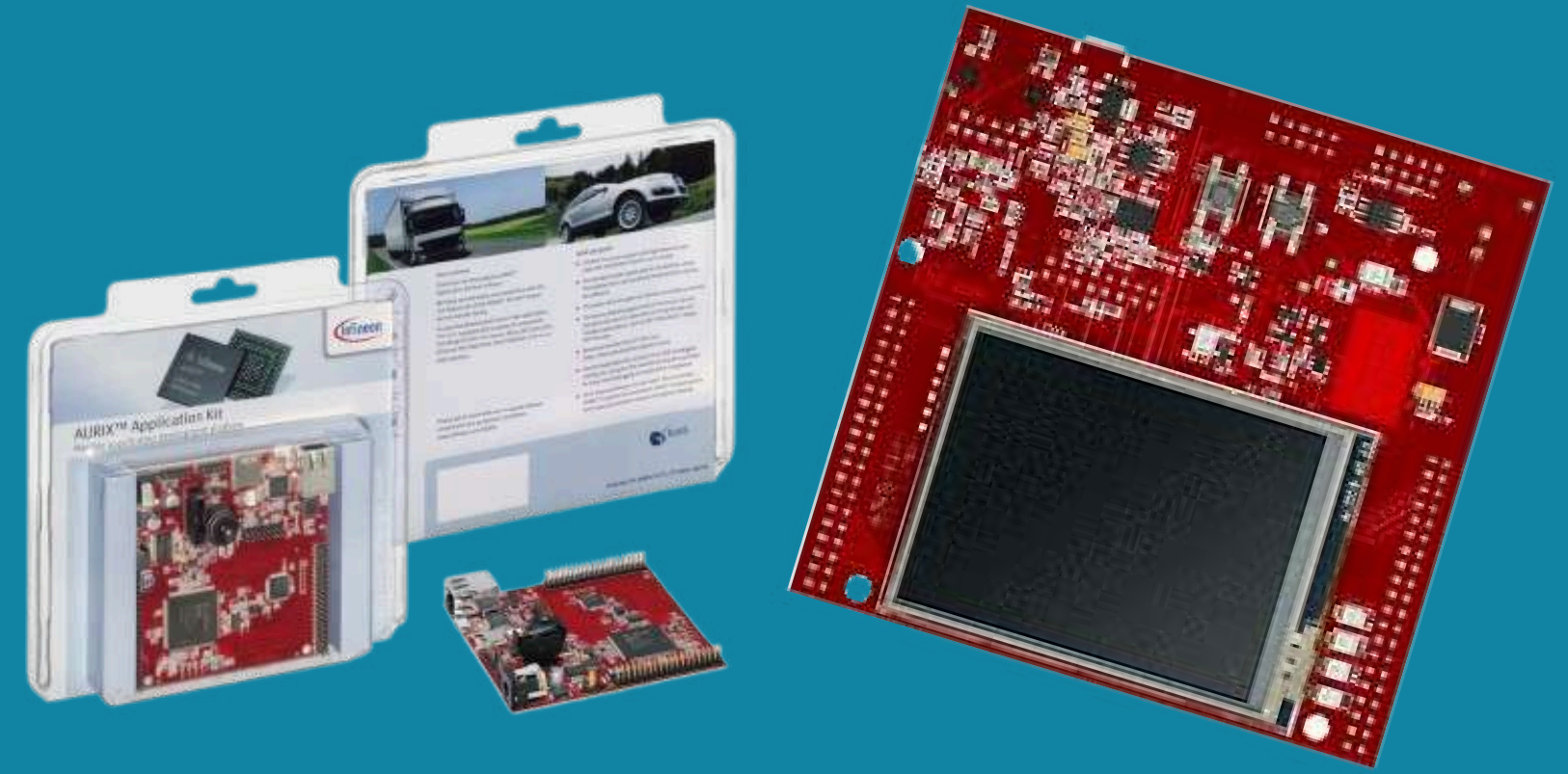
# Kits de Desenvolvimento



## AURIX™ TC2x TriBoards



## AURIX™ TC2x TFT kits



## AURIX™ TC2x Arduino ShieldBuddy kit



## AURIX™ TC2x lite kit





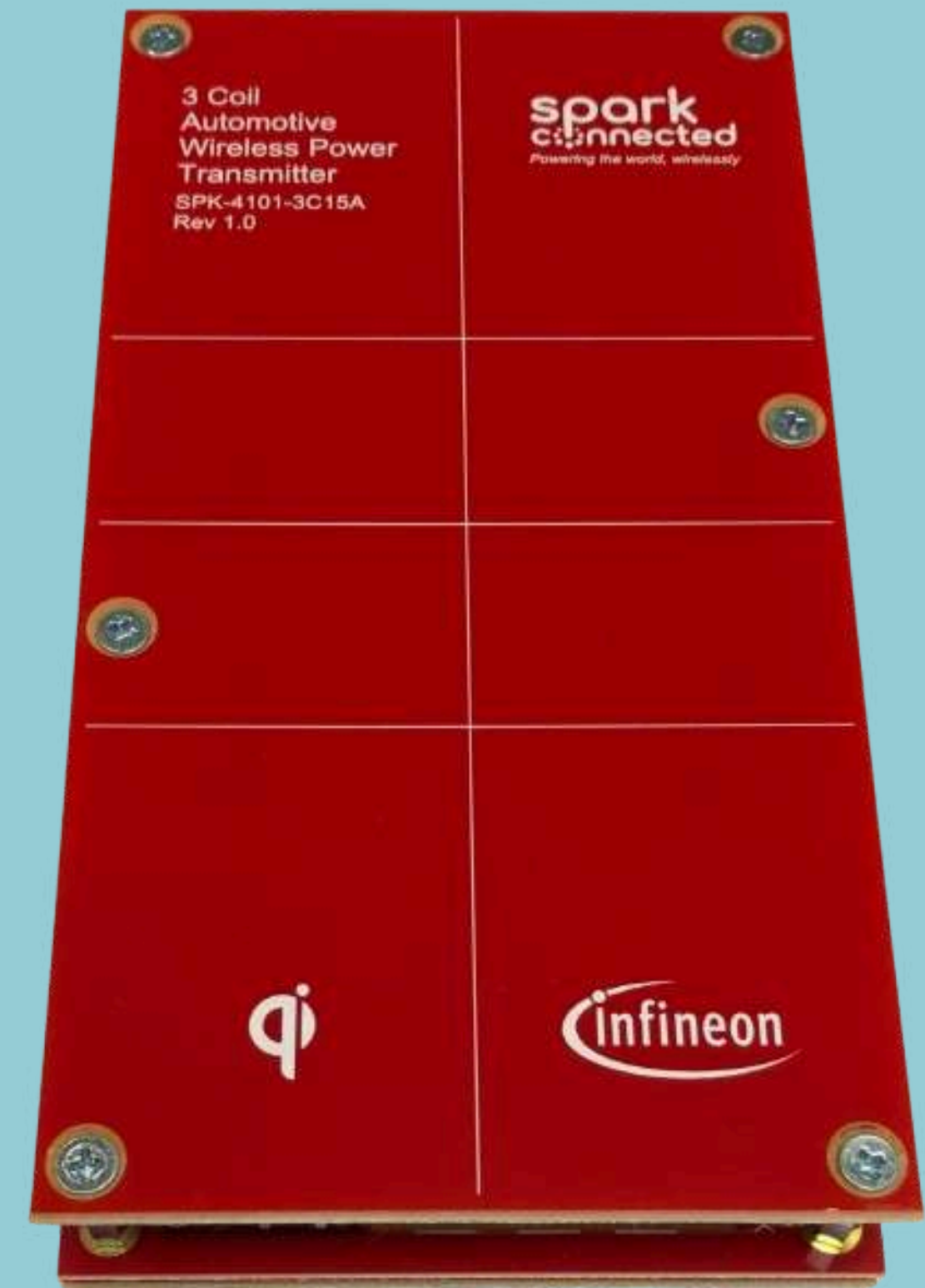
# Kits de Aplicação



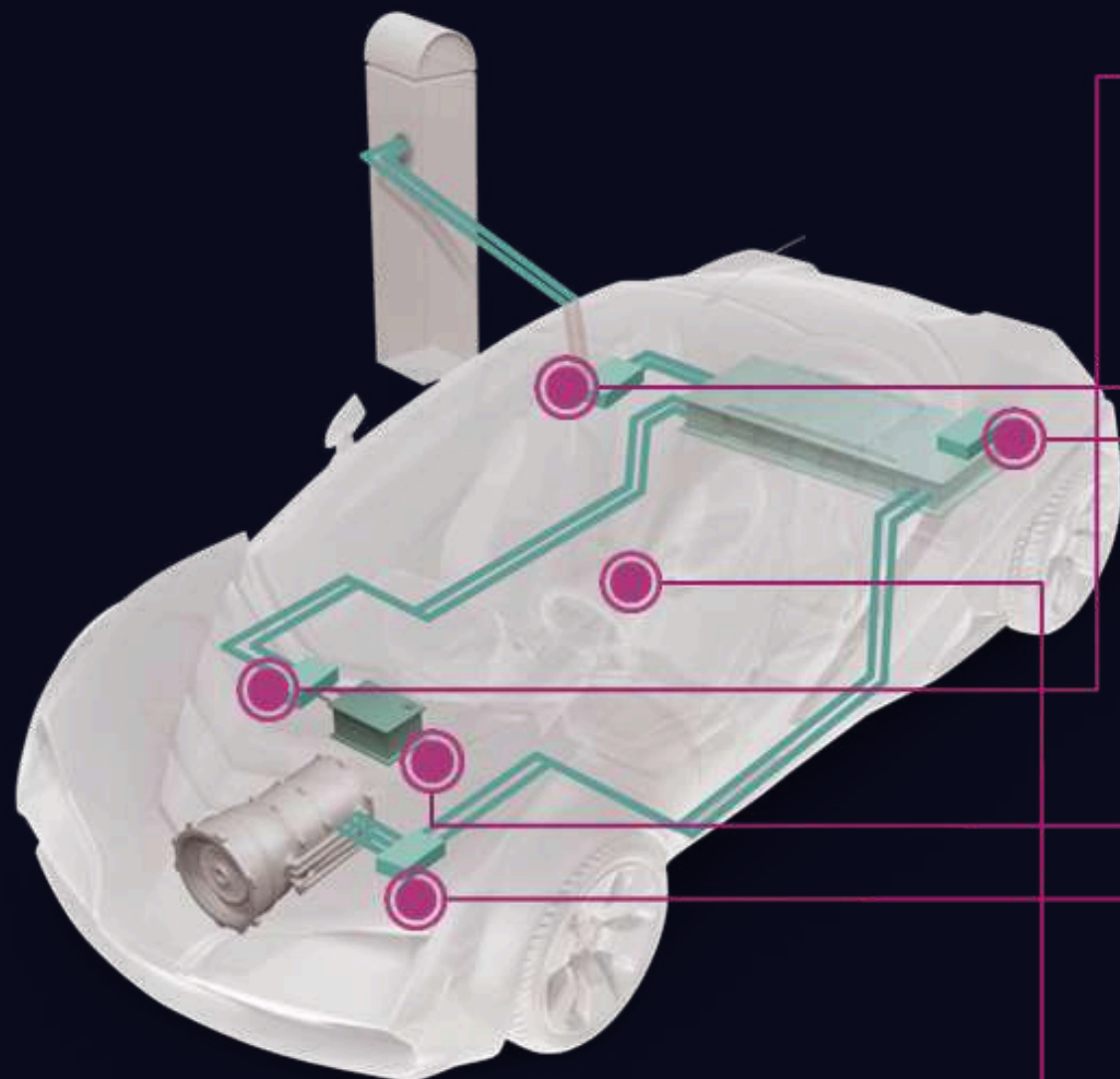
## KIT\_ATV\_24GHZ\_RADAR



## KIT\_AURIX\_TC21\_SC



# Mobilidade Elétrica



### AURIX™ for Main Inverter

Realiza o gerenciamento de torque e o posicionamento do rotor para um controle eficiente do motor.



### AURIX™ for DC/DC Converter

Gerencia a comunicação com a pilha AUTOSAR, bem como executa loops de controle para múltiplas fases (até 8).



### AURIX™ for Auxiliary Inverters/Converters

Aciona a bomba d'água, o compressor de HVAC, o aquecedor PTC, além de gerenciar o sistema térmico para um uso mais eficiente de energia.



### AURIX™ for Battery Management

Monitora e reage ao Estado de Saúde (SoH), Estado de Carga (SoC) e Profundidade de Descarga (DoD) com o auxílio de ASICs de balanceamento; previne manipulações ilegais tanto no sistema quanto nos pacotes de bateria por meio de fluxo de dados criptografado; monitora a segurança do sistema através de topologia em anel habilitada por SPI.



### AURIX™ for On-board Charger

Garante isolamento galvânico entre a rede elétrica e a bateria de alta tensão (HV-battery), além de fornecer cobrança segura de carga e habilitar várias classes de potência para tempos de carregamento flexíveis.



### AURIX™ for HCU & Domain Control

Gerencia a comunicação entre os submódulos, como o motor e os motores híbridos dentro de um veículo elétrico híbrido (Hybrid EV). Garante comunicação segura e suporte a Software Over the Air (SOTA) como gateway central para o domínio do powertrain na arquitetura E/E.

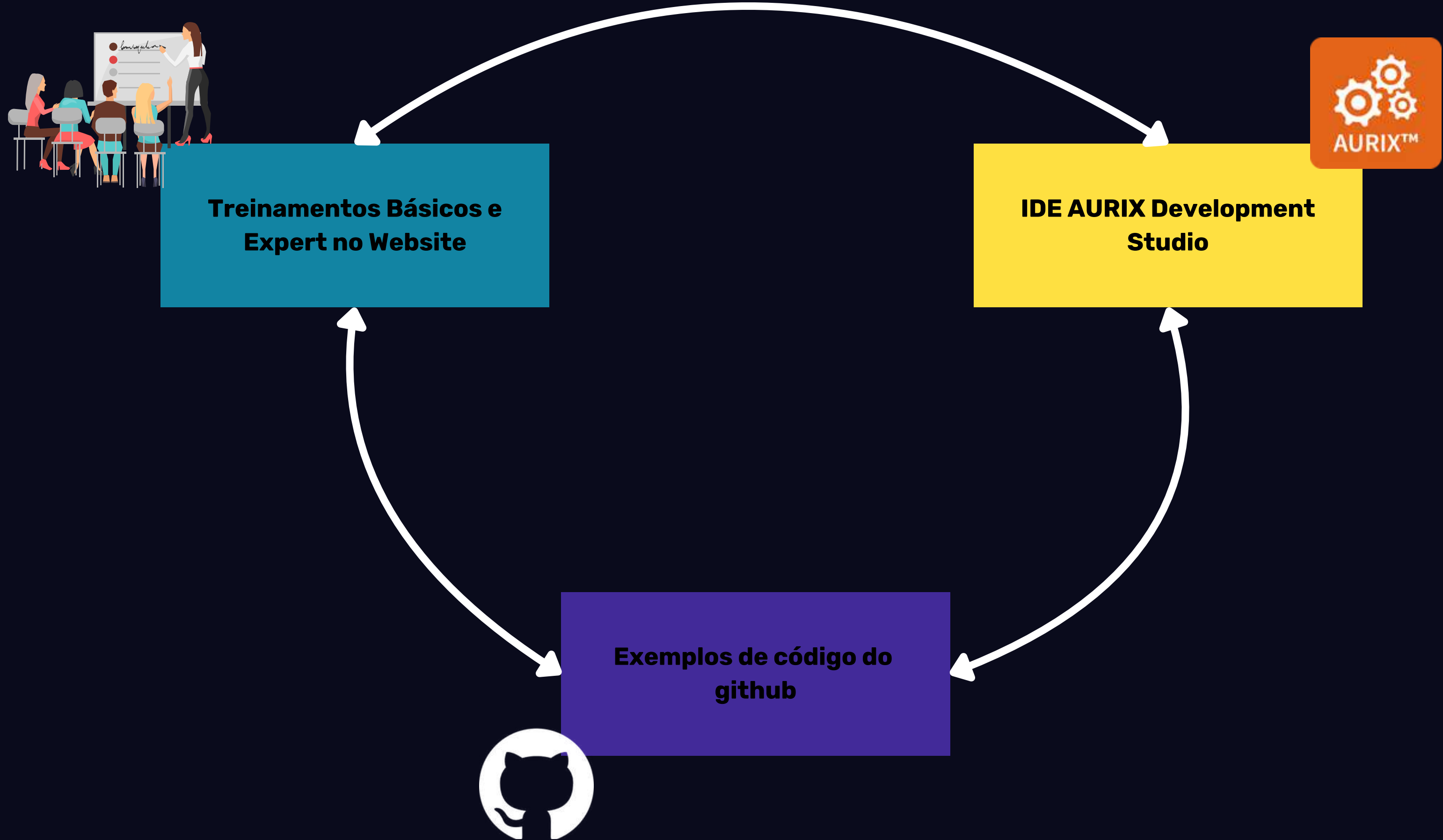


 Inverter/HCU	  DC-DC/OBC	 BMS
 VW MEB platform TC275	 Hyundai 48V Tucson TC264	 Daimler S Class 48V TC277
 Ford C-Max & F150 TC298	 Mazda Atenza TC264	 Kia Soul TC264
 Mercedes E Class TC277	 London Taxi Company TC265 & TC233	 Hyundai Sonata TC264
 Suzuki Ertiga TC233	 AUDI E-Tron TC277	 BMW 530 TC277
 AUDI Q5 HEV TC277	 SAIC Baojun730 TC275	 Next EV Nio TC275
 Renault Twizy & Zoe TC1782	 Hyundai Avante XC164	 BJEV EU260 TC1782
 Volvo XC90 TC1798	 VW Blue e-Motion TC1797	 AUDI A3 TC1797
 BMW i3 & i8 TC1797	 Mercedes S500 TC1797	 Mercedes SLS TC1797



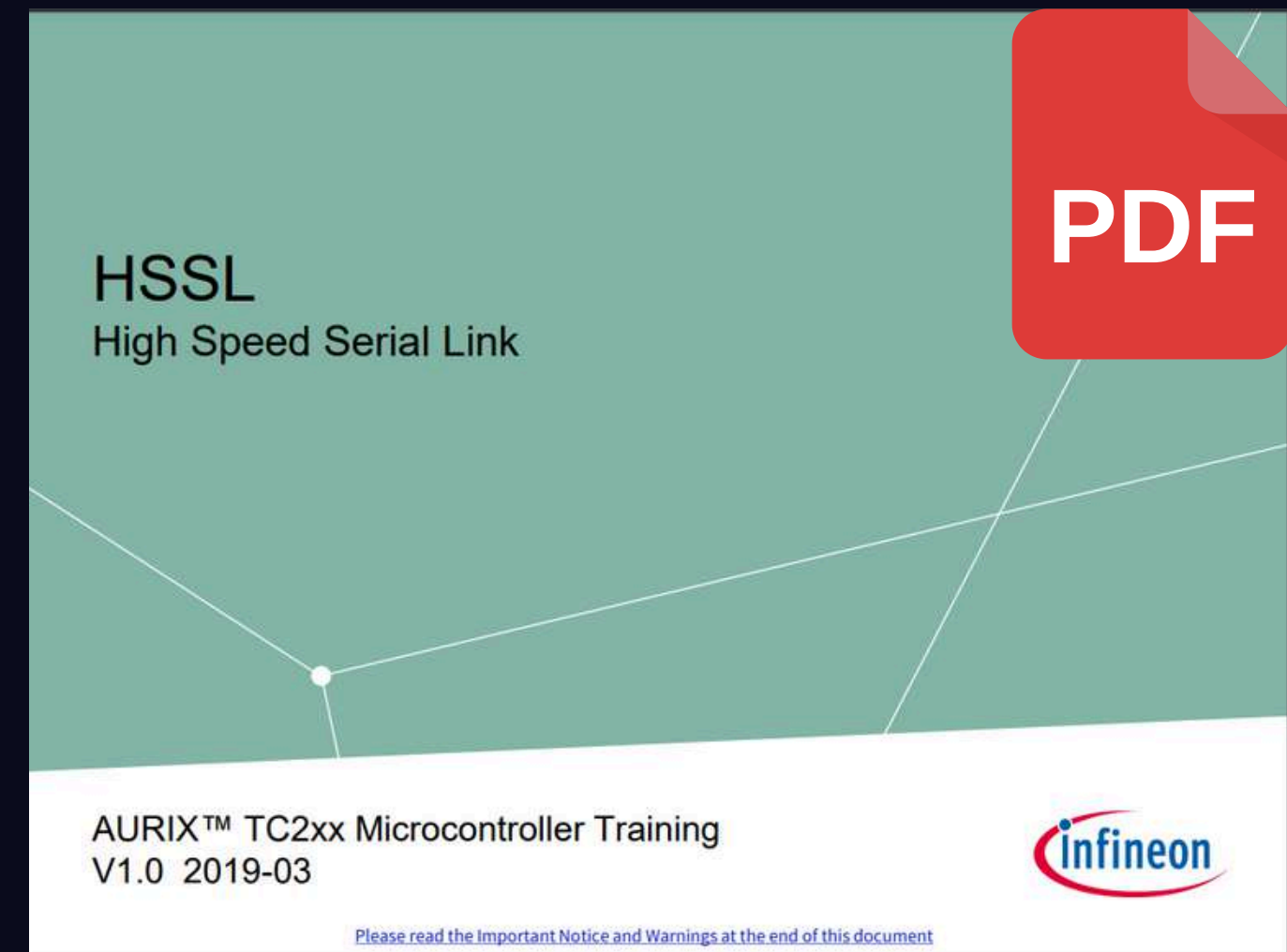
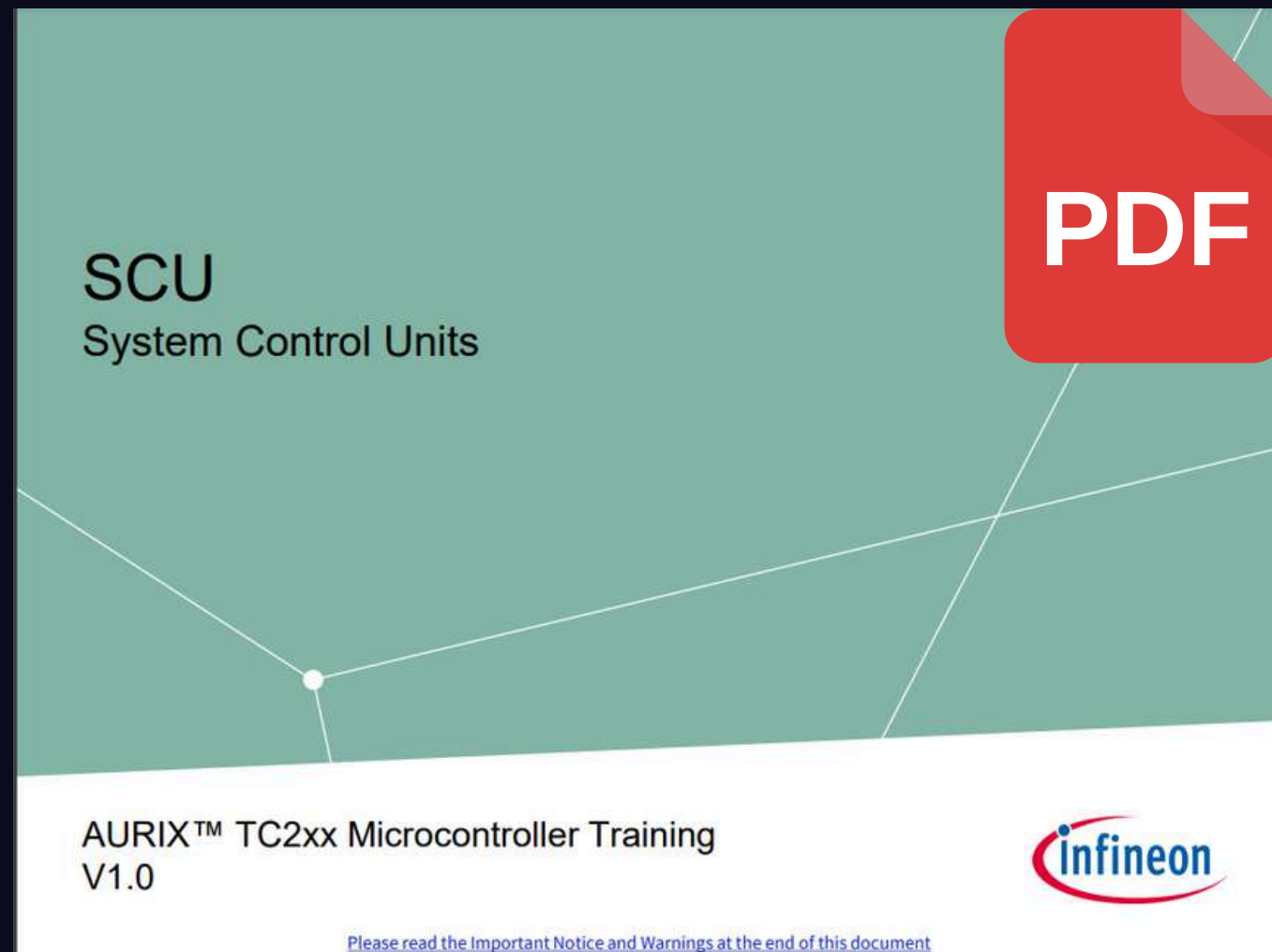
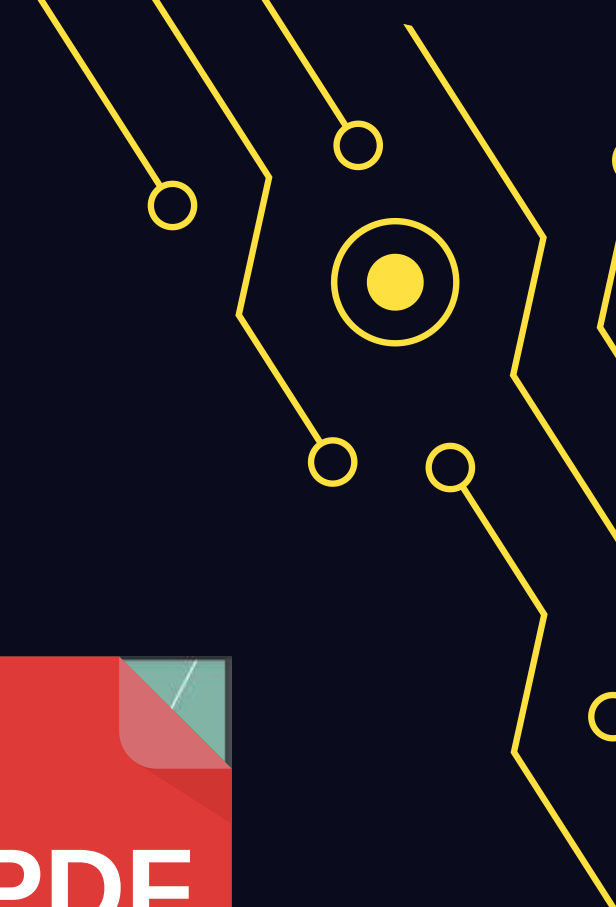


# Ambiente de Desenvolvimento



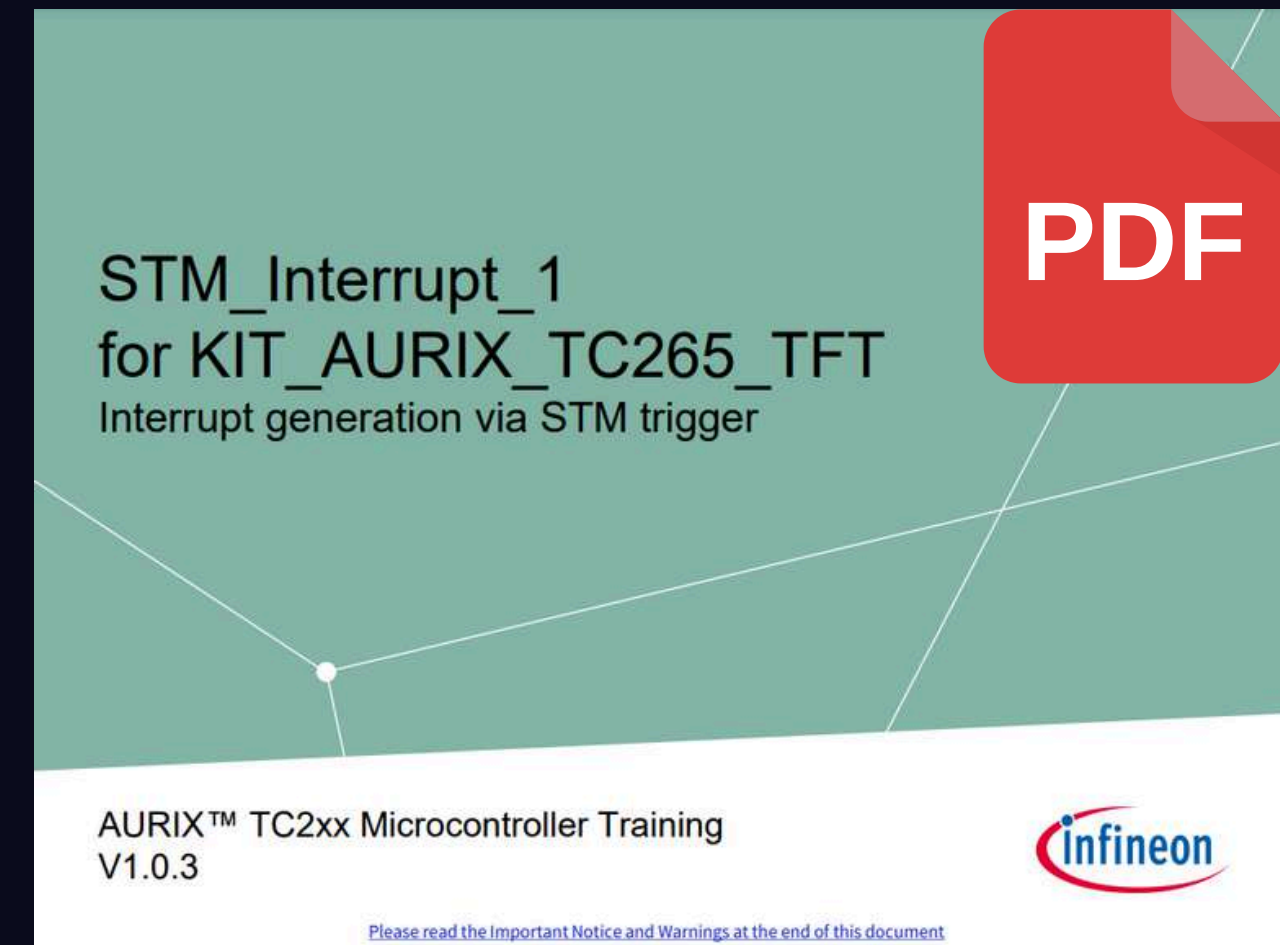


# Treinamentos “Rápidos”





# Treinamentos “Experts”







# Exemplos de código no Github

Communication Protocols	Data Handling	Timers	Analog/Digital Signals	CPU Management	Alarms, Interrupts and Resets
ASCLIN module handling for LIN, UART and SPI	Flexible CRC Engine control	Capture and Compare Unit control	Versatile Analog-to-Digital converter control	Assembly code execution	Device Reset type trigger and detection
Ethernet communication	Input-Output Monitor control	Clock System	Delta-Sigma Analog-to-Digital converter control	Code execution from SRAM	Interrupt handling
I2C communication	BUS register protection	General Purpose Timer control	GPIO port control	Performance registers usage	Safety Management Unit control
CAN communication	Memory Protection control	Generic Timer Module control		Power Management System control	
SPI communication	Data and Program Flash programming	System Timer Module control		Multicore	
	DMA control			Trap recognition	
	Memory test			Watchdog handling	



```
int core0_main(void)
{
    IfxCpu_enableInterrupts();
    /* Wait for CPU sync event */
    IfxCpu_emitEvent(&g_cpuSyncEvent);
    IfxCpu_waitEvent(&g_cpuSyncEvent, 1);

    /* Initialize the VADC module and start the background scan */
    initADC();
}
```

C Cpu0\_Main.c  
C Cpu1\_Main.c  
C Cpu2\_Main.c



```
int core1_main(void)
{
    IfxCpu_enableInterrupts();
    /* Wait for CPU sync event */
    IfxCpu_emitEvent(&g_cpuSyncEvent);
    IfxCpu_waitEvent(&g_cpuSyncEvent, 1);
}
```



```
int core2_main(void)
{
    IfxCpu_enableInterrupts();
    /* Wait for CPU sync event */
    IfxCpu_emitEvent(&g_cpuSyncEvent);
    IfxCpu_waitEvent(&g_cpuSyncEvent, 1);
}
```



# IDE AURIX Development Studio

Import AURIX Development Studio Project

Select an AURIX Development Studio Project to import

Select a Code Examples repository  
Infineon Code Examples Repository

Repository root  
Browse...

Search Code Examples  
Search Code Examples

Select a project to import 300 Projects

Name	Abstract	Boards	Last Updated	Documents	Keywords
<input type="checkbox"/> ADC_Background_Scan_1_KIT_TC	The Versatile Analog-to-Digital Converter (VAD) is configured to measure multiple analog signals.	AURIX TC275 lite Kit, KIT_AU	29.06.2021	<a href="https://www.infineon.com">https://www.infineon.com</a>	ADC, background
<input type="checkbox"/> ADC_Background_Scan_1_KIT_TC	The Versatile Analog-to-Digital Converter (VAD) is configured to measure multiple analog signals.	APPLICATION KIT TC2X7 V1.	18.12.2020	<a href="https://www.infineon.com">https://www.infineon.com</a>	ADC, background
<input type="checkbox"/> ADC_Filtering_1_KIT_TC275_LK	Four VADC channels are used to convert the sampled analog signal with different filters enabled.	AURIX TC275 lite Kit, KIT_AU	29.06.2021	<a href="https://www.infineon.com">https://www.infineon.com</a>	ADC, ADC_Filtering
<input type="checkbox"/> ADC_Filtering_1_KIT_TC297_TFT	Four VADC channels are used to convert the sampled analog signal with different filters enabled.	APPLICATION KIT TC2X7 V1.	18.12.2020	<a href="https://www.infineon.com">https://www.infineon.com</a>	ADC, ADC_Filtering
<input type="checkbox"/> ADC_Filtering_1_KIT_TC375_LK	Four EVADC channels are used to convert the sampled analog signal with different filters enabled.	AURIX TC375 lite Kit, KIT_A2	22.03.2021	<a href="https://www.infineon.com">https://www.infineon.com</a>	ADC, ADC_Filtering
<input type="checkbox"/> ADC_Filtering_1_KIT_TC397_TFT	Four EVADC channels are used to convert the sampled analog signal with different filters enabled.	APPLICATION KIT TC3X7 V2.	18.12.2020	<a href="https://www.infineon.com">https://www.infineon.com</a>	ADC, ADC_Filtering
<input type="checkbox"/> ADC_Group_Scan_1_KIT_TC275_L	The Versatile Analog-to-Digital Converter (VAD) is configured to measure multiple analog signals.	AURIX TC275 lite Kit, KIT_AU	29.06.2021	<a href="https://www.infineon.com">https://www.infineon.com</a>	ADC, ADC_Group
<input type="checkbox"/> ADC_Group_Scan_1_KIT_TC297_T	The Versatile Analog-to-Digital Converter (VAD) is configured to measure multiple analog signals.	APPLICATION KIT TC2X7 V1.	29.06.2021	<a href="https://www.infineon.com">https://www.infineon.com</a>	ADC, ADC_Group

Description of ADC\_Filtering\_1\_KIT\_TC375\_LK

The Enhanced Versatile Analog-to-Digital Converter (EVADC) module is configured to convert four channels. The data resulting from the conversions of three channels is automatically modified: one channel computes an average on 4 results, another channel applies a 3rd order Finite Impulse Response (FIR) filter and another channel applies a 1st order Infinite Impulse Response (IIR) filter.

? < Back Next > Finish Cancel





# Bibliografia

- Site Oficial
  - <https://www.infineon.com/cms/en/product/microcontroller/32-bit-tricore-microcontroller/32-bit-tricore-aurix-tc2xx/>
- Software
  - <https://www.infineon.com/cms/en/tools/aurix-embedded-sw/>
  - [https://github.com/Infineon/AURIX\\_code\\_examples](https://github.com/Infineon/AURIX_code_examples)
- Hardware
  - [https://www.infineon.com/dgdl/Infineon-Power\\_and\\_Sensing-ProductSelectionGuide-v04\\_00-EN.pdf?fileId=8ac78c8c90530b3a0190e3b892a95f09&da=t](https://www.infineon.com/dgdl/Infineon-Power_and_Sensing-ProductSelectionGuide-v04_00-EN.pdf?fileId=8ac78c8c90530b3a0190e3b892a95f09&da=t)
  - [https://www.infineon.com/dgdl/Infineon-TriCore\\_Family\\_BR-ProductBrochure-v01\\_00-EN.pdf?fileId=5546d4625d5945ed015dc81f47b436c7](https://www.infineon.com/dgdl/Infineon-TriCore_Family_BR-ProductBrochure-v01_00-EN.pdf?fileId=5546d4625d5945ed015dc81f47b436c7)



**Perguntas?**



# Obrigado!