

Title		
Size	Number	Revision
A4		
Date:	5.17.2021	Sheet of
File:	G:\Проекты по разработке плат в Альтим\У.УСОЕХ\Documents\Bors schematics.SchDoc	

A

B

C

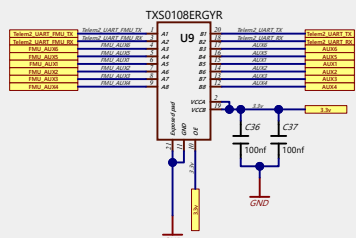
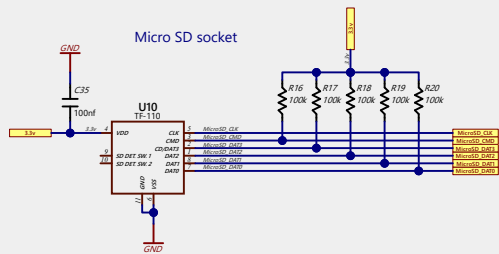
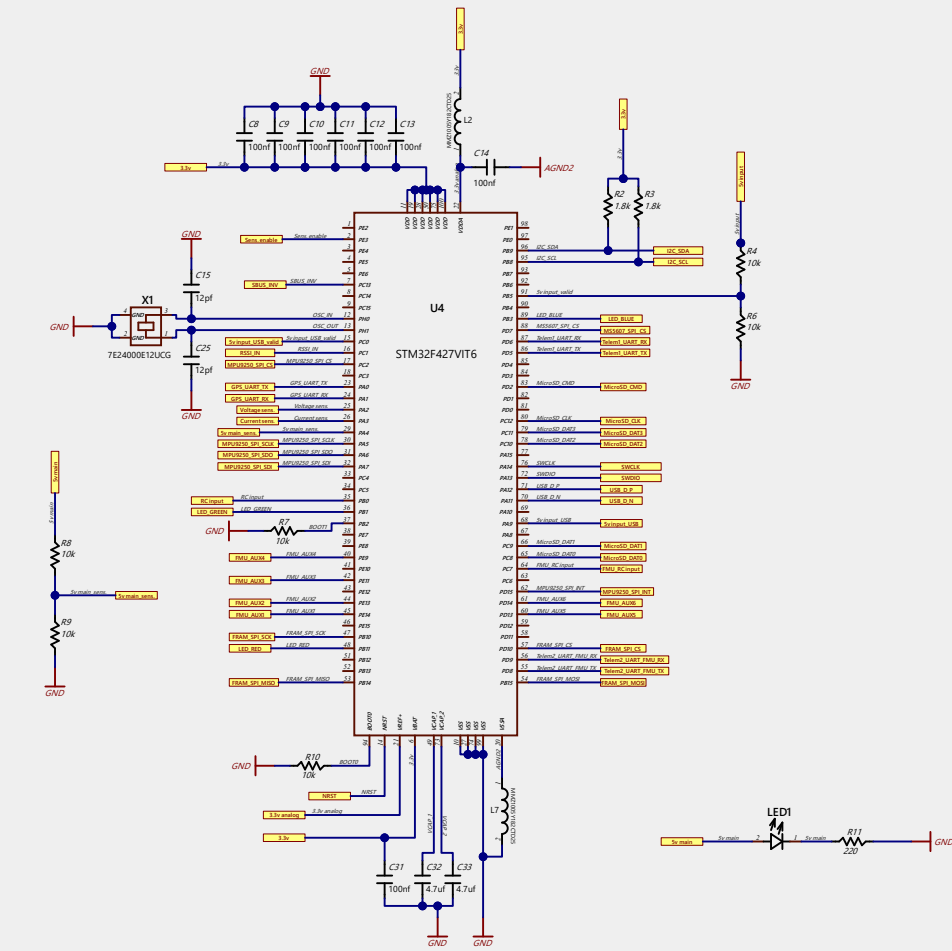
D

A

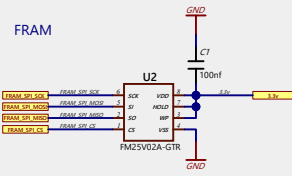
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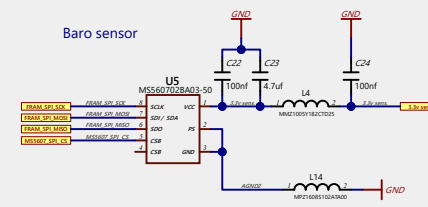
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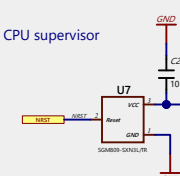
FRAM



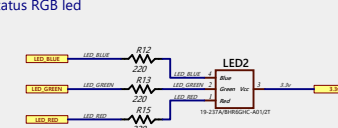
Baro sensor



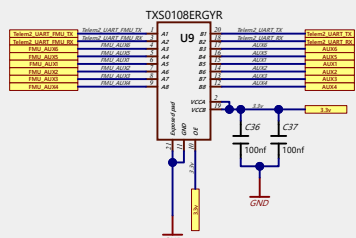
CPU supervisor



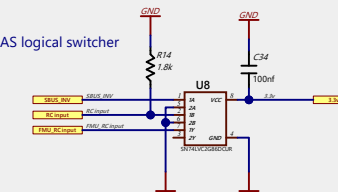
Status RGB led



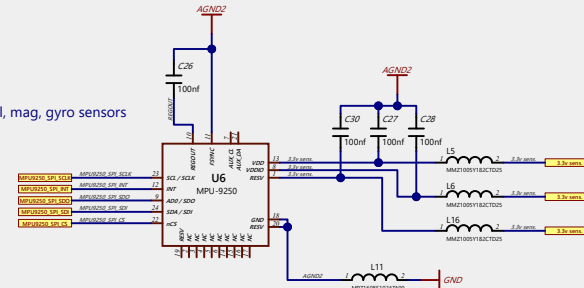
PWM and UART level translator



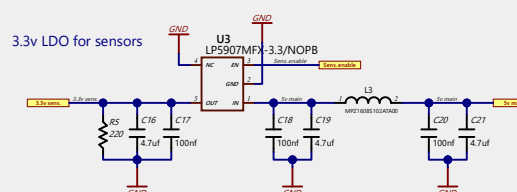
PPM - SBAS logical switcher



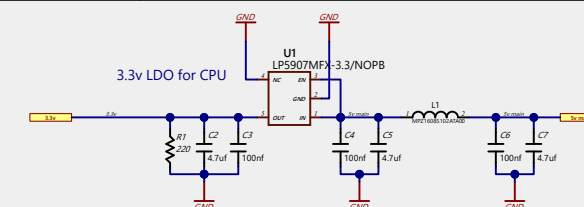
Accel, mag, gyro sensors



3.3v LDO for sensors



3.3v LDO for CPU



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