DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

THAPAR INSTITUTE
OF ENGINEERING & TECHNOLOGY
(Deemed to be University)

Analog IC Design

Experiment-6

Submitted by

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Aim:

To design a common source amplifier with current mirror load for gain of 10 and analyse its transient characteristics.

Tool Used:

LTspice tool

Theory:

When the input signal is applied at the gate terminal and source terminal, then the output voltage is amplified and obtained across the resistor at the load in the drain terminal. This is called a common source amplifier.

Common source amplifier is similar to the common-emitter follower of Bipolar Junction transistor. If we use P-channel FET, the polarity of the input voltage will be reversed.

For a NMOS let's assume

VDD = 1.8V, VT = 0.4V, VGS = 0.6V, Kn = 120µA/V^2,

For a PMOS let's assume

VDD = 1.8V, VT = -0.4V, VGS = 0.6V, Kp = 120µA/V^2,

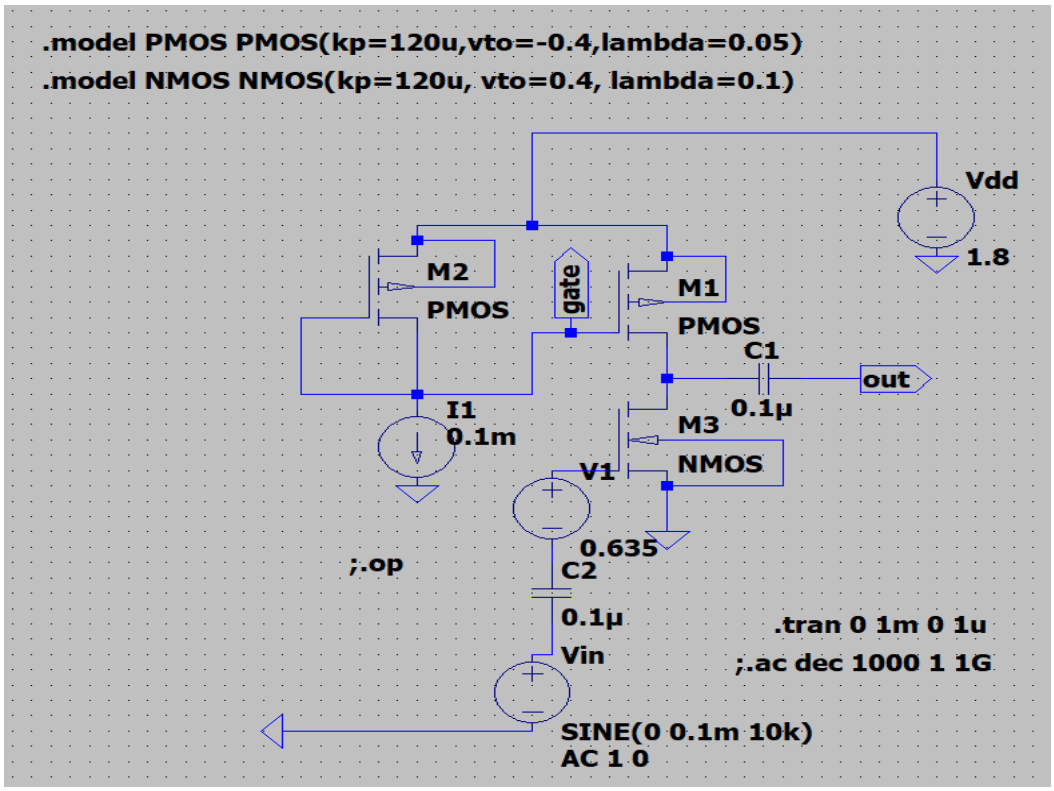
Which gives a value of (W/L) = (29.6) for 1mA ID.

Also, for these values' gm is attained as 10mΩ-1, therefore for gain 10, RD is taken as 1KΩ.

The value of VDS should be maintained above (VGS - VT = 0.6 - 0.4 = 0.2V) for the transistor to stay in saturation region.

As for M3, the width is taken as 296µm and the length is taken as 10µm and for M1 and M2, the width is taken as 10 µm and the length is taken as 10 µm.

Circuit Schematic:



DC Operating Point

* C:\Users\singh\Documents\Analog IC Design Lab\Lab4\common gat

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        --- Operating Point ---

V(n001):        1.8             voltage
V(n002):        0.405172        voltage
V(gate):        0.6             voltage
V(n003):        0               voltage
V(out):         4.05171e-007    voltage
Id(M2):         4.62144e-005    device_current
Ig(M2):         0               device_current
Ib(M2):         -4.15172e-013   device_current
Is(M2):         -4.62144e-005   device_current
Id(M1):         4.62144e-005    device_current
Ig(M1):         0               device_current
Ib(M1):         -1.40483e-012   device_current
Is(M1):         -4.62144e-005   device_current
I(C2):          4.05171e-019    device_current
I(C1):          -6e-019         device_current
I(R2):          6e-006          device_current
I(R1):          6e-006          device_current
I(V2):          6e-019          device_current
I(V1):          -5.22144e-005   device_current
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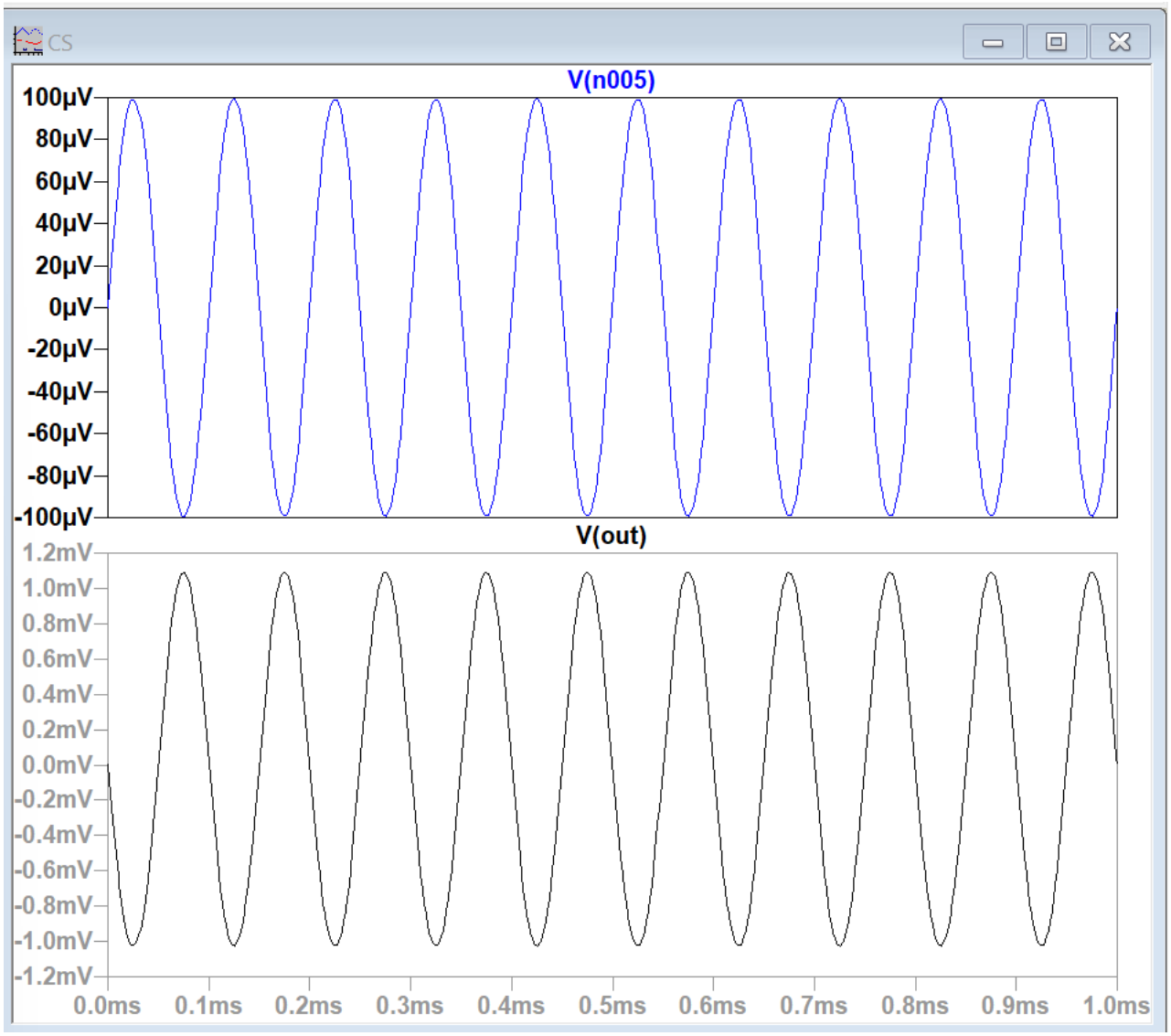
Output Waveforms:

Transient Response:



Result:

The circuit is designed for a gain of 10 and the output is verified to be correct.