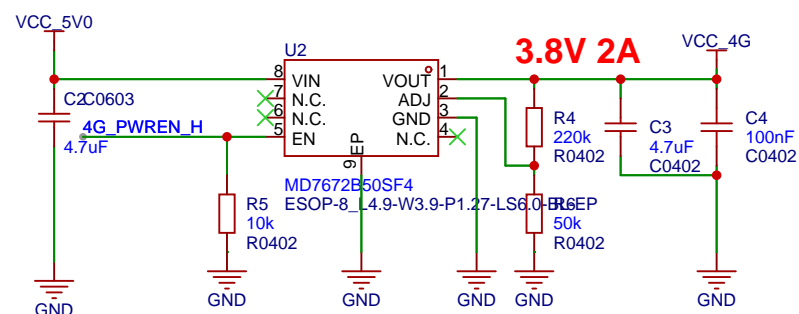

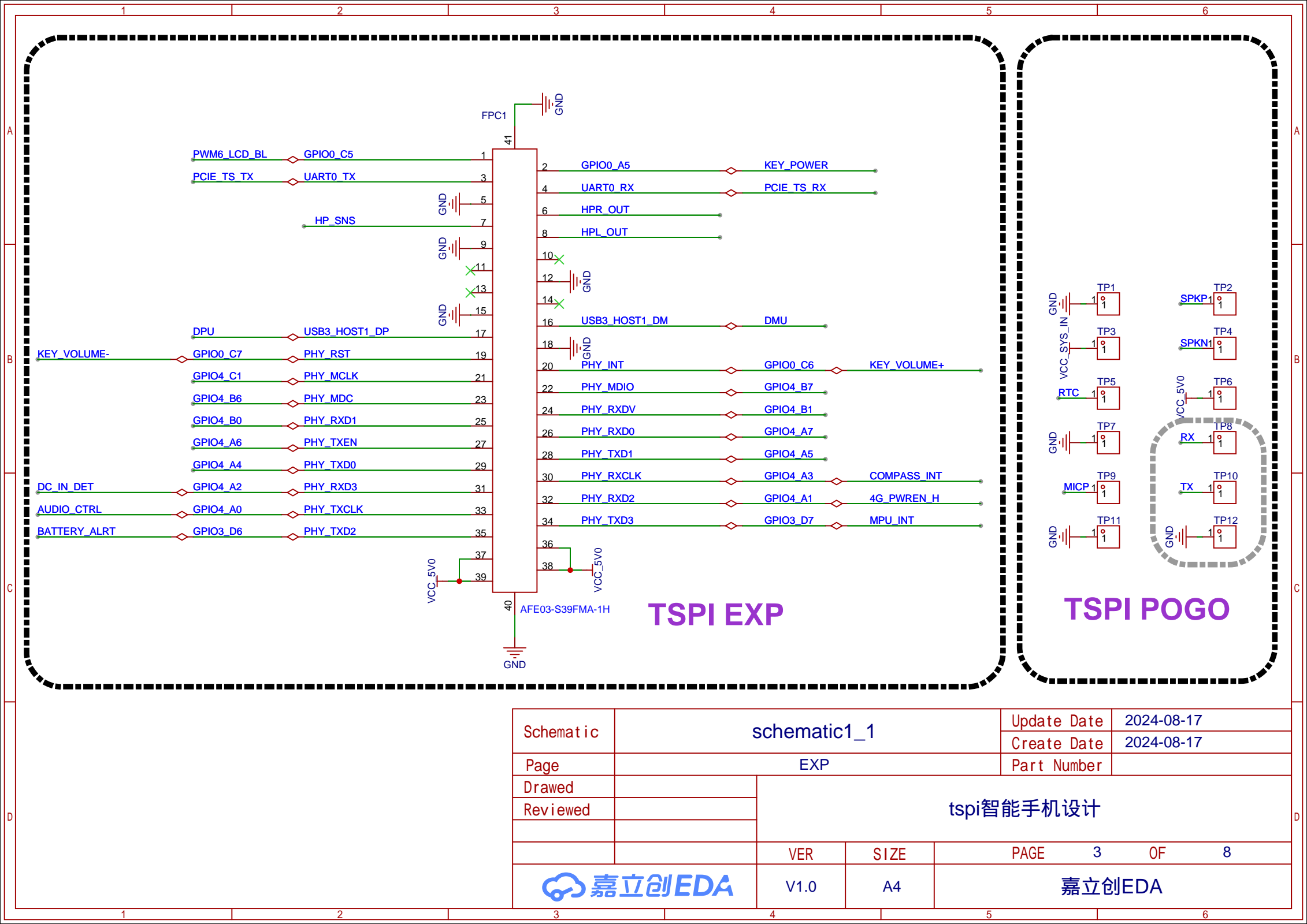


POWER PD IN

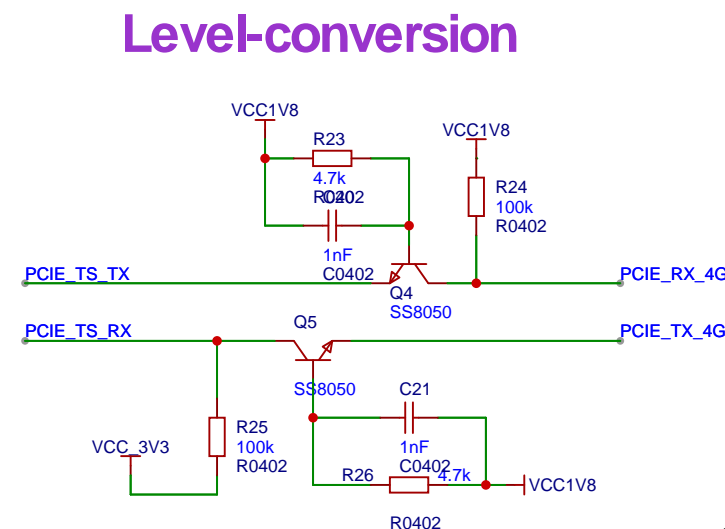
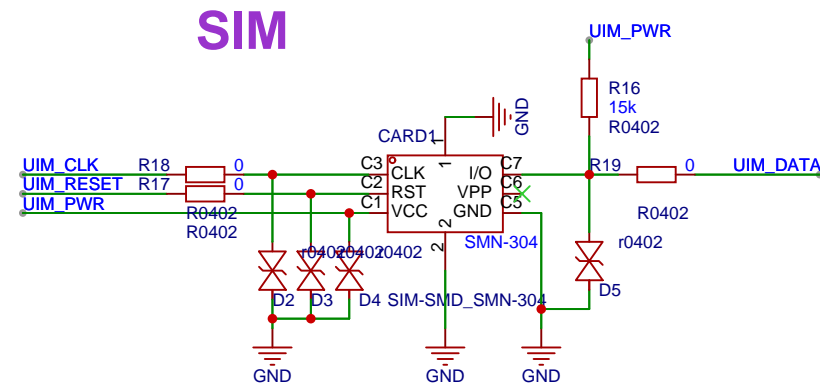
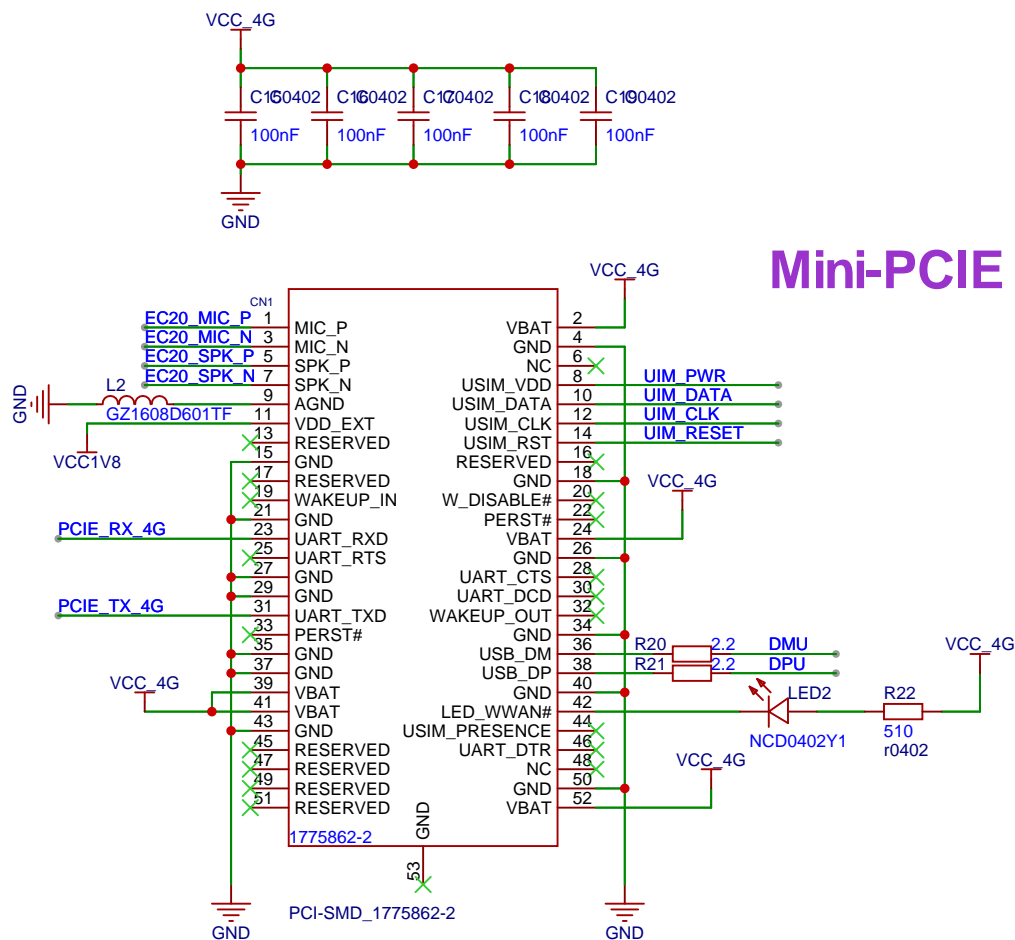


VCC 4G FOR EC20

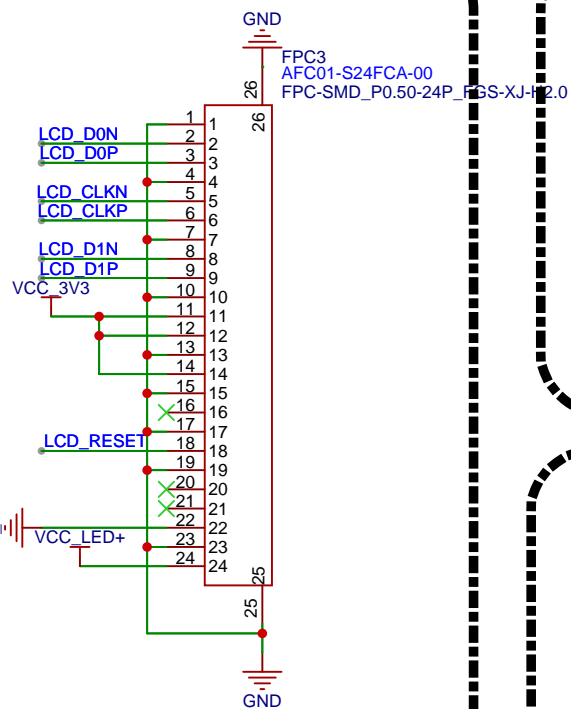
Schematic	schematic1_1			Update Date	2024-08-17
				Create Date	2024-08-17
Page	POWER			Part Number	
Drawed		tspi智能手机设计			
Reviewed					
		VER	SIZE	PAGE	1 OF 8
		V1.0	A4	嘉立创EDA	



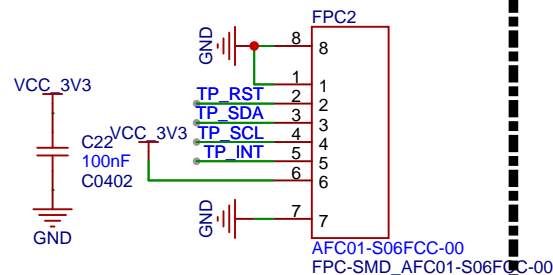
Schematic	schematic1_1			Update Date	2024-08-17
				Create Date	2024-08-17
Page	EXP			Part Number	
Drawn		tspi智能手机设计			
Reviewed					
		VER	SIZE	PAGE	3 OF 8
嘉立创EDA		V1.0	A4	嘉立创EDA	



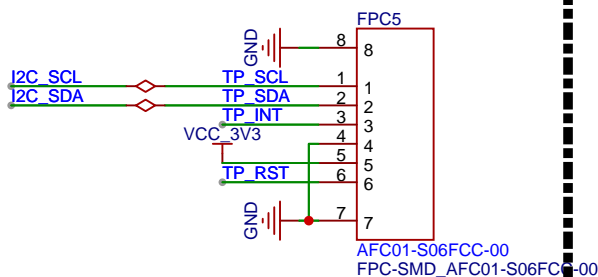
Schematic	schematic1_1			Update Date	2024-08-17
Page	PCIE			Create Date	2024-08-17
Drawn				Part Number	
Reviewed				tspi智能手机设计	
		VER	SIZE	PAGE	4 OF 8
		V1.0	A4	嘉立创EDA	



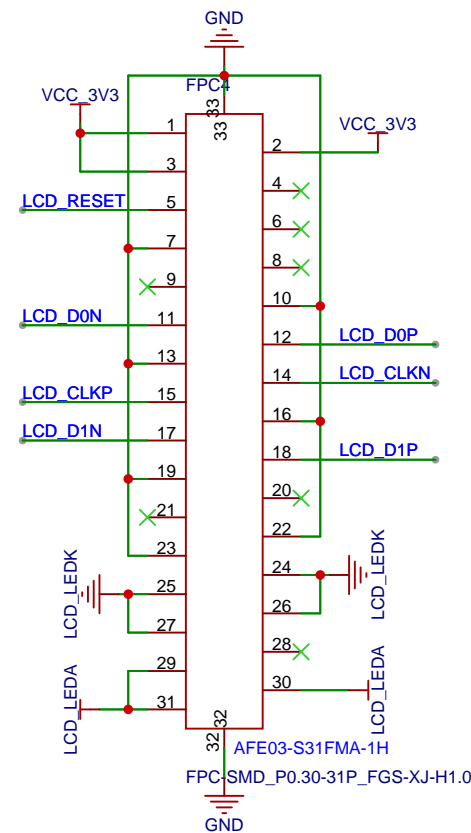
MPIP LCD



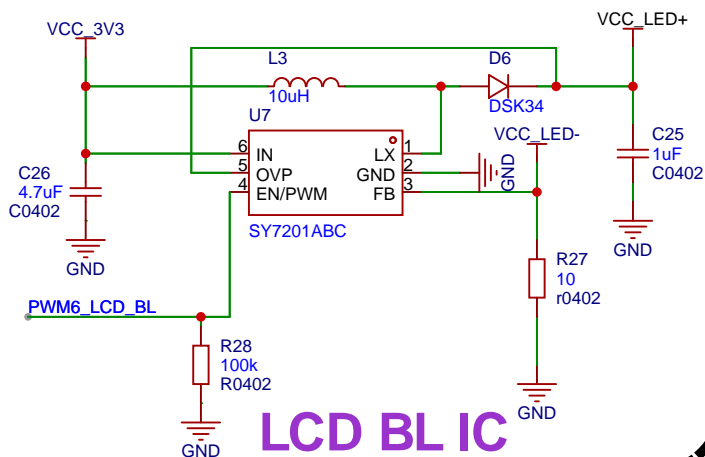
TP




TSPI TP/I2C1

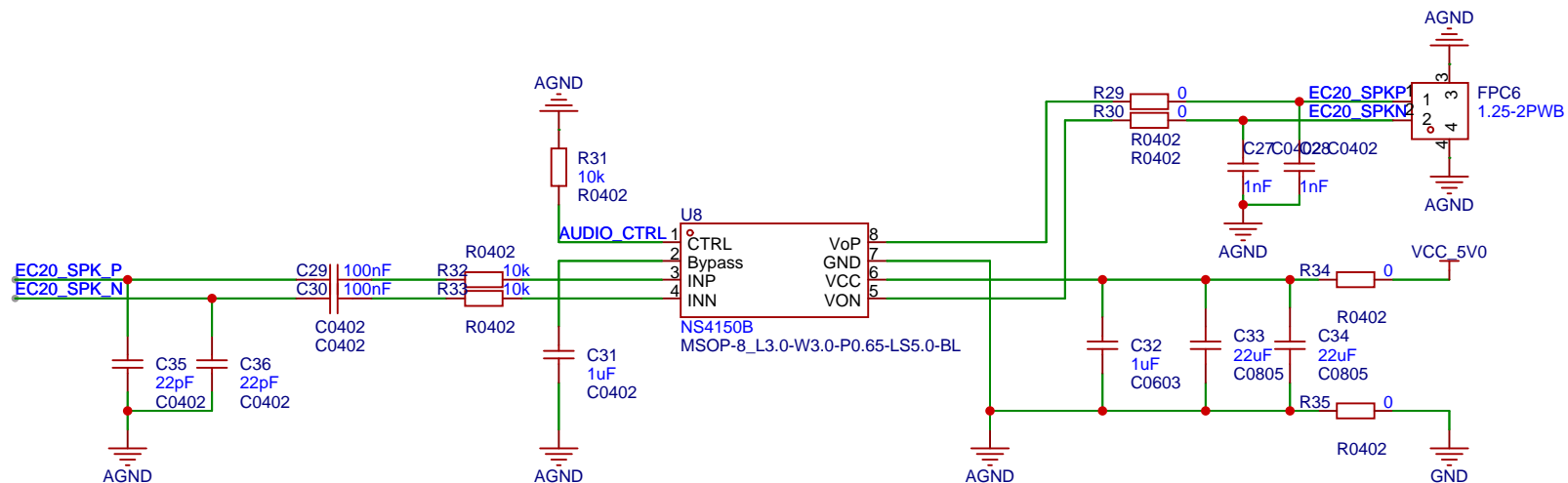


TSPI DSI

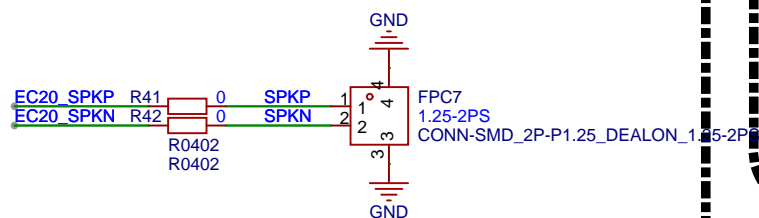


LCD BL IC

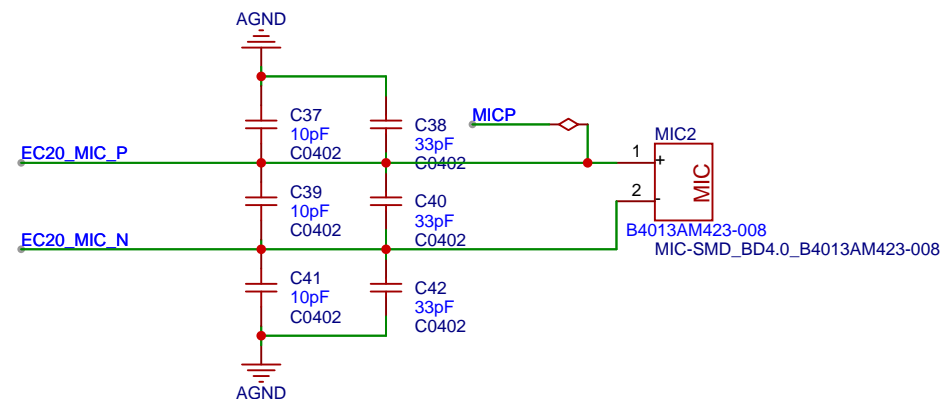
Schematic	schematic1_1			Update Date	2024-08-17
				Create Date	2024-08-17
Page	LCD			Part Number	
Drawed		tspi智能手机设计			
Reviewed					
		VER	SIZE	PAGE	5 OF 8
		V1.0	A4	嘉立创EDA	




AFA FOR EC20



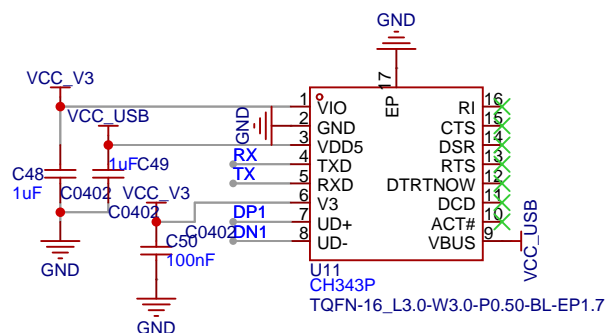
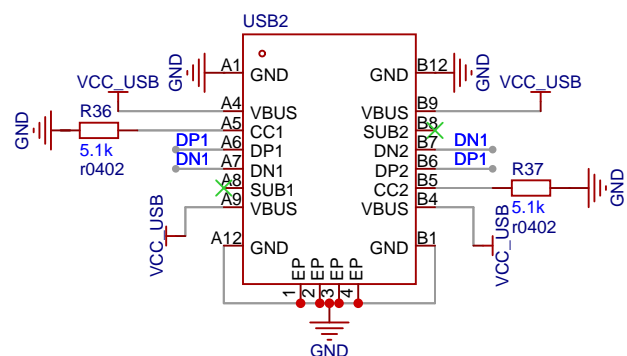
TSPI SPK



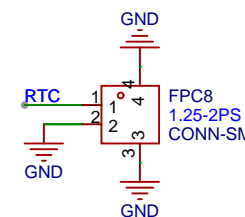
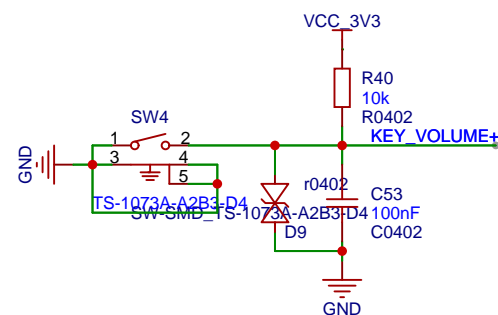
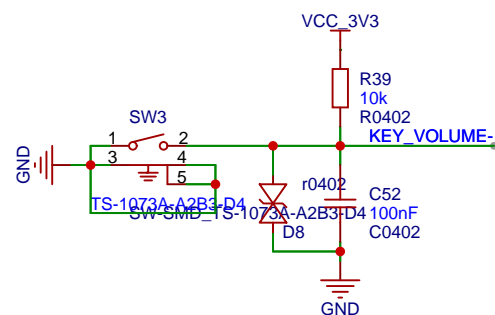
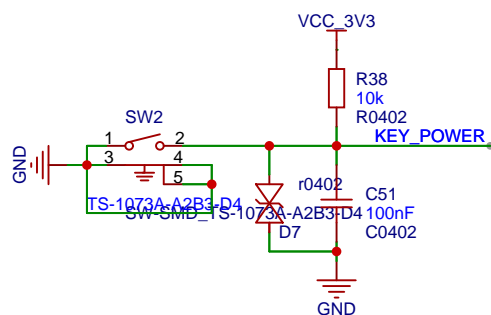
MIC FOR TSPI/EC20

Schematic	schematic1_1			Update Date	2024-08-17
				Create Date	2024-08-17
Page	AUDIO			Part Number	
Drawed		tspi智能手机设计			
Reviewed					
		VER	SIZE	PAGE	6 OF 8
		V1.0	A4	嘉立创EDA	



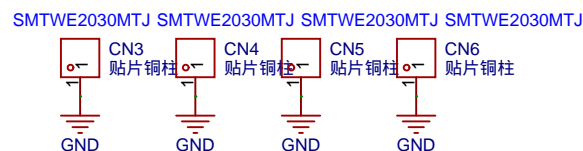


UART FOR DEBUG




KEY-POWER/VOLUME UP/VOLUME DOWN

RTC



FIX

Schematic	schematic1_1			Update Date	2024-08-17
				Create Date	2024-08-17
Page	OTHERS			Part Number	
Drawed		tspi智能手机设计			
Reviewed					
		VER	SIZE	PAGE	8 OF 8
		V1.0	A4	嘉立创EDA	