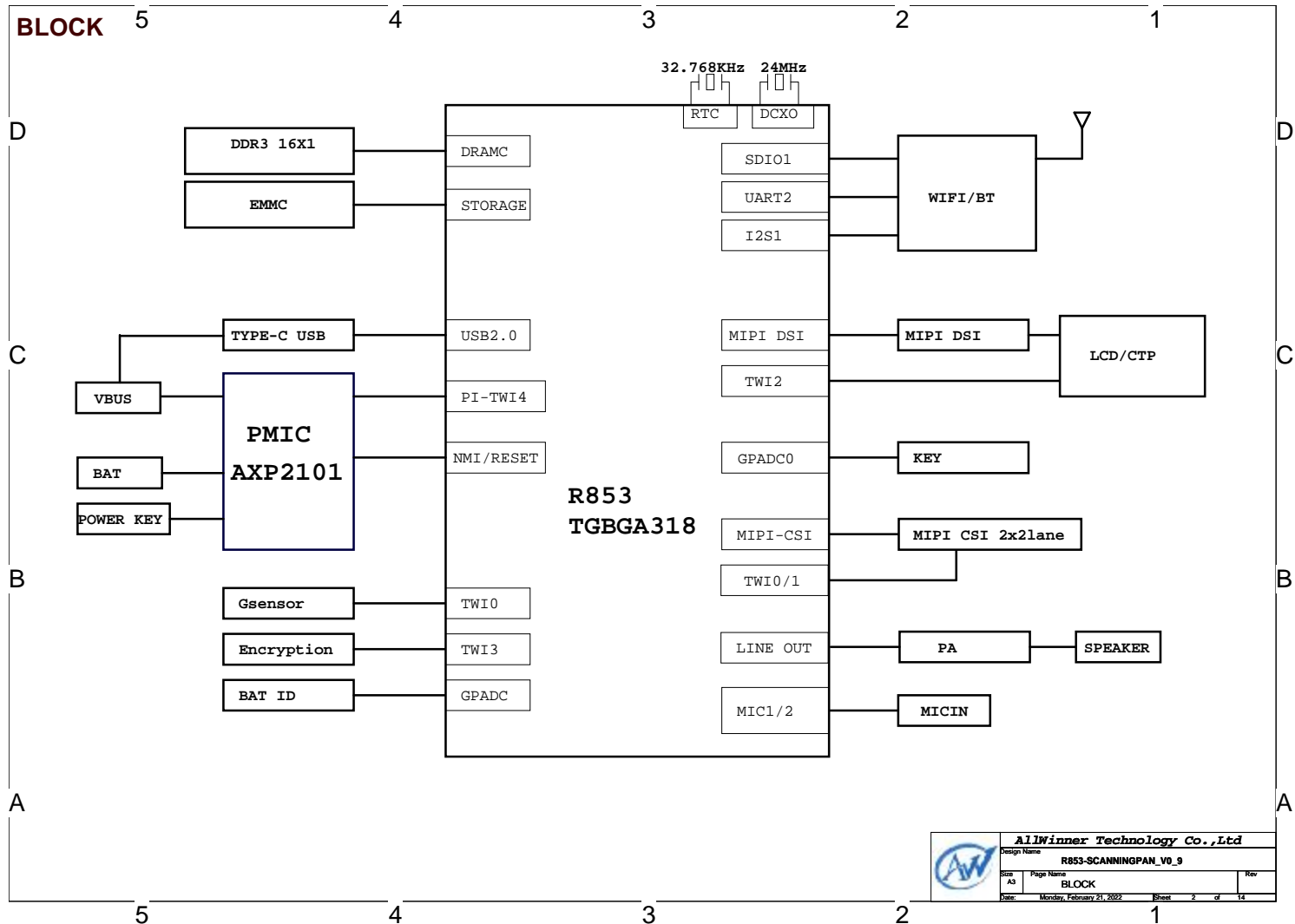


REVISION HISTORY

Revision	Description	Date	Drawn	Checked
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Schematics Index:

- P01: REVISION HISTORY
- P02: BLOCK
- P03: POWER TREE
- P04: POWER
- P05: CPU
- P06: PF/PG/PH/PI
- P07: DDR3_16X1
- P08: eMMC
- P09: KEY/DEBUG
- P10: AUDIO
- P11: LCD/CTP
- P12: MIPI CSI
- P13: G-SENSOR
- P14: WIFI/BT



POWER TREE

4

3

2

1

☐ DEFAULT POWER ON
☐ DEFAULT POWER OFF

AXP2101

D

D

C

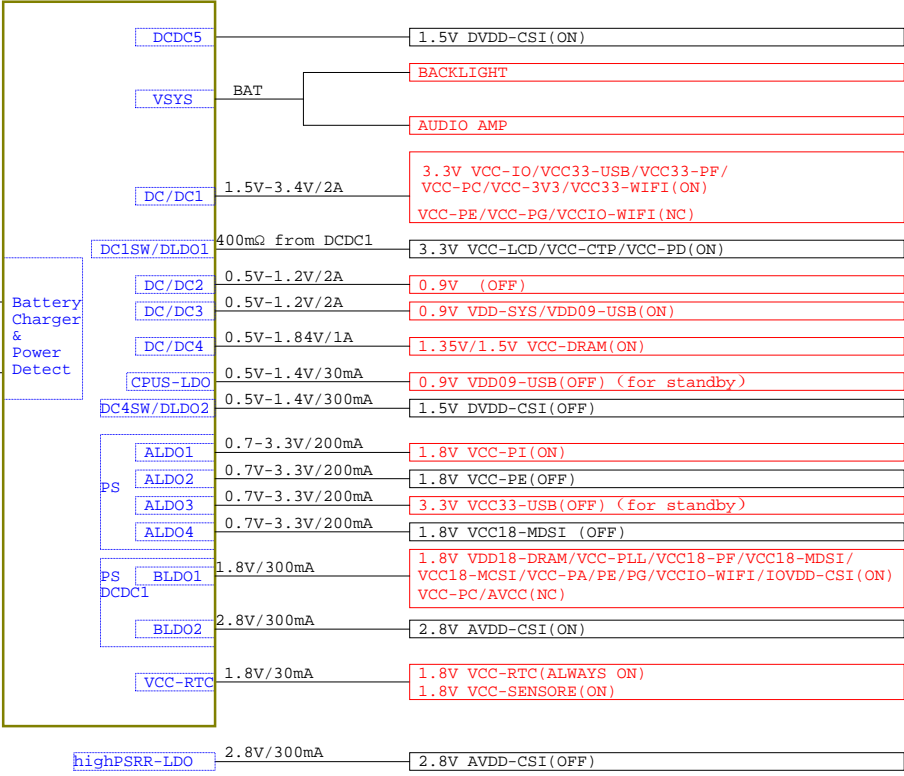
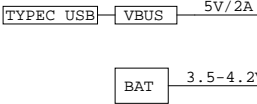
C

B

B

A

A



5

4

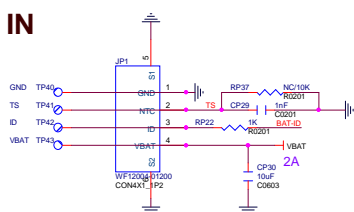
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2

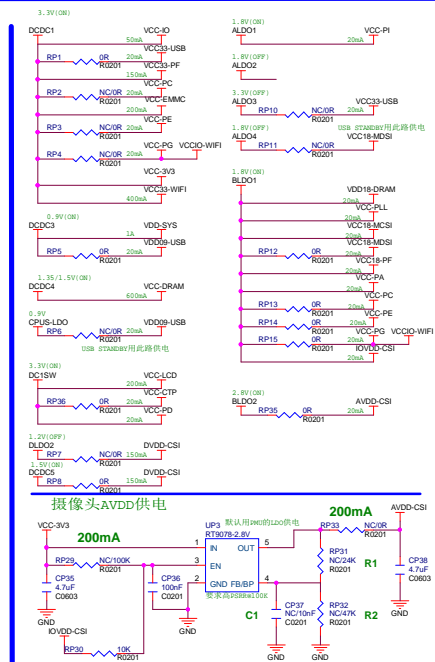
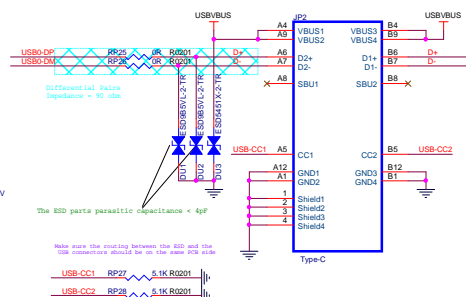
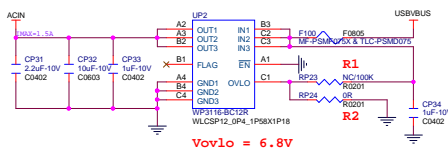
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Allwinner Technology Co.,Ltd			
R853-SCANNINGPAN_V0_9			
Design Name	Page Name	Rev	
Size	Ad	POWER TREE	
Date	Monday, February 21, 2022	Sheet	3 of 14

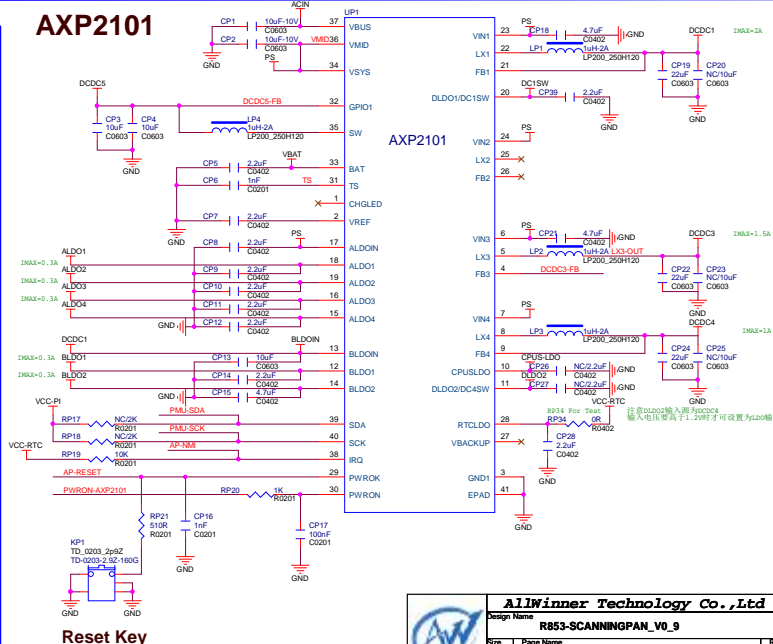
BAT IN



USB IN



AXP2101

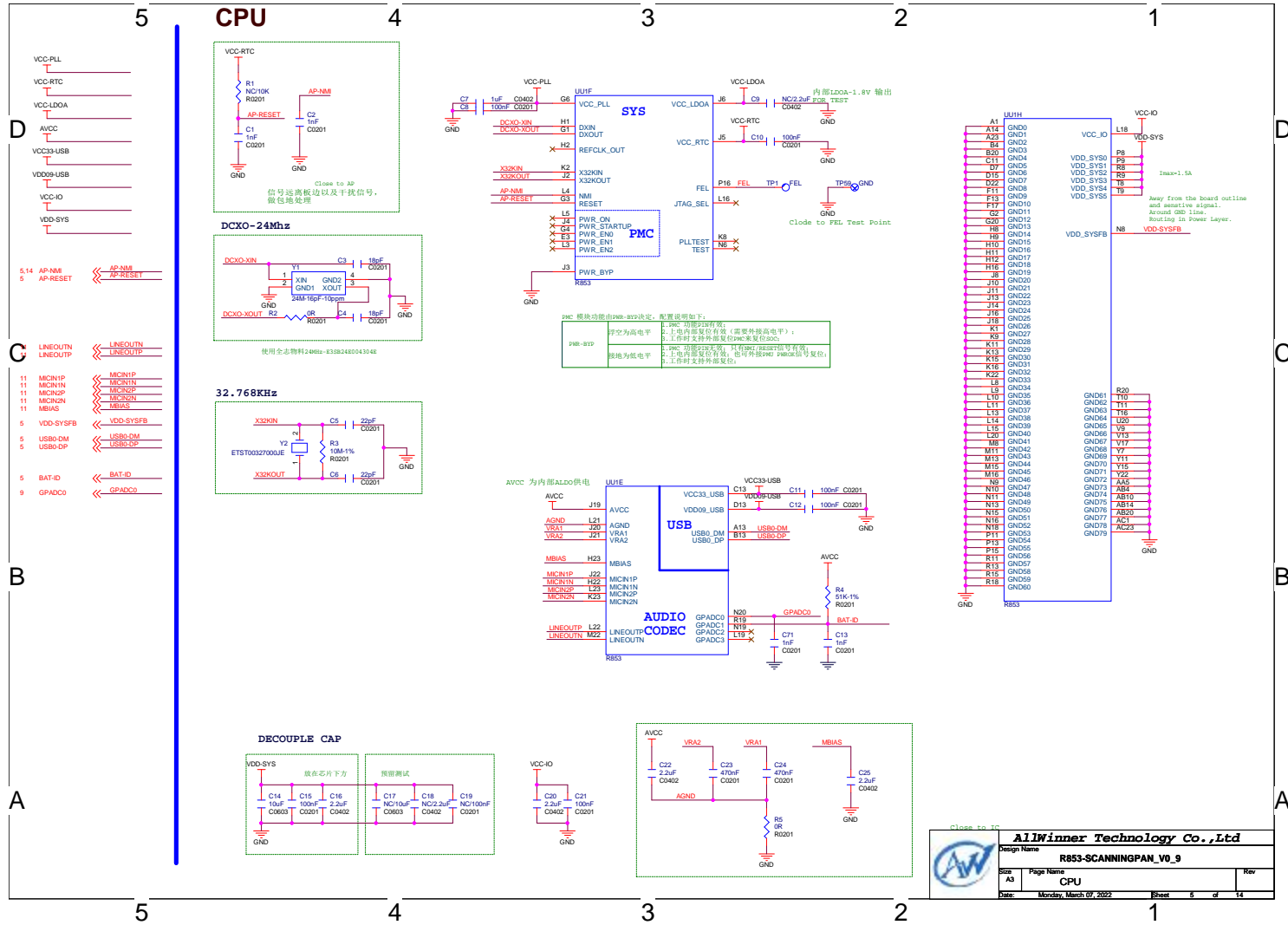


AllWinner Technology Co.,Ltd

Design Name **R853-SCANNINGPAN V0 9**

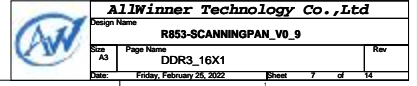
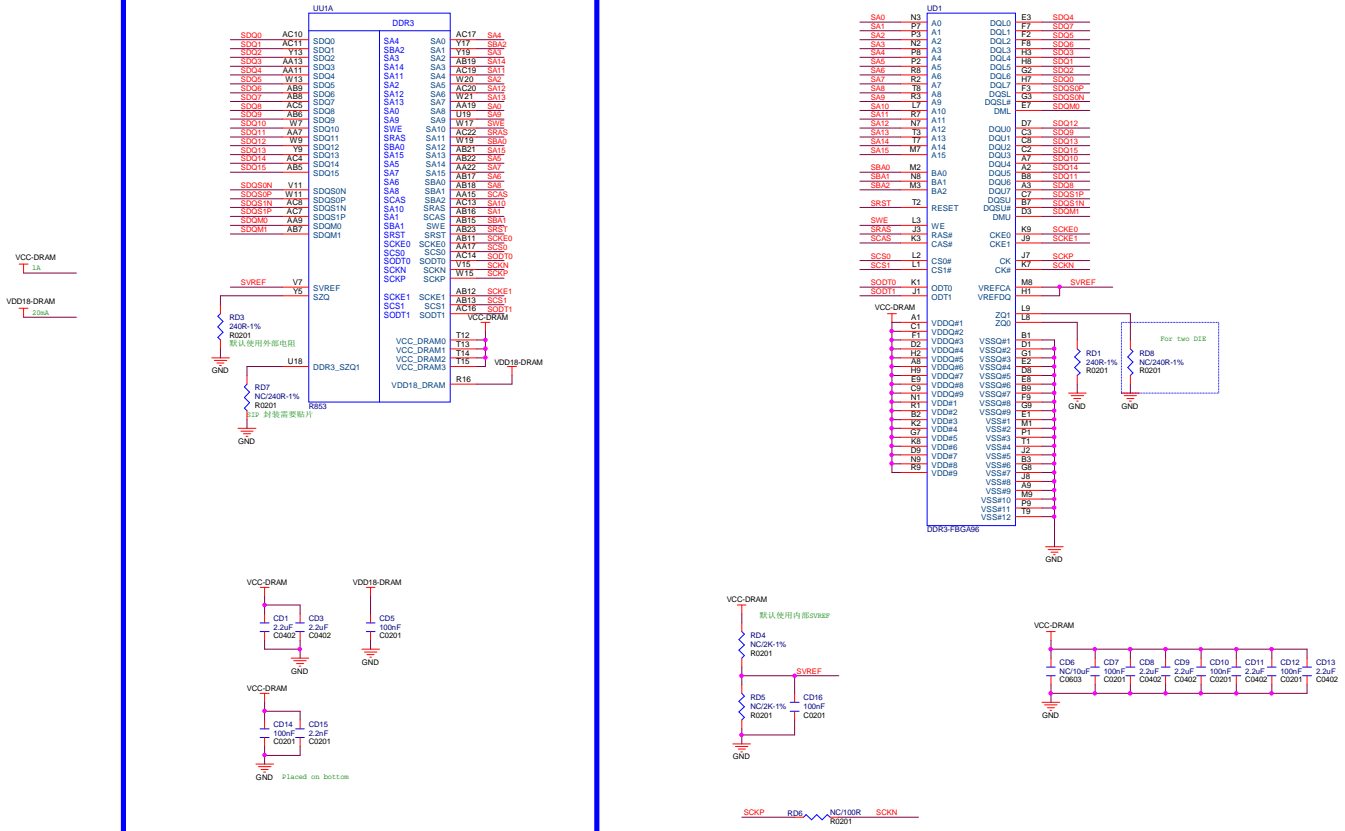
Size	Page Name
43	

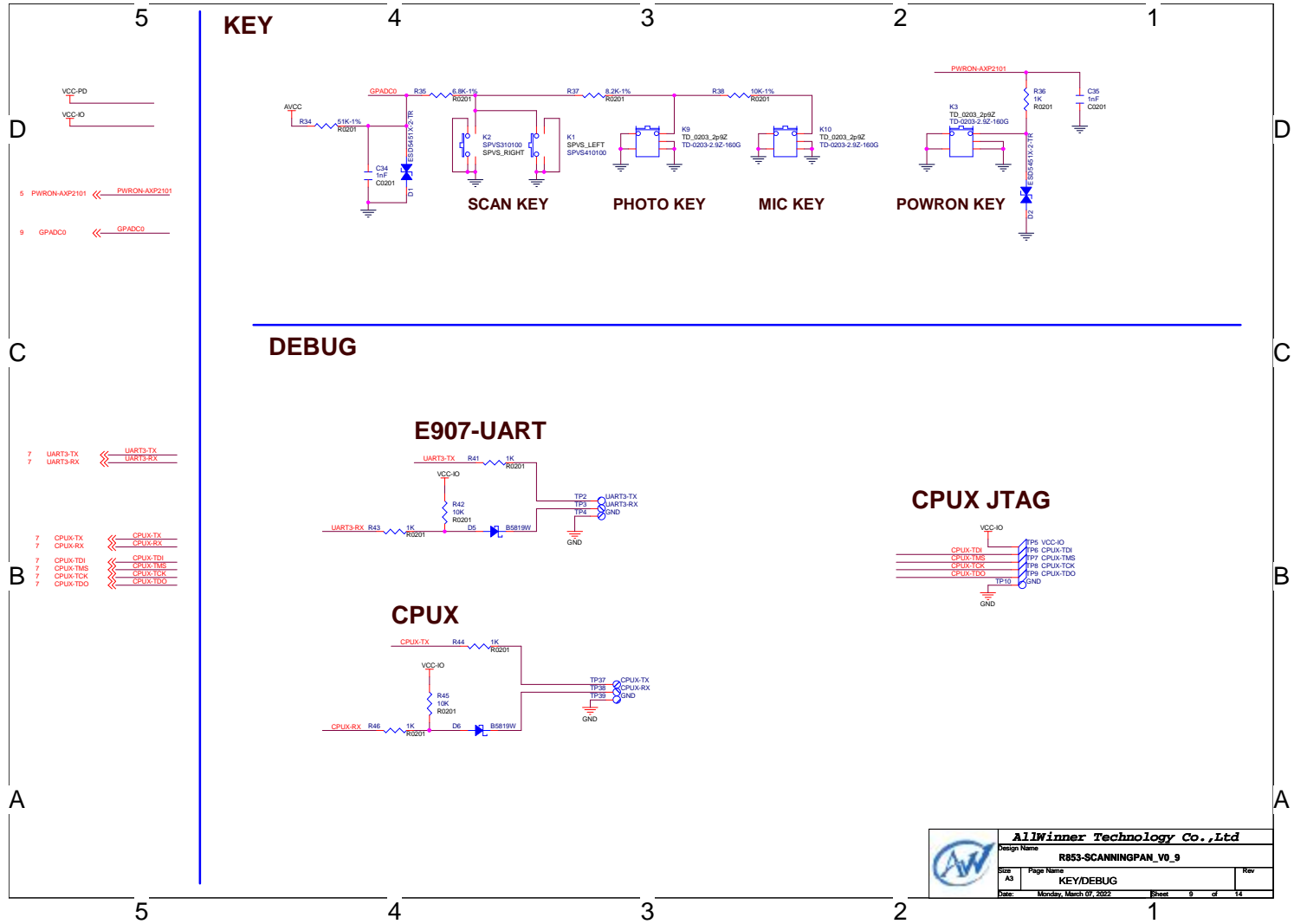
A3	POWER
Date: Monday, March 07, 2022	Sheet 4 of 14

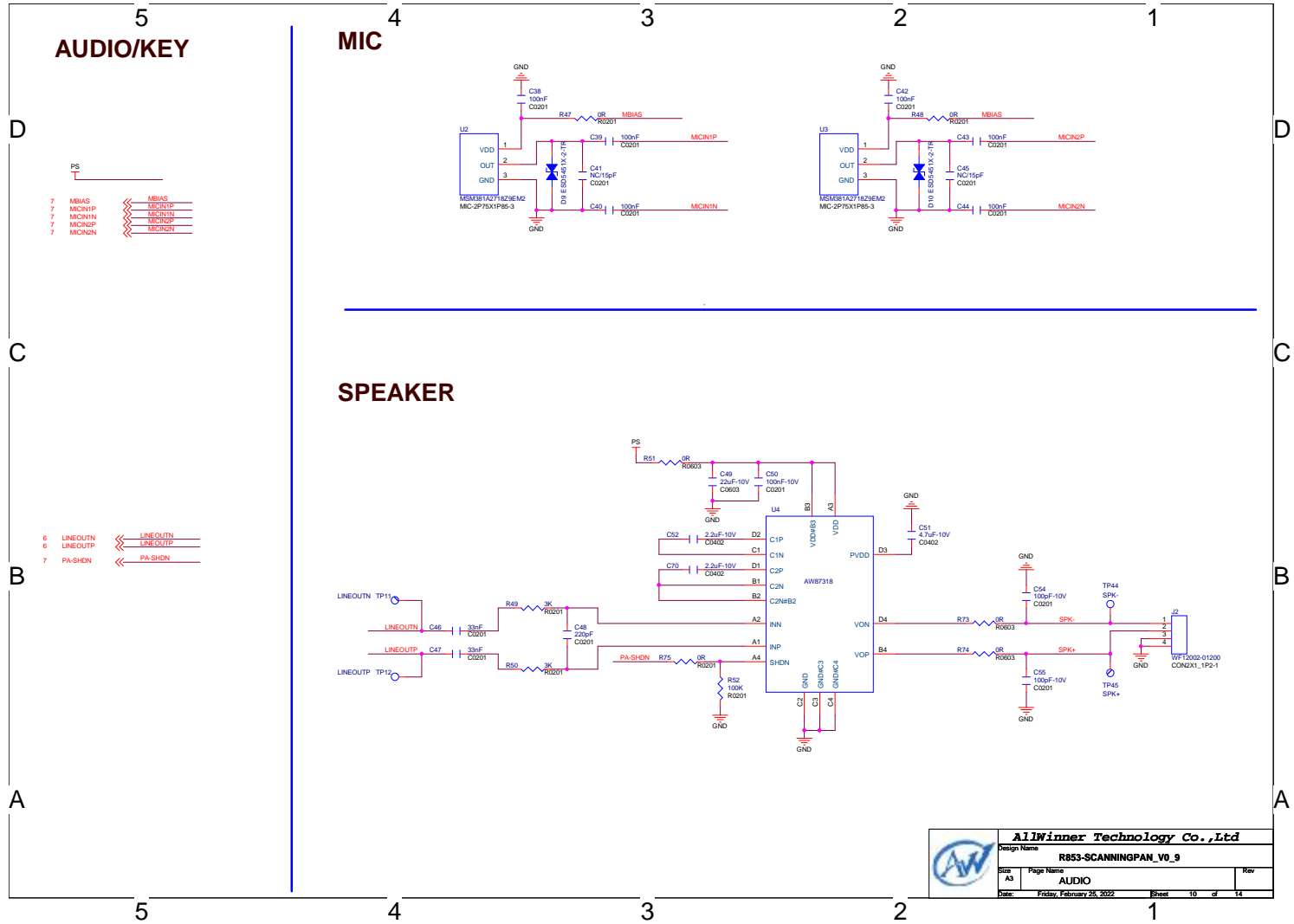


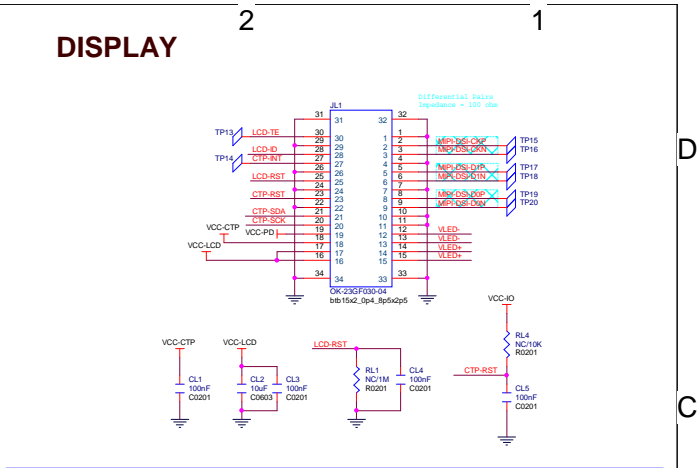
Allwinner Technology Co.,Ltd			
R853-SCANNINGPAN_V0_9			
Design Name	Page Name	Rev	
CPU			
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DDR3 16X1







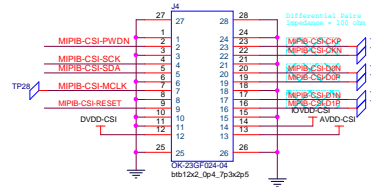
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	AllWinner Technology Co.,Ltd		
	Design Name		
	R853-SCANNINGPAN_V0_9		
	Size A3	Page Name LCD/CTP	Rev
Date: Monday, March 07, 2022 Sheet 11 of 14			

[illegible]

VCC-PA			
R55	2K	R0201	MPIA-CSI-SCK
R56	2K	R0201	MPIA-CSI-SDA
R57	2K	R0201	MPIB-CSI-SCK
R58	2K	R0201	MPIB-CSI-SDA
R59	100K	R0201	MPIA-CSI-RESET
R60	100K	R0201	MPIA-CSI-PWDN
R61	100K	R0201	MPIB-CSI-RESET
R62	100K	R0201	MPIB-CSI-PWDN

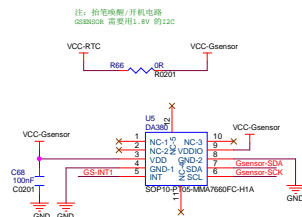
The schematic diagram illustrates the differential pair input impedance of the AD9288. It shows three input channels: DVDD-CSI, AVDD-CSI, and IOVDD-CSI. Each channel has a 100pF capacitor (C09, C10, C11) connected to ground. The input signals are connected to pins 27, 28, and 29. The internal circuitry includes a differential pair with a tail resistor of 100kΩ. The output signals are connected to pins 26, 27, and 28. The input impedance is 100kΩ. The output signals are connected to pins 26, 27, and 28. The input impedance is 100kΩ.



G-SENSOR (DA380)

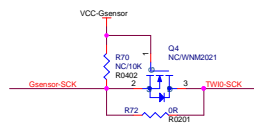
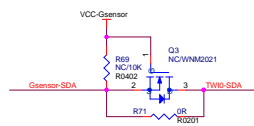
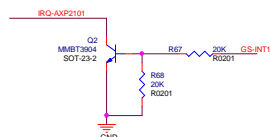
VCC-RTC

5.6 IRQ-AXP2101
7 TWI0-SDA
7 TWI0-SCK



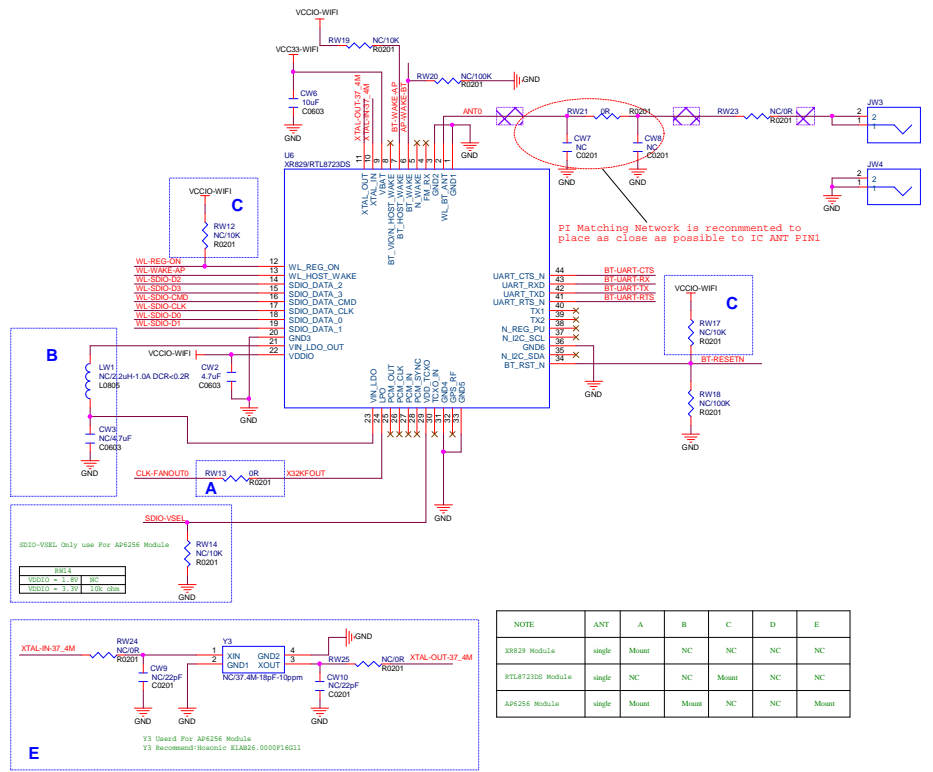
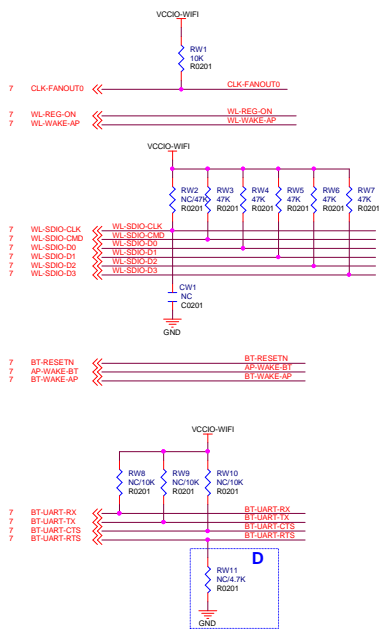
G-SENSOR 12与屏平行放置，放在屏的左上方，右上方放置G3501。

1. 关机前配置为中断输出，采用高电平脉冲中断方式；
2. 屏接收到的1.6ms以上低电平脉冲将产生开机；
3. 开机之后，配置屏中断输出，采用I2C轮询方式；



VCC-PE	R69	R70	Q3	Q4	R71	R72
1.8V	NC	NC	NC	NC	0R	0R
3.3V	10K	10K	WNM2021	WNM2021	NC	NC

WIFI+BT



NOTE	ANT	A	B	C	D	E
X9829 Module	single	Mount	NC	NC	NC	NC
BT1872305 Module	single	NC	NC	Mount	NC	NC
AP9255 Module	single	Mount	Mount	NC	NC	Mount

AllWinner Technology Co., Ltd			
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Size: A3	Page Name: WIFI/BT	Rev:	
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