VERSION HISTORY

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P12 MIPI-CSI
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P15 RMII0
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P17 WIFI+BT

Revision	Description	Date	Drawn	Checked	Approved
Ver 1.0	Initial Edition	2020/06/01	Alen		

ArtInChip Technology Co.,Ltd

Design Name: AIC800_REF_SIP_LP4_32X1_V1_0

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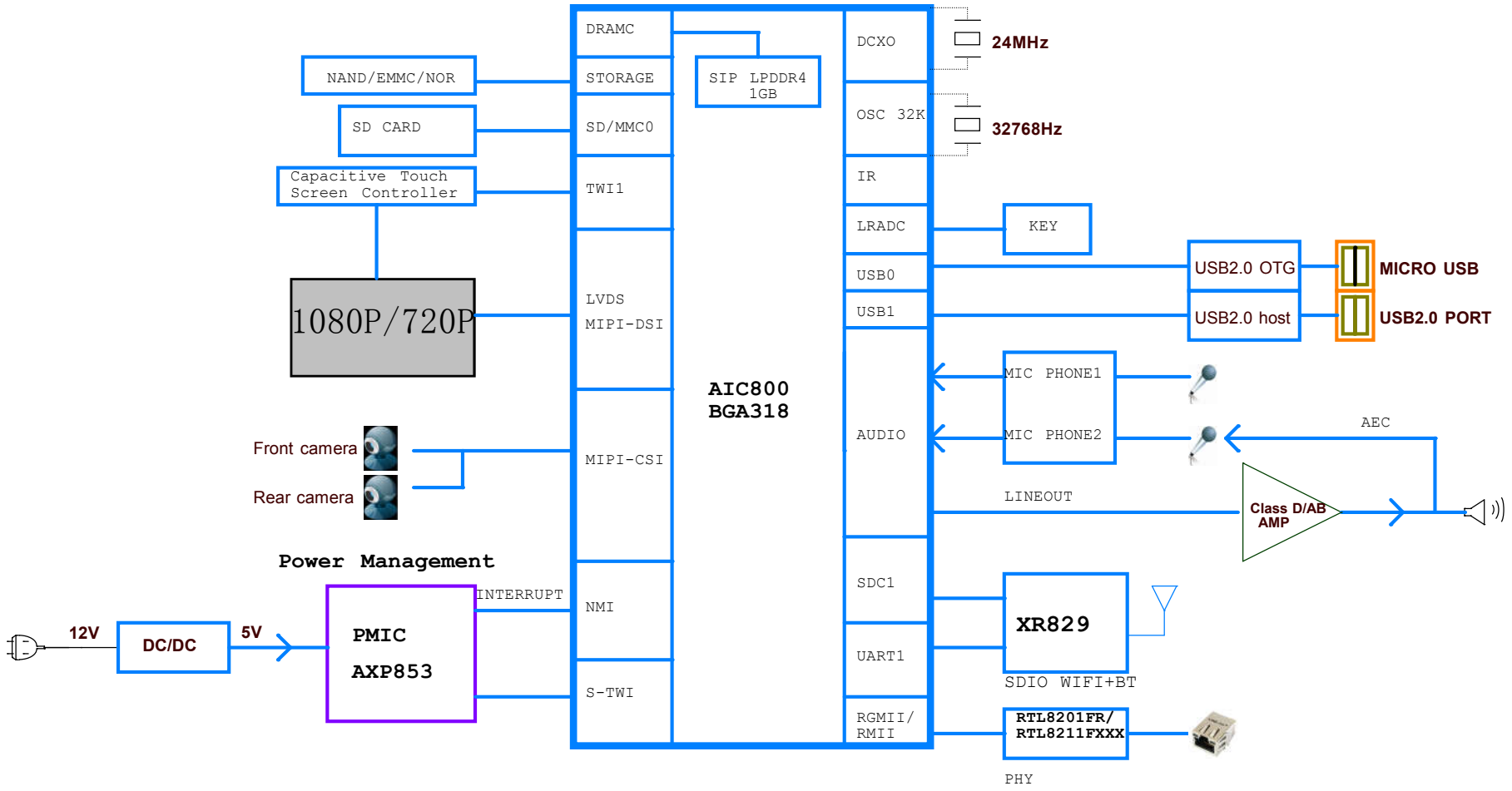
BLOCK DIAGRAM



ArtInChip Technology Co.,Ltd			
Design Name	AIC800_REF_SIP_LP4_32X1_V1_0		
Size: Custom	Page Name: BLOCK DIAGRAM		Rev: 2.0
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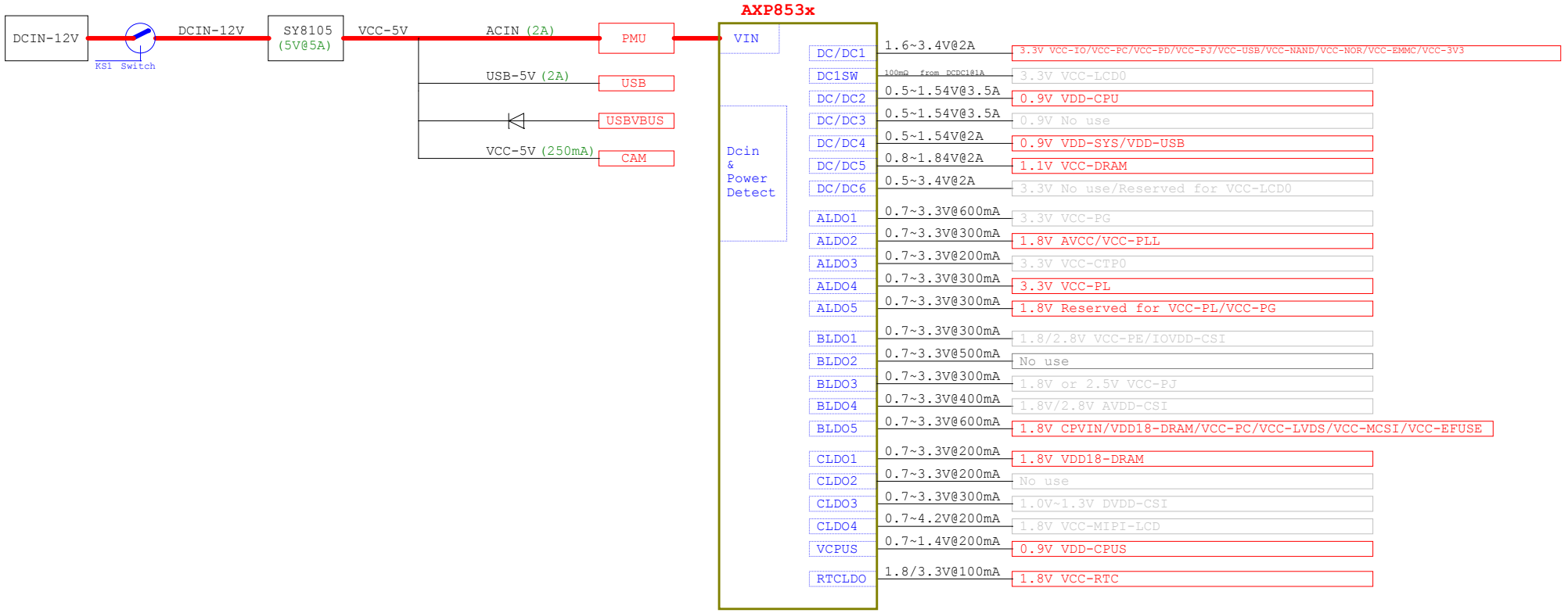
POWER TREE



ArtInChip Technology Co.,Ltd	
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Size A2	Page Name POWER TREE
Rev 2.0	
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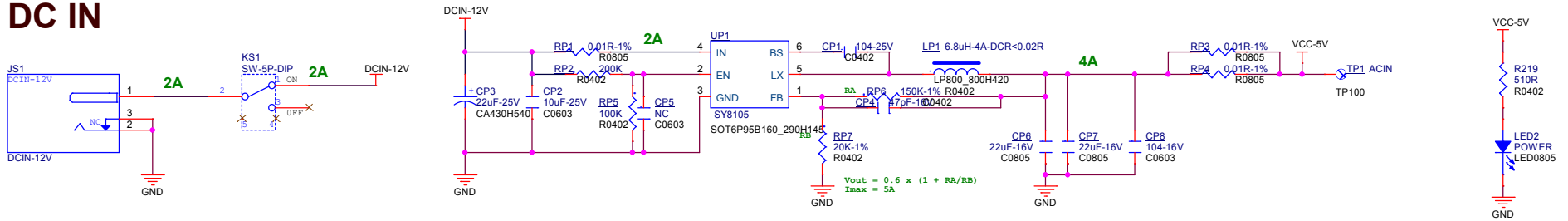
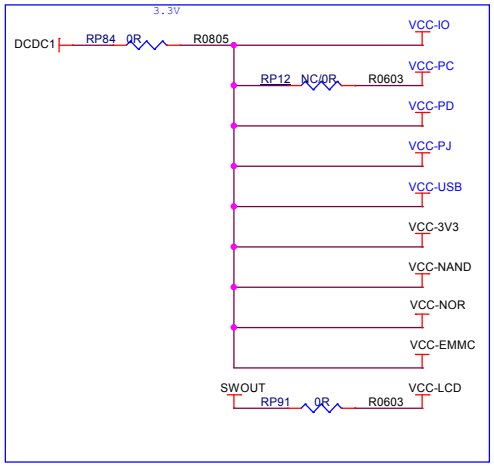
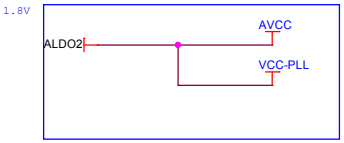
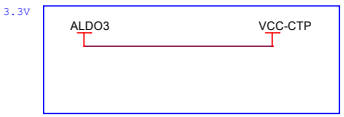
DC IN

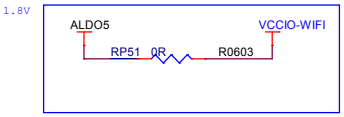


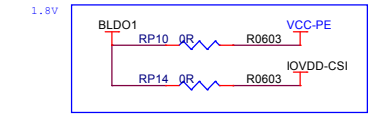
蓝色电 源 是SO C需求

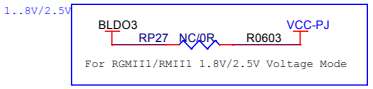


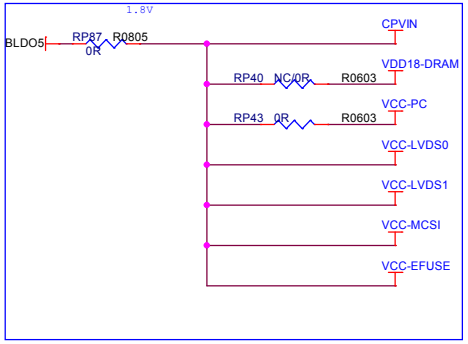


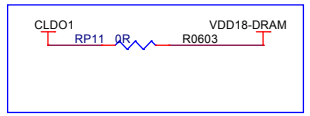


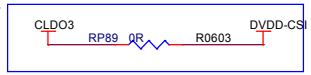


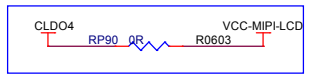


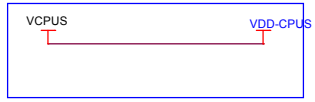




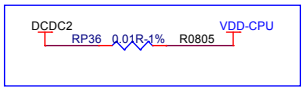


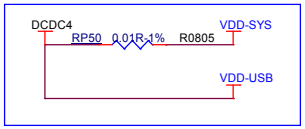


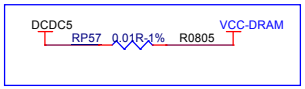


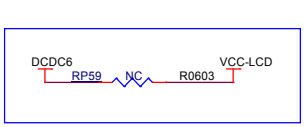


0.9V~1.0V **the 0.01R resistance used for measure currrent**









ArtInChip Technology Co.,Ltd

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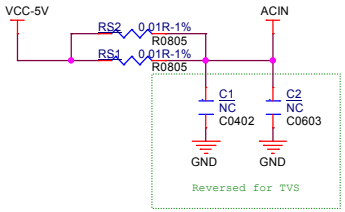
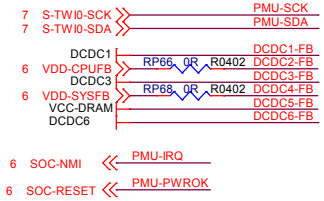
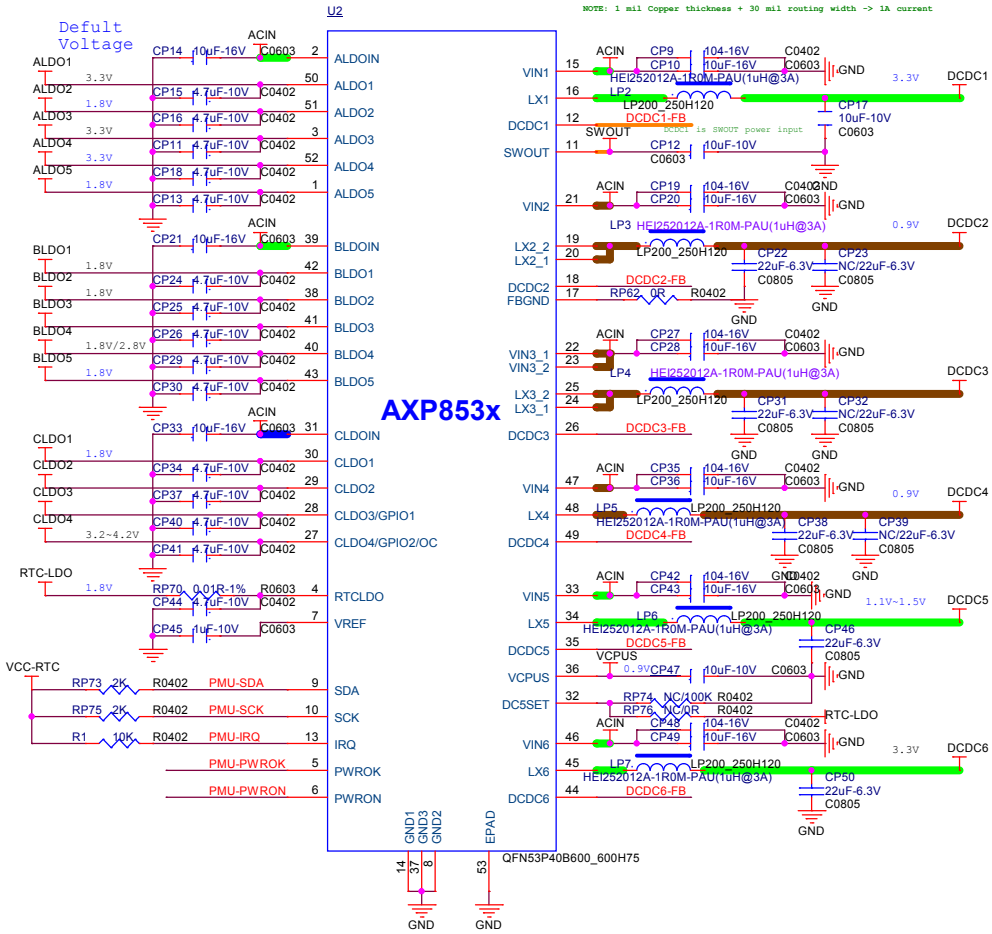
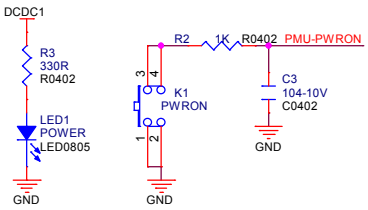
PMIC
AXP85x

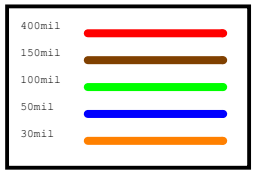


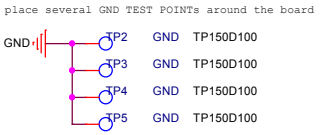






DC5SET select	DCDC5 default
Connect to GND	1.2V
Pull Down to GND with 20K~200K R	1.1V
Connect to RTCLDO	1.5V
Floating	Decide by OTP Default 1.1V





ArtInChip Technology Co.,Ltd		
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Size: A3	Page Name: PMIC	Rev: 2.0
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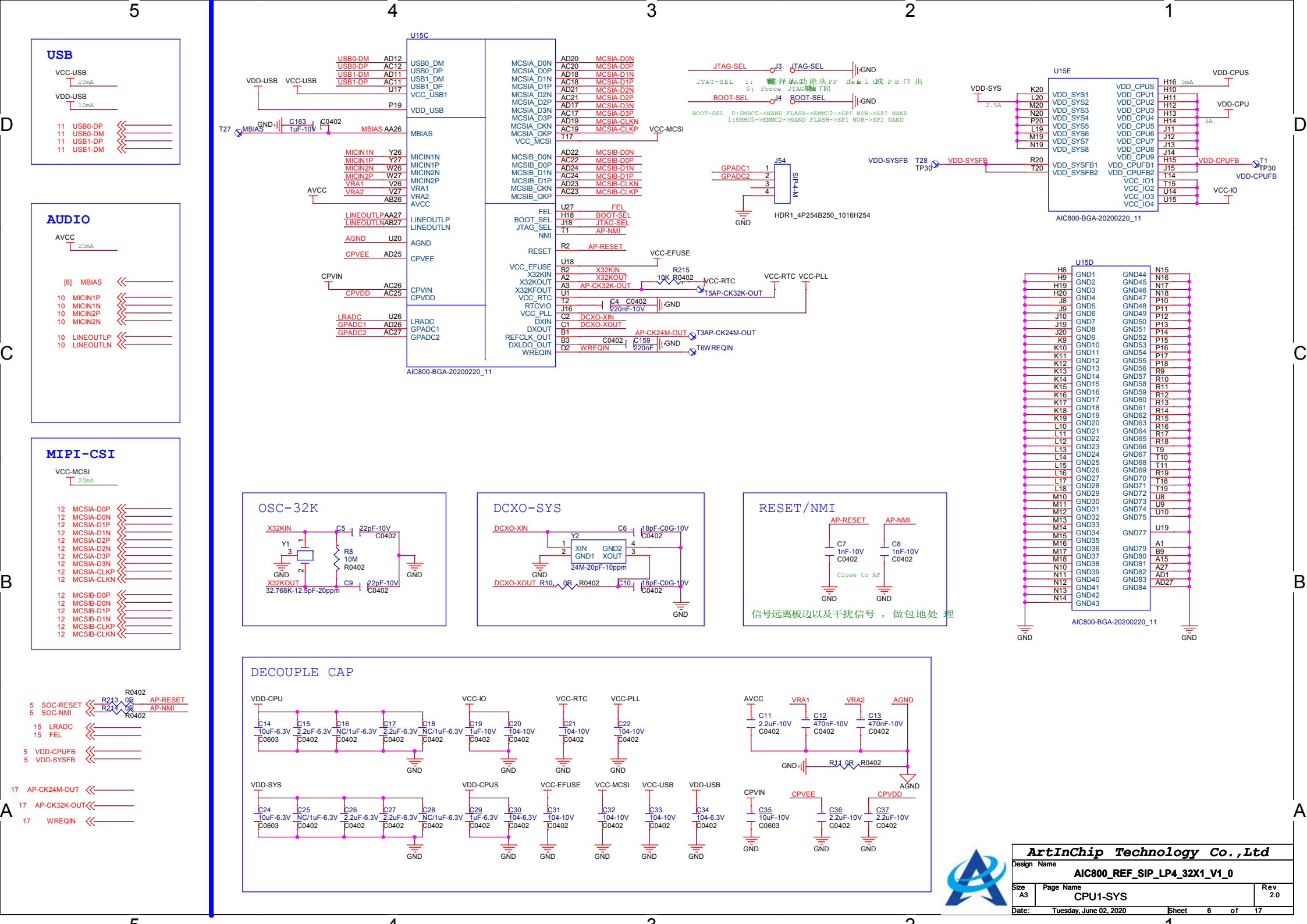
USB
VCC-USB
20mA
VDD-USB
10mA
11 USB0-DP
11 USB0-DM
11 USB1-DP
11 USB1-DM
AUDIO
AVCC
20mA
[6] MBIAS
10 MICIN1P
10 MICIN1N
10 MICIN2P
10 MICIN2N
10 LINEOUTLP
10 LINEOUTLN
MIPI-CSI
VCC-MCSI
20mA
12 MCSIA-D0P
12 MCSIA-D0N
12 MCSIA-D1P
12 MCSIA-D1N
12 MCSIA-D2P
12 MCSIA-D2N
12 MCSIA-D3P
12 MCSIA-D3N
12 MCSIA-CLKP
12 MCSIA-CLKN
12 MCSIB-D0P
12 MCSIB-D0N
12 MCSIB-D1P
12 MCSIB-D1N
12 MCSIB-CLKP
12 MCSIB-CLKN
R0402
5 SOC-RESET
5 SOC-NMI
R213 0R
R214 0R
AP-RESET
AP-NMI
R0402
15 LRADC
15 FEL
5 VDD-CPUFB
5 VDD-SYSFB
17 AP-CK24M-OUT
17 AP-CK32K-OUT
17 WREQIN
U15C
USB0-DM AD12
USB0-DP AC12
USB1-DM AD11
USB1-DP AC11
U17
P19
VDD_USB
VDD-USB VCC-USB
MBIAS
T27 MBIAS
GND C163 1uF-10V C0402
MBIAS AA26
MICIN1N Y26
MICIN1P Y27
MICIN2N W26
MICIN2P W27
VRA1 V26
VRA2 V27
AB26
AVCC
LINEOUTLP AA27
LINEOUTLN AB27
AGND U20
CPVEE AD25
CPVIN
AC26
CPVDD AC25
LRADC U26
GPADC1 AD26
GPADC2 AC27
MCSIA_D0N AD20
MCSIA_D0P AC20
MCSIA_D1N AD18
MCSIA_D1P AC18
MCSIA_D2N AD21
MCSIA_D2P AC21
MCSIA_D3N AD17
MCSIA_D3P AC17
MCSIA_CLKN AD19
MCSIA_CLKP AC19
VCC_MCSI T17
MCSIB_D0N AD22
MCSIB_D0P AC22
MCSIB_D1N AD24
MCSIB_D1P AC24
MCSIB_CLKN AD23
MCSIB_CLKP AC23
U27 FEL
H18 BOOT_SEL
J18 JTAG_SEL
T1 NMI
R2 RESET
AP-NMI
AP-RESET
VCC-EFUSE
U18
VCC_EFUSE B2
X32KIN A2
X32KOUT A3
AP-CK32K-OUT U1
VCC_RTC T2
RTCVIO J16
VCC_PLL C2
DXIN C1
DXOUT B1
REFCLK_OUT B3
DXLDO_OUT D2
WREQIN
X32KIN
X32KOUT
C4 C0402 220nF-10V
GND
R215 10K R0402
VCC-RTC
T5AP-CK32K-OUT
VCC-RTC VCC-PLL
DCXO-XIN
DCXO-XOUT
AP-CK24M-OUT
T3AP-CK24M-OUT
C0402
C159 220nF
GND
T6WREQIN
AIC800-BGA-20200220_11
JTAG-SEL J3 JTAG-SEL GND
JTAT-SEL 1: 强选择从功能从PF Debug或PBO出
0: Force JTAG功能口出
BOOT-SEL J4 BOOT-SEL GND
BOOT-SEL 0:SMHC0->NAND FLASH->EMMC2->SPI NOR->SPI NAND
1:SMHC0->EMMC2->NAND FLASH->SPI NOR->SPI NAND
GPADC1 1
GPADC2 2
3
4
J54
SIP-4-M
GND
HDR1_4P254B250_1016H254
U15E
VDD-SYS
2.5A
K20 VDD_SYS1
L20 VDD_SYS2
M20 VDD_SYS3
N20 VDD_SYS4
P20 VDD_SYS5
L19 VDD_SYS6
M19 VDD_SYS7
N19 VDD_SYS8
VDD-SYSFB T28
TP30
VDD-SYSFB
R20 VDD_SYSFB1
T20 VDD_SYSFB2
VDD_CPUS H16 5mA
VDD_CPU1 H10
VDD_CPU2 H11
VDD_CPU3 H12
VDD_CPU4 H13
VDD_CPU5 H14
VDD_CPU6 J11
VDD_CPU7 J12
VDD_CPU8 J13
VDD_CPU9 J14
VDD_CPUFB1 H15
VDD_CPUFB2 J15
VCC_IO1 T14
VCC_IO2 T15
VCC_IO3 U14
VCC_IO4 U15
VDD-CPUS
VDD-CPU
3A
VDD-CPUFB
T1
TP30
VDD-CPUFB
VCC-IO
AIC800-BGA-20200220_11
U15D
H8 GND1
H9 GND2
H19 GND3
H20 GND4
J8 GND5
J9 GND6
J10 GND7
J19 GND8
J20 GND9
K9 GND10
K10 GND11
K11 GND12
K12 GND13
K13 GND14
K14 GND15
K15 GND16
K16 GND17
K17 GND18
K18 GND19
K19 GND20
L10 GND21
L11 GND22
L12 GND23
L13 GND24
L14 GND25
L15 GND26
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L17 GND28
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M18 GND38
N10 GND39
N11 GND40
N12 GND41
N13 GND42
N14 GND43
GND44 N15
GND45 N16
GND46 N17
GND47 N18
GND48 P10
GND49 P11
GND50 P12
GND51 P13
GND52 P14
GND53 P15
GND54 P16
GND55 P17
GND56 P18
GND57 R9
GND58 R10
GND59 R11
GND60 R12
GND61 R13
GND62 R14
GND63 R15
GND64 R16
GND65 R17
GND66 R18
GND67 T9
GND68 T10
GND69 T11
GND70 R19
GND71 T18
GND72 T19
GND73 U8
GND74 U9
GND75 U10
GND77 U19
GND79 A1
GND80 B9
GND81 A15
GND82 A27
GND83 AD1
GND84 AD27
AIC800-BGA-20200220_11
GND
GND
OSC-32K
X32KIN C5 22pF-10V C0402
Y1 3 1 2
R8 10M R0402
GND
X32KOUT 32.768K-12.5pF-20ppm
C9 22pF-10V C0402
GND
DCXO-SYS
DCXO-XIN C6 18pF-C0G-10V C0402
Y2
1 XIN GND1 4 GND2 3 XOUT
24M-20pF-10ppm
GND
DCXO-XOUT R10 0R R0402
C10 18pF-C0G-10V C0402
GND
RESET/NMI
AP-RESET AP-NMI
C7 1nF-10V C0402
C8 1nF-10V C0402
Close to AP
GND GND
信号远离板边以及干扰信号 ， 做包地处 理
DECOUPLE CAP
VDD-CPU
C14 10uF-6.3V C0603
C15 2.2uF-6.3V C0402
C16 NC/1uF-6.3V C0402
C17 2.2uF-6.3V C0402
C18 NC/1uF-6.3V C0402
GND
VCC-IO
C19 1uF-10V C0402
C20 104-10V C0402
GND
VCC-RTC
C21 104-10V C0402
GND
VCC-PLL
C22 104-10V C0402
GND
AVCC
VRA1
VRA2
AGND
C11 2.2uF-10V C0402
C12 470nF-10V C0402
C13 470nF-10V C0402
GND
R11 0R R0402
AGND
VDD-SYS
C24 10uF-6.3V C0603
C25 NC/1uF-6.3V C0402
C26 2.2uF-6.3V C0402
C27 2.2uF-6.3V C0402
C28 NC/1uF-6.3V C0402
GND
VDD-CPUS
C29 1uF-6.3V C0402
C30 104-6.3V C0402
GND
VCC-EFUSE
C31 104-10V C0402
GND
VCC-MCSI
C32 104-10V C0402
GND
VCC-USB
C33 104-10V C0402
GND
VDD-USB
C34 104-6.3V C0402
GND
CPVIN
C35 10uF-10V C0603
GND
CPVEE
C36 2.2uF-10V C0402
GND
CPVDD
C37 2.2uF-10V C0402
GND
ArtInChip Technology Co.,Ltd
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AIC800_REF_SIP_LP4_32X1_V1_0
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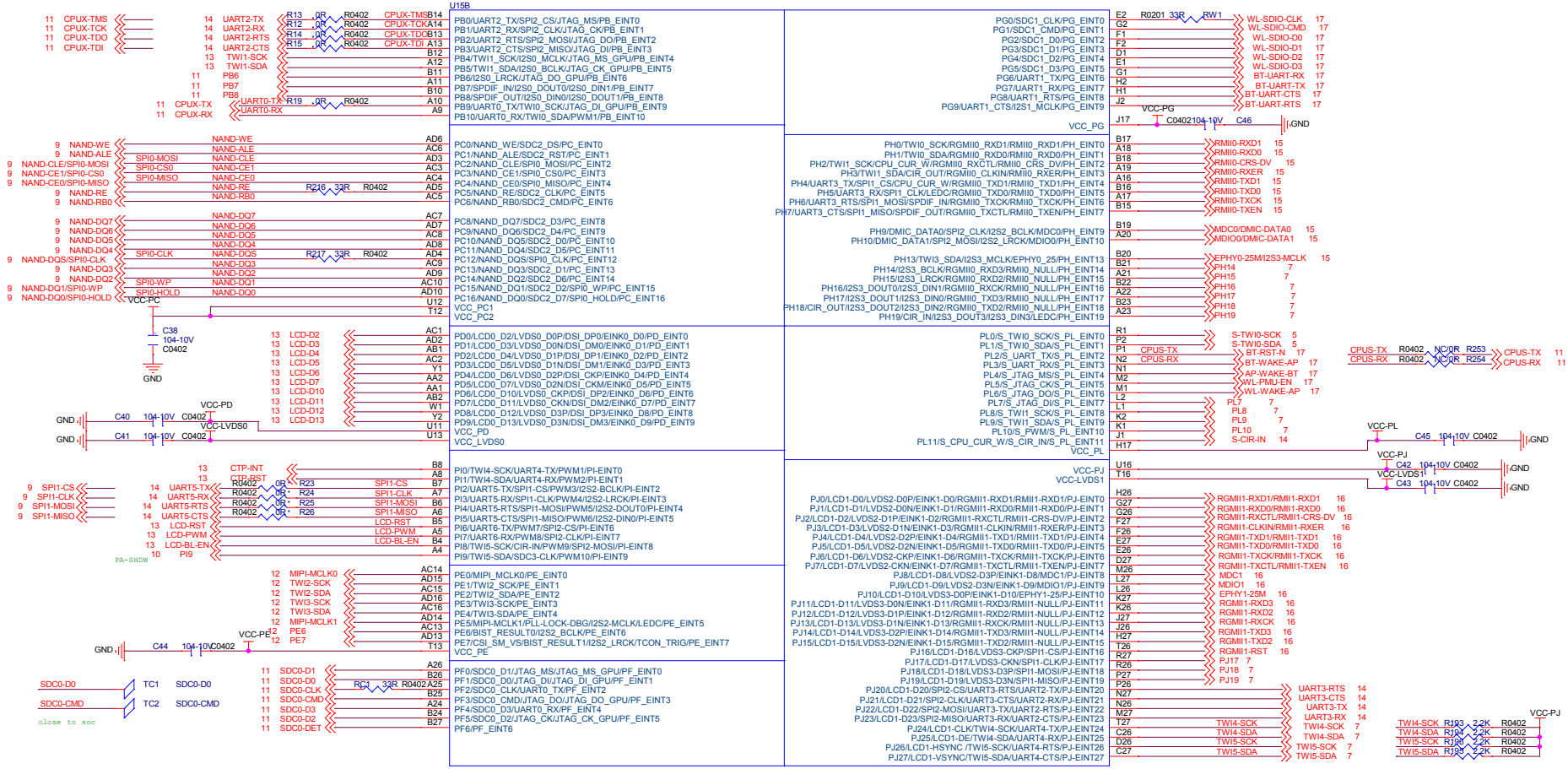

GPIO 使用说明
1、注意 SDC0端口 与较的 IO电平匹配
2、IO电压选择对应GPIO所属电源域电压

ArtInChip Technology Co.,Ltd		
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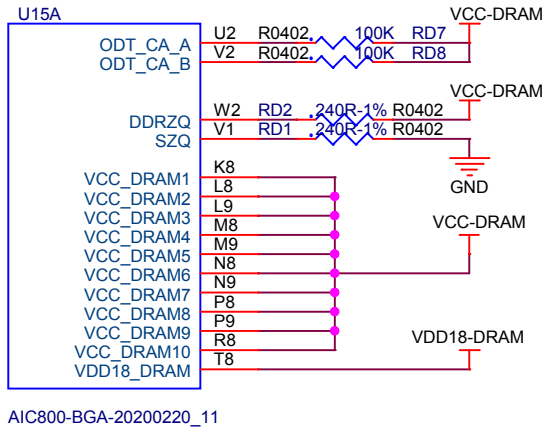
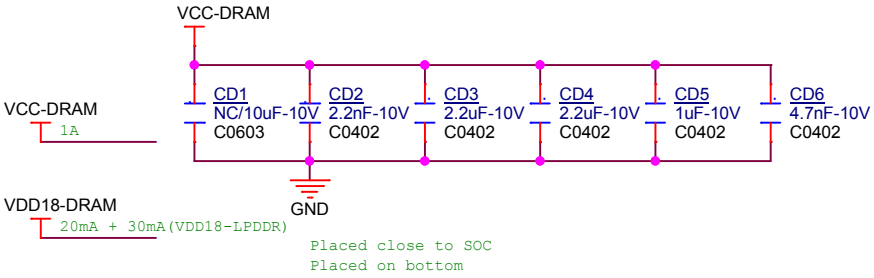
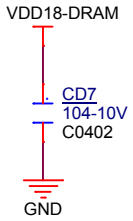
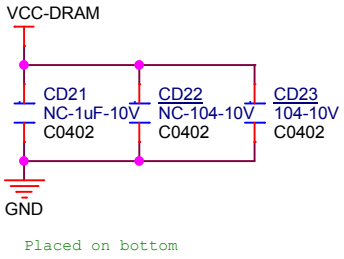
LPDDR4









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Size: A4	Page Name: LPDDR4 32X1	Rev: 2.0
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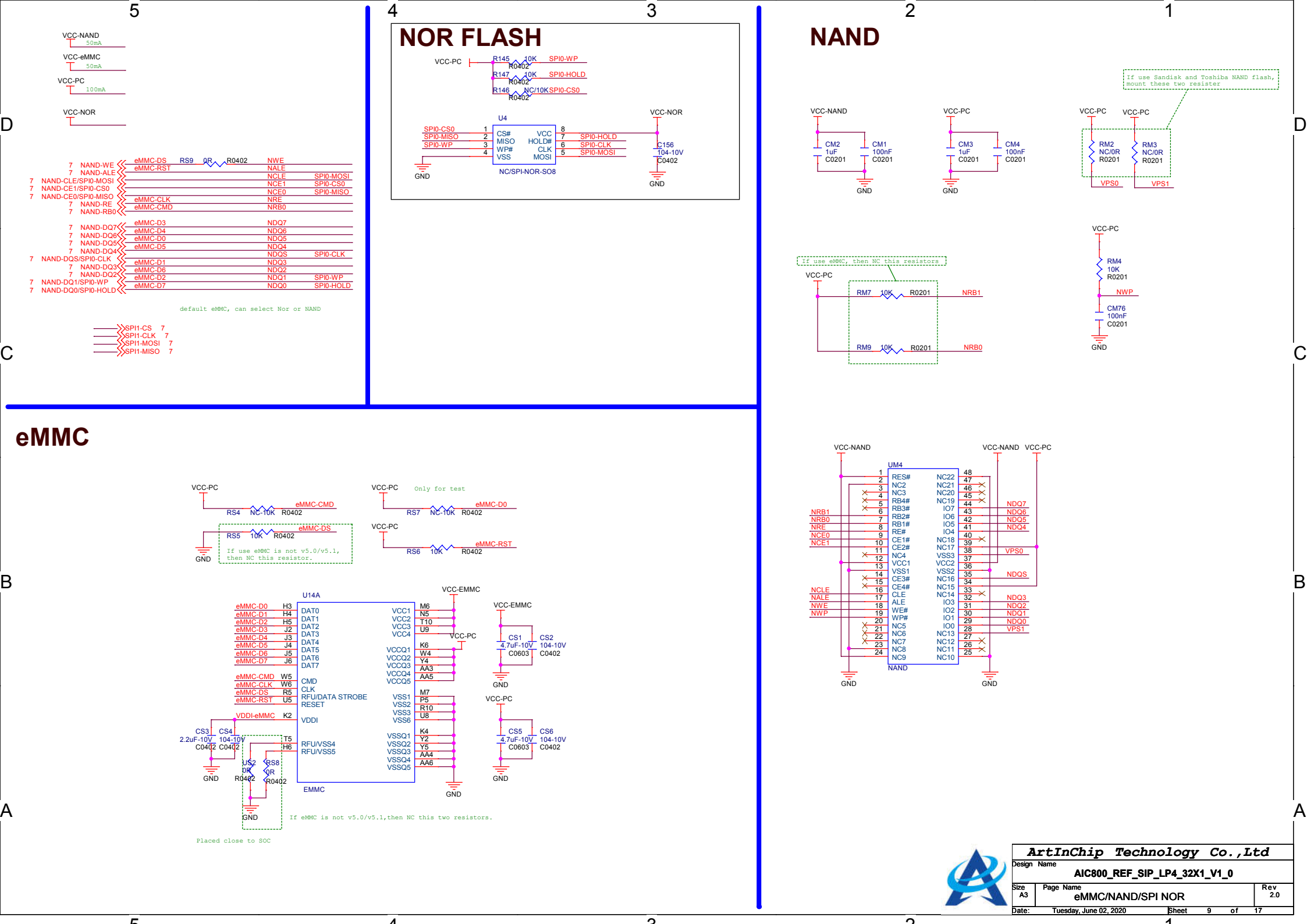
VCC-NAND
50mA
VCC-eMMC
50mA
VCC-PC
100mA
VCC-NOR
7 NAND-WE
7 NAND-ALE
7 NAND-CLE/SPI0-MOSI
7 NAND-CE1/SPI0-CS0
7 NAND-CE0/SPI0-MISO
7 NAND-RE
7 NAND-RB0
7 NAND-DQ7
7 NAND-DQ6
7 NAND-DQ5
7 NAND-DQ4
7 NAND-DQS/SPI0-CLK
7 NAND-DQ3
7 NAND-DQ2
7 NAND-DQ1/SPI0-WP
7 NAND-DQ0/SPI0-HOLD
eMMC-DS
eMMC-RST
eMMC-D3
eMMC-D4
eMMC-D0
eMMC-D5
eMMC-D1
eMMC-D6
eMMC-D2
eMMC-D7
RS9 0R R0402
NWE
NALE
NCLE
NCE1
NCE0
NRE
NRB0
NDQ7
NDQ6
NDQ5
NDQ4
NDQS
NDQ3
NDQ2
NDQ1
NDQ0
SPI0-MOSI
SPI0-CS0
SPI0-MISO
SPI0-CLK
SPI0-WP
SPI0-HOLD
default eMMC, can select Nor or NAND
SPI1-CS 7
SPI1-CLK 7
SPI1-MOSI 7
SPI1-MISO 7
NOR FLASH
VCC-PC
R145 10K SPI0-WP
R147 10K SPI0-HOLD
R146 NC/10K SPI0-CS0
R0402
U4
SPI0-CS0 1
SPI0-MISO 2
SPI0-WP 3
4
CS# VCC 8
MISO HOLD# 7 SPI0-HOLD
WP# CLK 6 SPI0-CLK
VSS MOSI 5 SPI0-MOSI
NC/SPI-NOR-SO8
VCC-NOR
C156
104-10V
C0402
GND
NAND
VCC-NAND
CM2 1uF C0201
CM1 100nF C0201
VCC-PC
CM3 1uF C0201
CM4 100nF C0201
If use Sandisk and Toshiba NAND flash, mount these two resister
VCC-PC
VCC-PC
RM2 NC/0R R0201
RM3 NC/0R R0201
VPS0
VPS1
If use eMMC, then NC this resistors
VCC-PC
RM7 10K R0201 NRB1
RM9 10K R0201 NRB0
VCC-PC
RM4 10K R0201
NWP
CM76 100nF C0201
GND
eMMC
VCC-PC
eMMC-CMD
RS4 NC-10K R0402
VCC-PC
Only for test
eMMC-D0
RS7 NC-10K R0402
eMMC-DS
RS5 10K R0402
If use eMMC is not v5.0/v5.1, then NC this resistor.
VCC-PC
eMMC-RST
RS6 10K R0402
GND
U14A
eMMC-D0 H3
eMMC-D1 H4
eMMC-D2 H5
eMMC-D3 J2
eMMC-D4 J3
eMMC-D5 J4
eMMC-D6 J5
eMMC-D7 J6
DAT0
DAT1
DAT2
DAT3
DAT4
DAT5
DAT6
DAT7
eMMC-CMD W5
eMMC-CLK W6
eMMC-DS R5
eMMC-RST U5
CMD
CLK
RFU/DATA STROBE
RESET
VDDI-eMMC K2
VDDI
RFU/VSS4 T5
RFU/VSS5 H6
VCC1 M6
VCC2 N5
VCC3 T10
VCC4 U9
VCCQ1 K6
VCCQ2 W4
VCCQ3 Y4
VCCQ4 AA3
VCCQ5 AA5
VSS1 M7
VSS2 P5
VSS3 R10
VSS6 U8
VSSQ1 K4
VSSQ2 Y2
VSSQ3 Y5
VSSQ4 AA4
VSSQ5 AA6
EMMC
VCC-EMMC
CS1 4.7uF-10V C0603
CS2 104-10V C0402
VCC-PC
VCC-EMMC
CS3 2.2uF-10V C0402
CS4 104-10V C0402
VCC-PC
CS5 4.7uF-10V C0603
CS6 104-10V C0402
US2 0R R0402
RS8 0R R0402
If eMMC is not v5.0/v5.1,then NC this two resistors.
Placed close to SOC
VCC-NAND
VCC-NAND VCC-PC
UM4
RES# NC2 NC3 RB4# RB3# RB2# RB1# RE# CE1# CE2# NC4 VCC1 VSS1 CE3# CE4# CLE ALE WE# WP# NC5 NC6 NC7 NC8 NC9
NC22 NC21 NC20 NC19 IO7 IO6 IO5 IO4 NC18 NC17 VSS3 VCC2 VSS2 NC16 NC15 NC14 IO3 IO2 IO1 IO0 NC13 NC12 NC11 NC10
NRB1 NRB0 NRE NCE0 NCE1 NCLE NALE NWE NWP
NDQ7 NDQ6 NDQ5 NDQ4 VPS0 NDQS NDQ3 NDQ2 NDQ1 NDQ0 VPS1
NAND
GND
ArtInChip Technology Co.,Ltd
Design Name
AIC800_REF_SIP_LP4_32X1_V1_0
Size A3
Page Name
eMMC/NAND/SPI NOR
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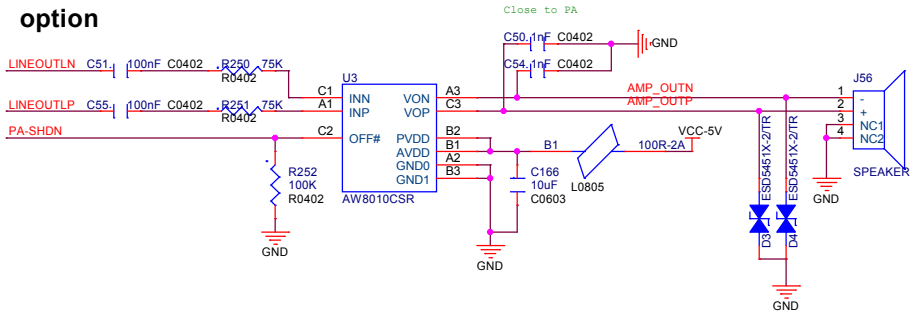
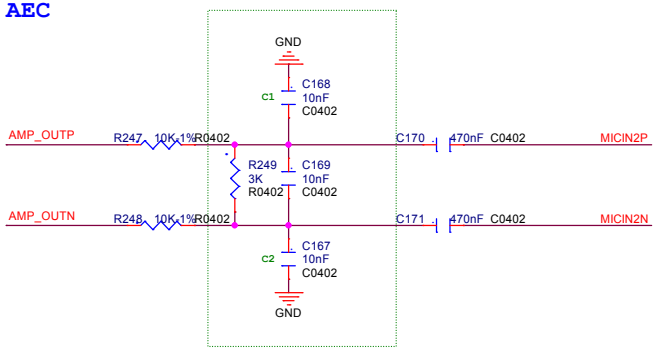
SPEAKER

D 类
option



AEC

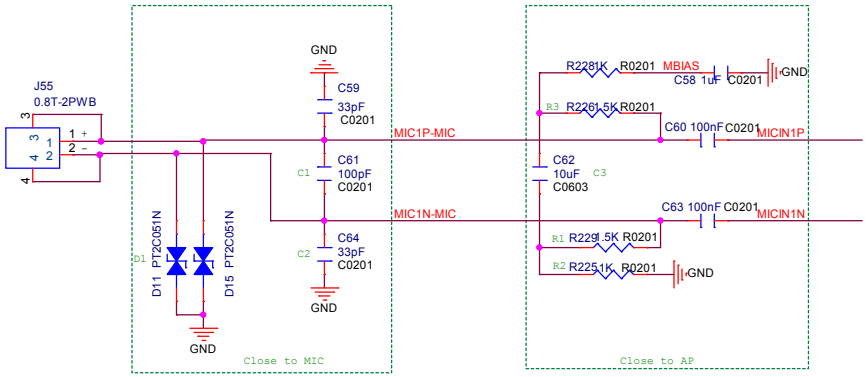


	C1/C2
AB AMP	NC
D AMP	10nF

[6] MICIN1P
[6] MICIN1N
[6] MICIN2P
[6] MICIN2N
[6] MBIAS
[6] LINEOUTLP
[6] LINEOUTLN
[7] PI9
PA-SHDN

VCC-5V 1A
AVCC 15mA

MIC



Component	Diferential	single-ended
R1 R2 C1 C3 D1	USE	NC
C2	33pF	0R
R3	1.5K	1K

ArtInChip Technology Co.,Ltd

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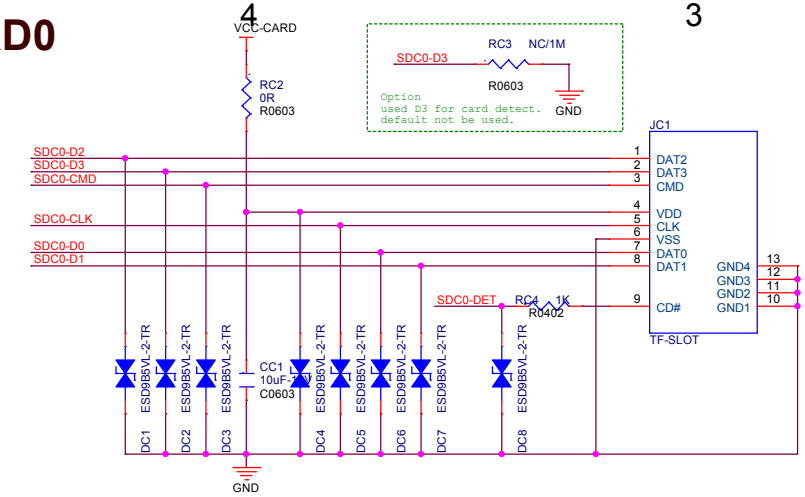
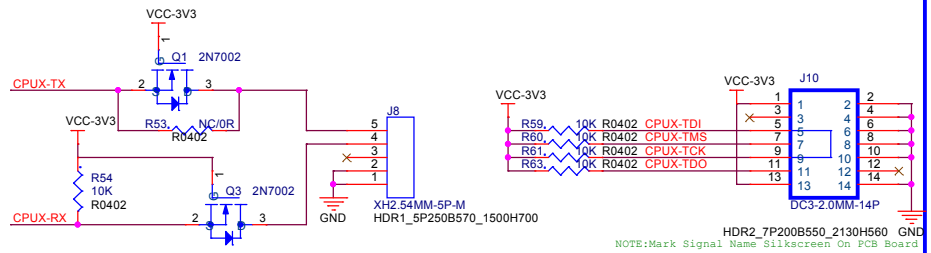
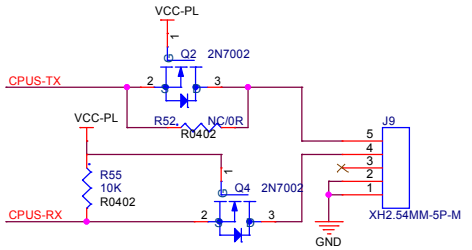
CARD0



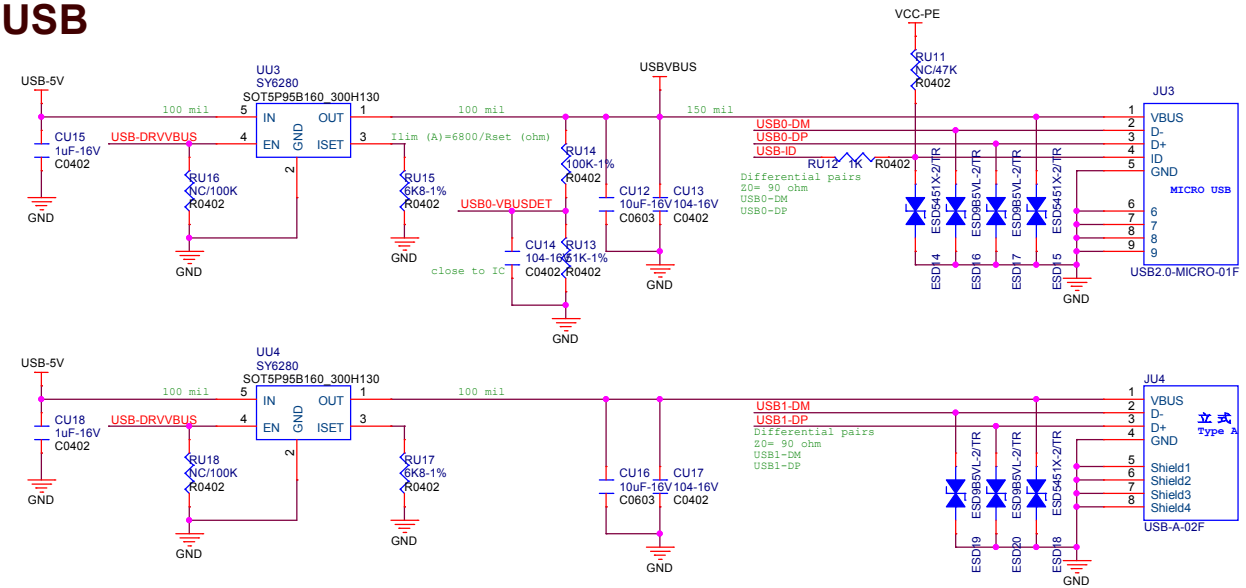
CPUX DEBUG



CPUS DEBUG



USB



VCC-3V3 — RP83 0.01R-1% — R0603 — VCC-CARD

7 SDC0-D1
7 SDC0-D0
7 SDC0-CLK
7 SDC0-CMD
7 SDC0-D3
7 SDC0-D2
7 SDC0-DET

VCC-3V3
VCC-PL 10mA

7 CPUX-TX
7 CPUX-RX
7 CPUX-TDI
7 CPUX-TMS
7 CPUX-TCK
7 CPUX-TDO

7 CPUS-TX
7 CPUS-RX

VCC-5V — R232 0R R0805 — USB-5V
R233 0R R0805
D19 SS34 SOD591_686H234 — USBVBUS

6 USB0-DP
6 USB0-DM
6 USB1-DP
6 USB1-DM

7 PB6 USB-ID
7 PB7 USB0-VBUSDET
7 PB8 USB-DRVVBUS

ArtInChip Technology Co.,Ltd			
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MIPI-CSI

MCSIA-D0[P/N]
MCSIA-D1[P/N]
MCSIA-D2[P/N]
MCSIA-D3[P/N]
MCSIA-CLK[P/N]
Differential pairs
Z0= 100 ohm

MCSIB-D0[P/N]
MCSIB-D1[P/N]
MCSIB-CLK[P/N]
Differential pairs
Z0= 100 ohm

NOTE:
Because GC2385 and GC5025 has the same I2C address, GC2385 and GC5025 cannot be used at the same time.

VCC-5V
VCC-3V3
IOVDD-CSI 100mA
DVDD-CSI 100mA
VCC-PE 10mA

6 MCSIA-D0P
6 MCSIA-D0N
6 MCSIA-D1P
6 MCSIA-D1N
6 MCSIA-D2P
6 MCSIA-D2N
6 MCSIA-D3P
6 MCSIA-D3N
6 MCSIA-CLKP
6 MCSIA-CLKN

6 MCSIB-D0P
6 MCSIB-D0N
6 MCSIB-D1P
6 MCSIB-D1N
6 MCSIB-CLKP
6 MCSIB-CLKN

7 MIPI-MCLK0 — MCSIA-MCLK
7 TWI2-SCK — MCSIA-SCK
7 TWI2-SDA — MCSIA-SDA
7 MIPI-MCLK1 — MCSIB-MCLK
7 TWI3-SCK — MCSIB-SCK
7 TWI3-SDA — MCSIB-SDA

7 PE6 — MCSI-RST-F
7 PE7 — MCSI-STBY-F
7 PJ18 — MCSI-RST-R
7 PJ19 — MCSI-STBY-R

MCSIA-RESET — MCSI-RST-F
MCSIA-PWDN — MCSI-STBY-F
MCSIB-RESET — MCSI-RST-R
MCSIB-PWDN — MCSI-STBY-R

VCC-PE
R29 100K R0201 MCSI-RST-F
R30 100K R0201 MCSI-STBY-F

VCC-PJ
R94 100K R0201 MCSI-RST-R
R31 100K R0201 MCSI-STBY-R

AFVCC-CSI: C103 1uF C0201
AVDD-CSI: C66 4.7uF C0402
IOVDD-CSI: C67 1uF C0201
DVDD-CSI: C68 1uF C0201
GND

NOTE:
Close to connector

NOTE:

1.The working voltage and current of power need determine according to the peripheral specification.

2.AFVCC-CSI has timing requirements, don't share the same power with DOVDD-CSI.

3.The front and rear cameras need to consider the compatibility of DVDD-CSI voltage.

Option

CON3 — CON39PIN

1	GND0	
MCSIA-D3N	2	MDN3_B
MCSIA-D3P	3	MDP3_B
4	GND1	
MCSIA-D2N	5	MDN2_B
MCSIA-D2P	6	MDP2_B
7	GND2	
MCSIA-D1N	8	MDN1_B
MCSIA-D1P	9	MDP1_B
10	GND3	
MCSIA-D0N	11	MDN0_B
MCSIA-D0P	12	MDP0_B
13	GND4	
MCSIA-CLKN	14	MCN_B
MCSIA-CLKP	15	MCP_B
16	GND5	
MCSIB-D1N	17	MDN1_F
MCSIB-D1P	18	MDP1_F
19	GND6	
MCSIB-D0N	20	MDN0_F
MCSIB-D0P	21	MDP0_F
22	GND7	
MCSIB-CLKN	23	MCN_F
MCSIB-CLKP	24	MCP_F
25	GND8	
MCSIA-MCLK	26	MCLK_F
27	GND9	
MCSIA-MCLK	28	MCLK_B
29	GND10	
MCSI-STBY-F	30	PWDN_F
MCSI-RST-F	31	RESET_F
MCSI-RST-R	32	RESET_B
MCSI-STBY-R	33	PWDN_B
MCSIA-SCK	34	SCL
MCSIA-SDA	35	SDA
AFVCC-CSI	36	AF_VDD28
DVDD-CSI	37	DVDD
AVDD-CSI	38	AVDD28
IOVDD-CSI	39	DOVDD18
	40	40
	41	41

C65 NC C0201 — GND

Connet IMX278 Sensor Module

J23 — FPC-26P-0.5MM-B
FPC28P50B450_1710H290_B

MCSIA-RESET	1	RESET
MCSIA-PWDN	2	PWRDN
MCSIA-MCLK	3	MCLK
	4	GND1
MCSIA-SCK	5	SCK
MCSIA-SDA	6	SDA
DVDD-CSI	7	HS
DVDD-CSI	8	DVDD
AVDD-CSI	9	VS
AVDD-CSI	10	AVDD
IOVDD-CSI	11	IOVDD
	12	GND2
MCSIA-D0P	13	D0P
MCSIA-D0N	14	D0N
	15	GND3
MCSIA-D1P	16	D1P
MCSIA-D1N	17	D1N
	18	GND4
MCSIA-CLKP	19	CKP
MCSIA-CLKN	20	CKN
	21	GND5
MCSIA-D2P	22	D2P
MCSIA-D2N	23	D2N
	24	GND6
MCSIA-D3P	25	D3P
MCSIA-D3N	26	D3N
	27	GND8
	28	GND7

GND, GND, GND

Connet IMX278 Sensor Module

J24 — FPC-26P-0.5MM-B
FPC28P50B450_1710H290_B

MCSIB-RESET	1	RESET
MCSIB-PWDN	2	PWRDN
MCSIB-MCLK	3	MCLK
	4	GND1
MCSIB-SCK	5	SCK
MCSIB-SDA	6	SDA
DVDD-CSI	7	HS
DVDD-CSI	8	DVDD
AVDD-CSI	9	VS
AVDD-CSI	10	AVDD
IOVDD-CSI	11	IOVDD
	12	GND2
MCSIB-D0P	13	D0P
MCSIB-D0N	14	D0N
	15	GND3
MCSIB-D1P	16	D1P
MCSIB-D1N	17	D1N
	18	GND4
MCSIB-CLKP	19	CKP
MCSIB-CLKN	20	CKN
	21	GND5
	22	D2P
	23	D2N
	24	GND6
	25	D3P
	26	D3N
	27	GND8
	28	GND7

GND, GND, GND

FLASH_LED

VCC-5V
FR1 NC/4.7R R0603
FC1 NC/104 C0201 — GND
J6 PAD2PIN
FQ1 NC/SI2300 SOT23 — GND
VCC-PL
FR2 NC/10K R0201
FR4 NC/10K R0201 — GND
FC2 NC/104 C0201 — GND
LED-EN

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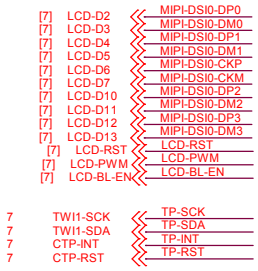
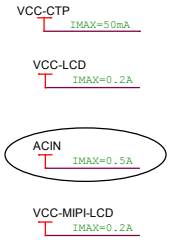
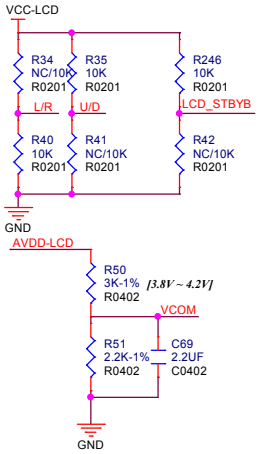
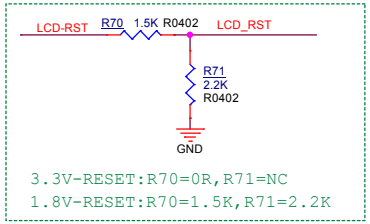
ArtInChip Technology Co.,Ltd

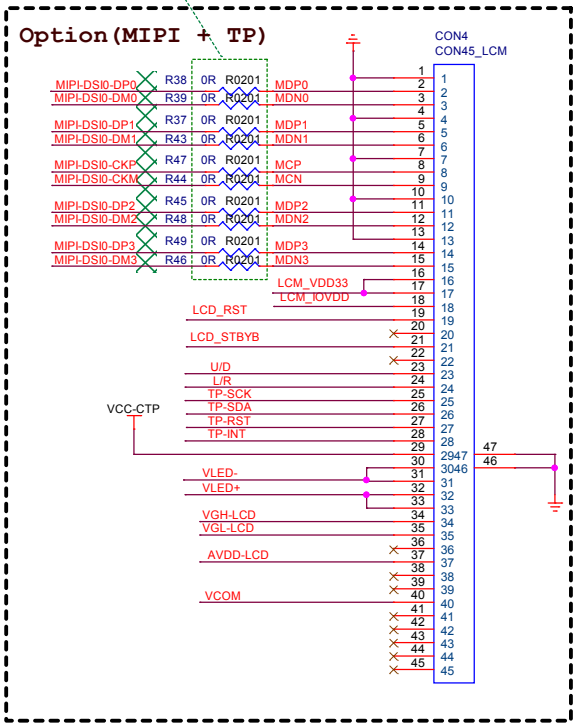
LCD

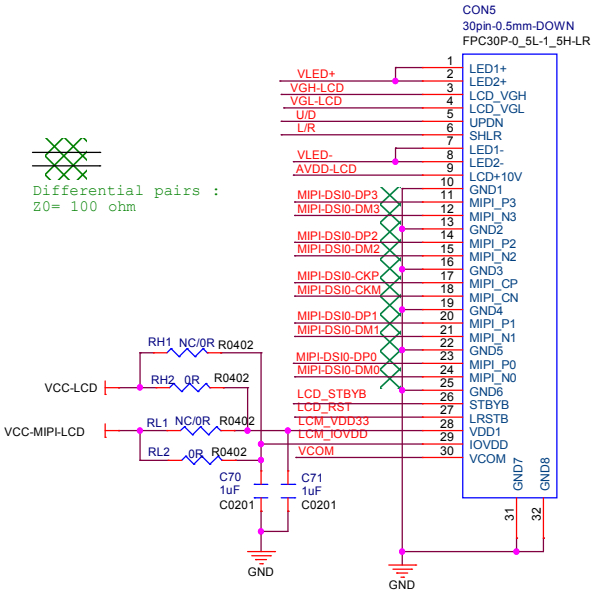




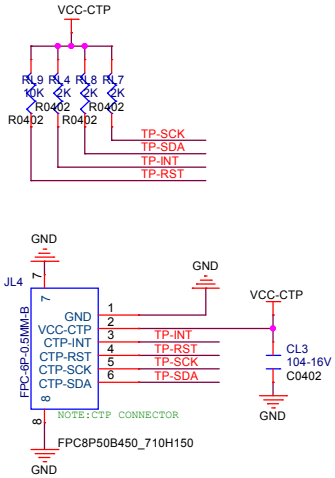




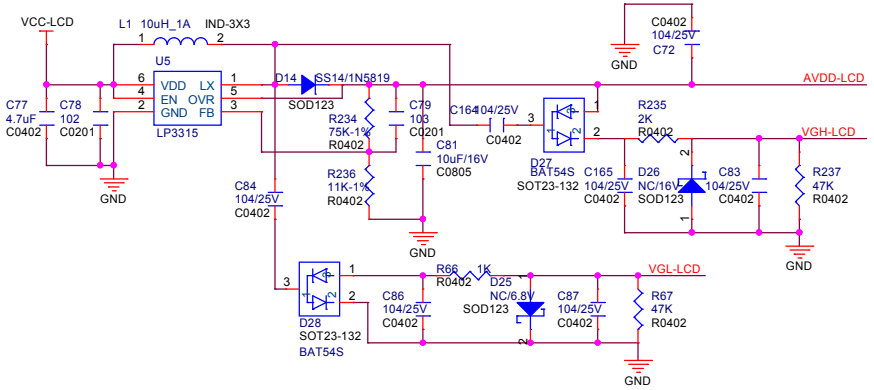




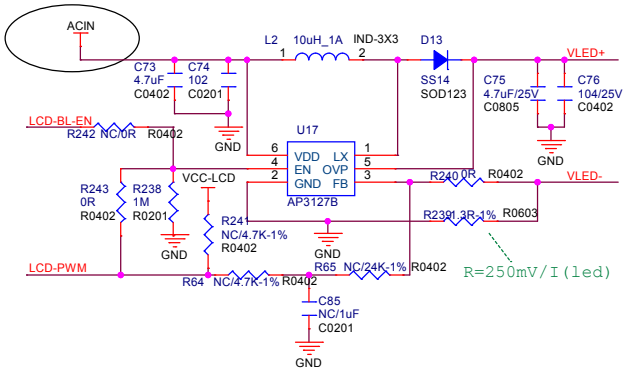
CTP



LCD POWER



BACKLIGHT



ArtInChip Technology Co.,Ltd		
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Size: A3	Page Name: LCM/CTP	Rev: 2.0
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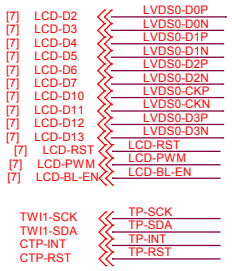
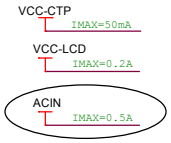
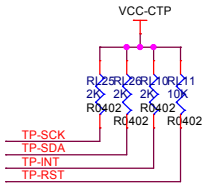
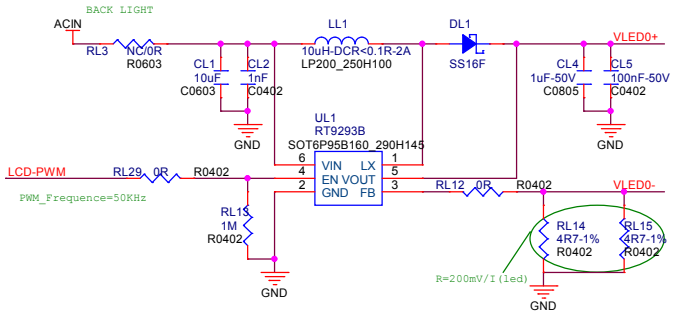
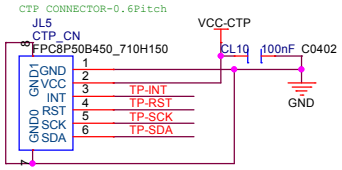
LVDS(Option)

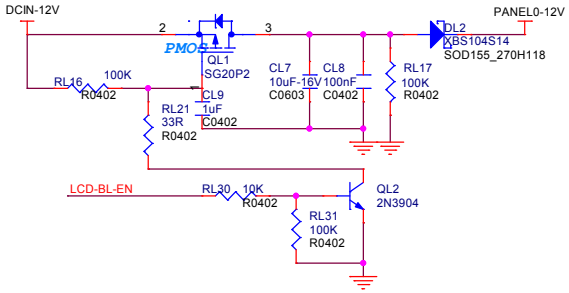


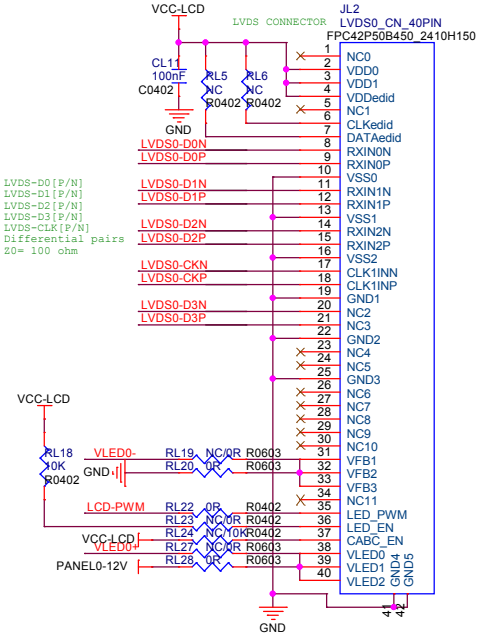












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Design Name: AIC800_REF_SIP_LP4_32X1_V1_0		
Size: A3	Page Name: LVDS	Rev: 2.0
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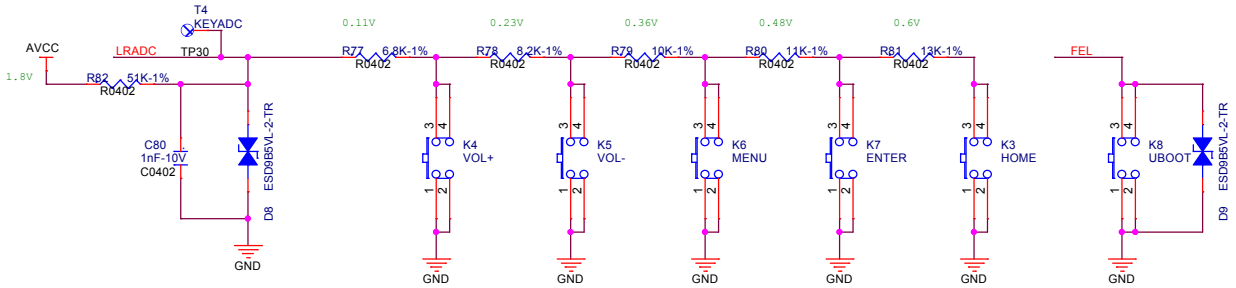
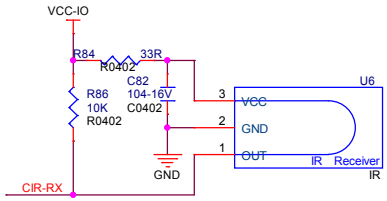
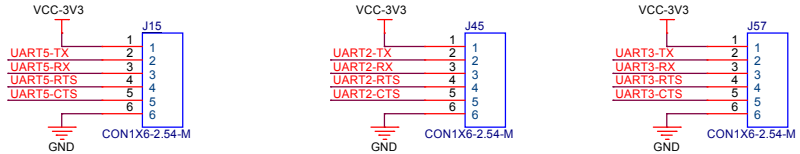
KEY



CIR



UART



AVCC
6 FEL
6 LRADC
7 S-CIR-IN CIR-RX
VCC-3V3
VCC-PL
7 UART5-TX
7 UART5-RX
7 UART5-RTS
7 UART5-CTS
7 UART2-TX
7 UART2-RX
7 UART2-RTS
7 UART2-CTS
7 UART3-TX
7 UART3-RX
7 UART3-RTS
7 UART3-CTS

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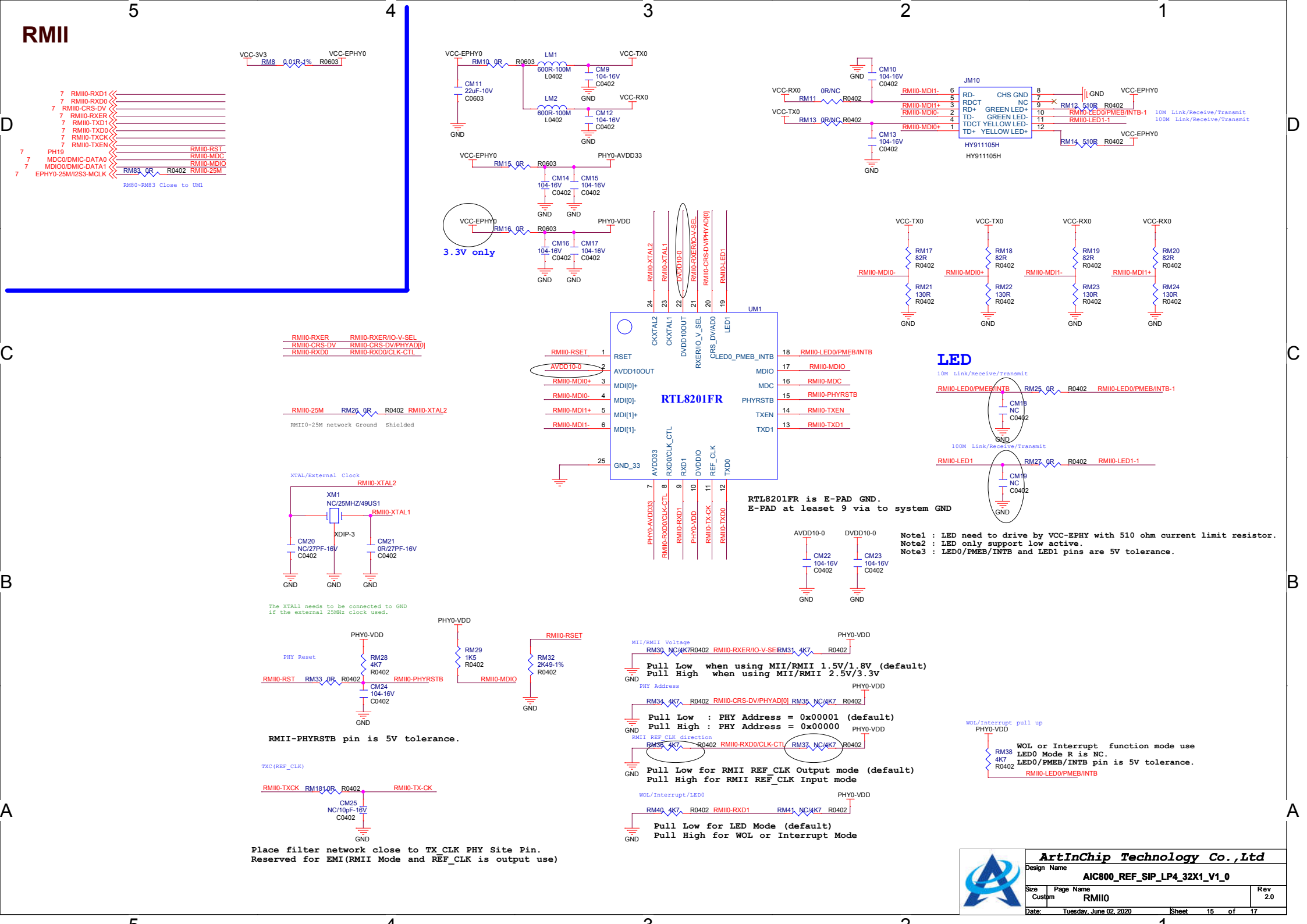
RMII



ArtInChip Technology Co.,Ltd	
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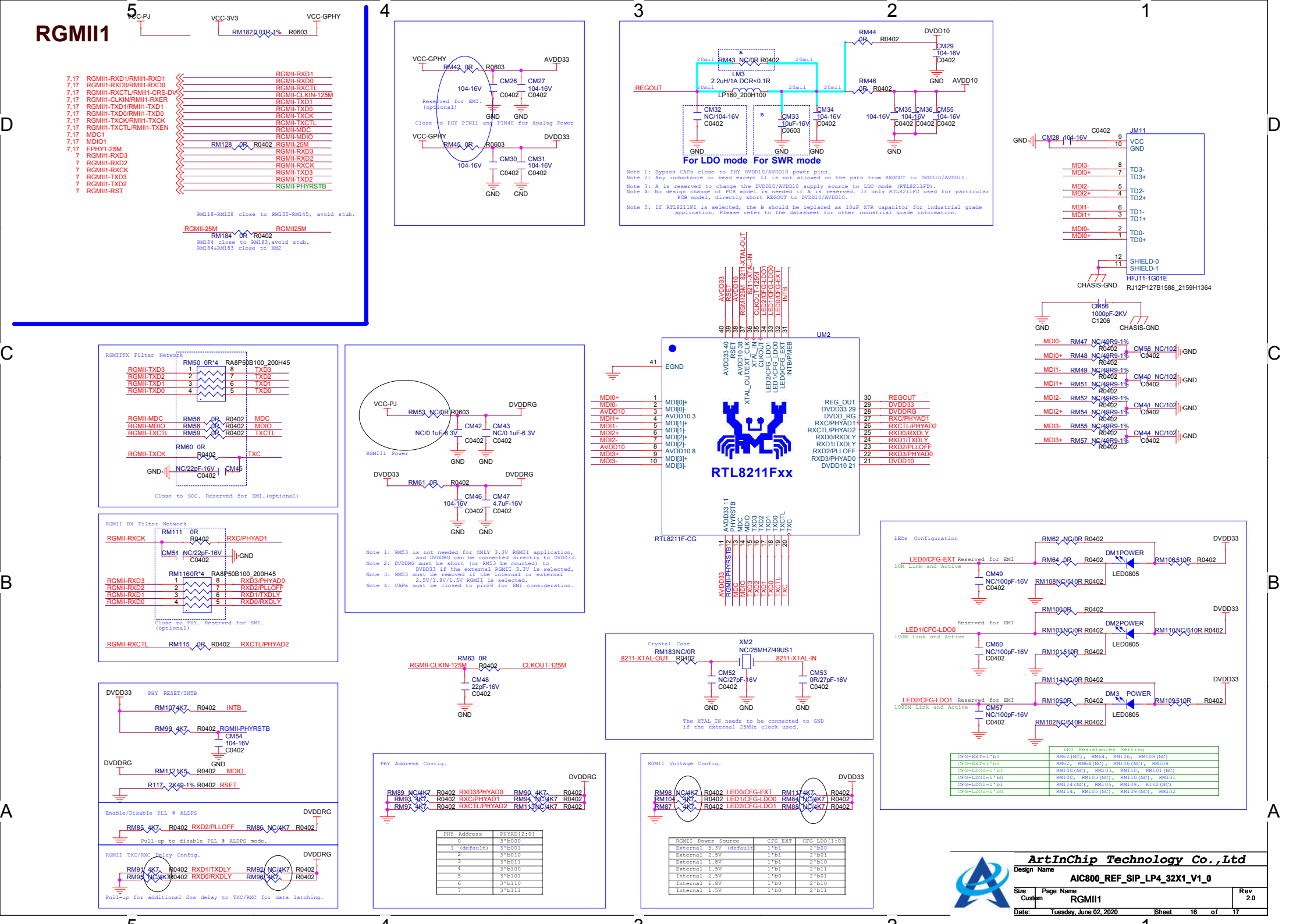
RGMII1

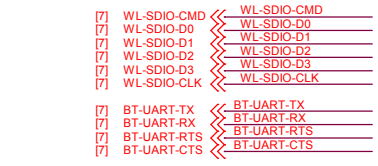


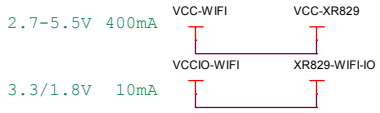
LED Resistances Setting
CFG-EXT=1'b1	RM62(NC), RM64, RM106, RM108(NC)
CFG-EXT=1'b0	RM62, RM64(NC), RM106(NC), RM108
CFG-LDO0=1'b1	RM100(NC), RM103, RM110, RM101(NC)
CFG-LDO0=1'b0	RM100, RM103(NC), RM110(NC), RM101
CFG-LDO1=1'b1	RM114(NC), RM105, RM109, R102(NC)
CFG-LDO1=1'b0	RM114, RM105(NC), RM109(NC), RM102

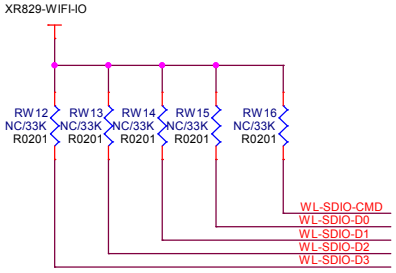
PHY Address	PHYAD[2:0]
0	3'b000
1 (default)	3'b001
2	3'b010
3	3'b011
4	3'b100
5	3'b101
6	3'b110
7	3'b111

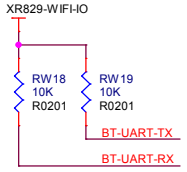
RGMII Power Source	CFG_EXT	CFG_LDO[1:0]
External 3.3V (default)	1'b1	2'b00
External 2.5V	1'b1	2'b01
External 1.8V	1'b1	2'b10
External 1.5V	1'b1	2'b11
Internal 2.5V	1'b0	2'b01
Internal 1.8V	1'b0	2'b10
Internal 1.5V	1'b0	2'b11

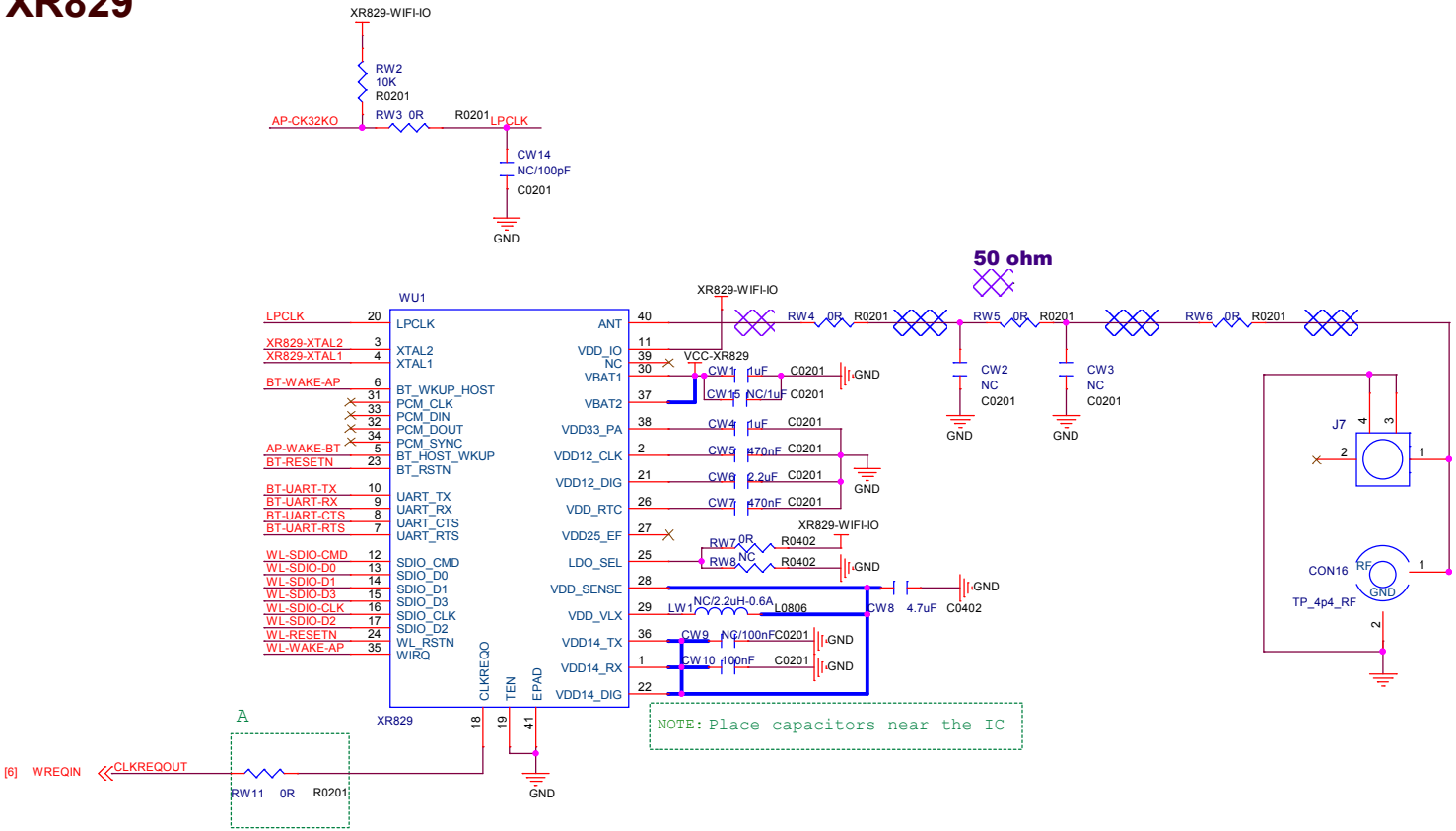
XR829





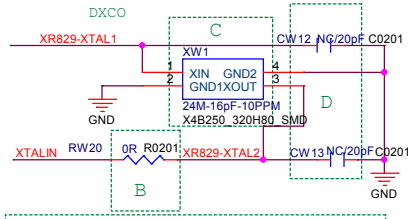






NOTE:

XR829 24M Crystal source	A	B	C	D
Crystal	NC	NC	24M-16pF-10PPM	CW12=20pF,CW13=20pF
DCXO-RFCLK	0R	0R	NC	CW12=0R,CW13=NC



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