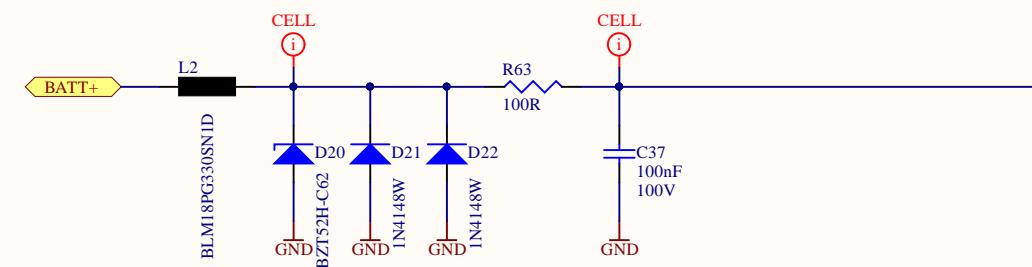


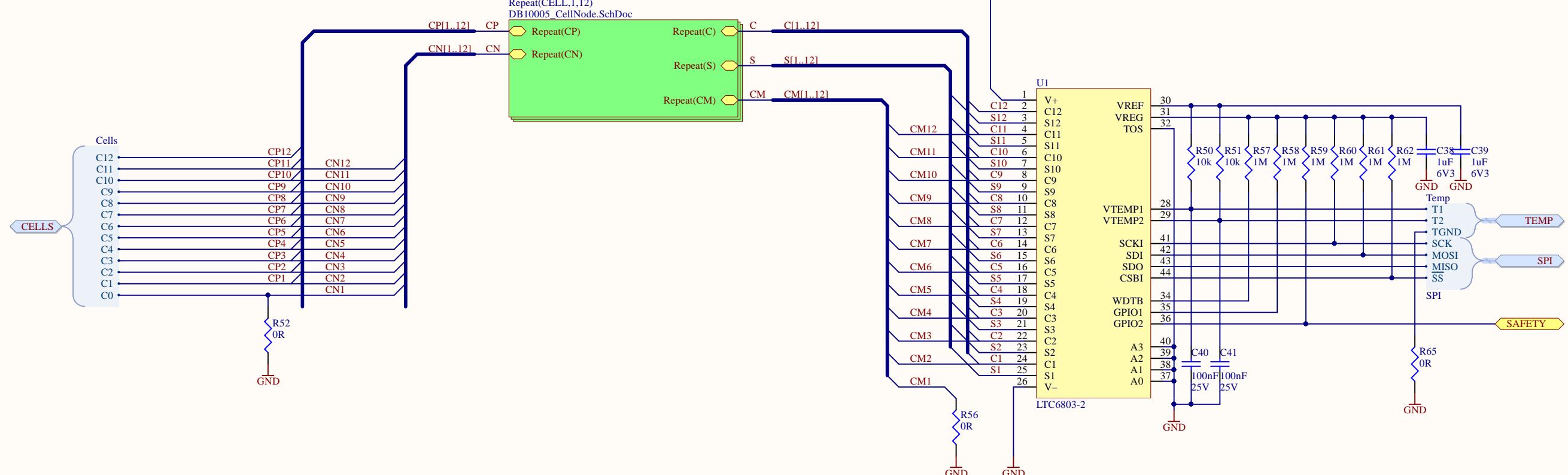
Title ***TopLevel schematic*** \*  
 Size: A3 Number: \* Revision: \*  
 Date: 8-8-2017 Time: 23:52:26 Sheet 1 of 7 \*  
 File: C:\Projecten\DieBieMS\GitHub\Hardware\DB10005\_TopLevel.SchDoc \*



A

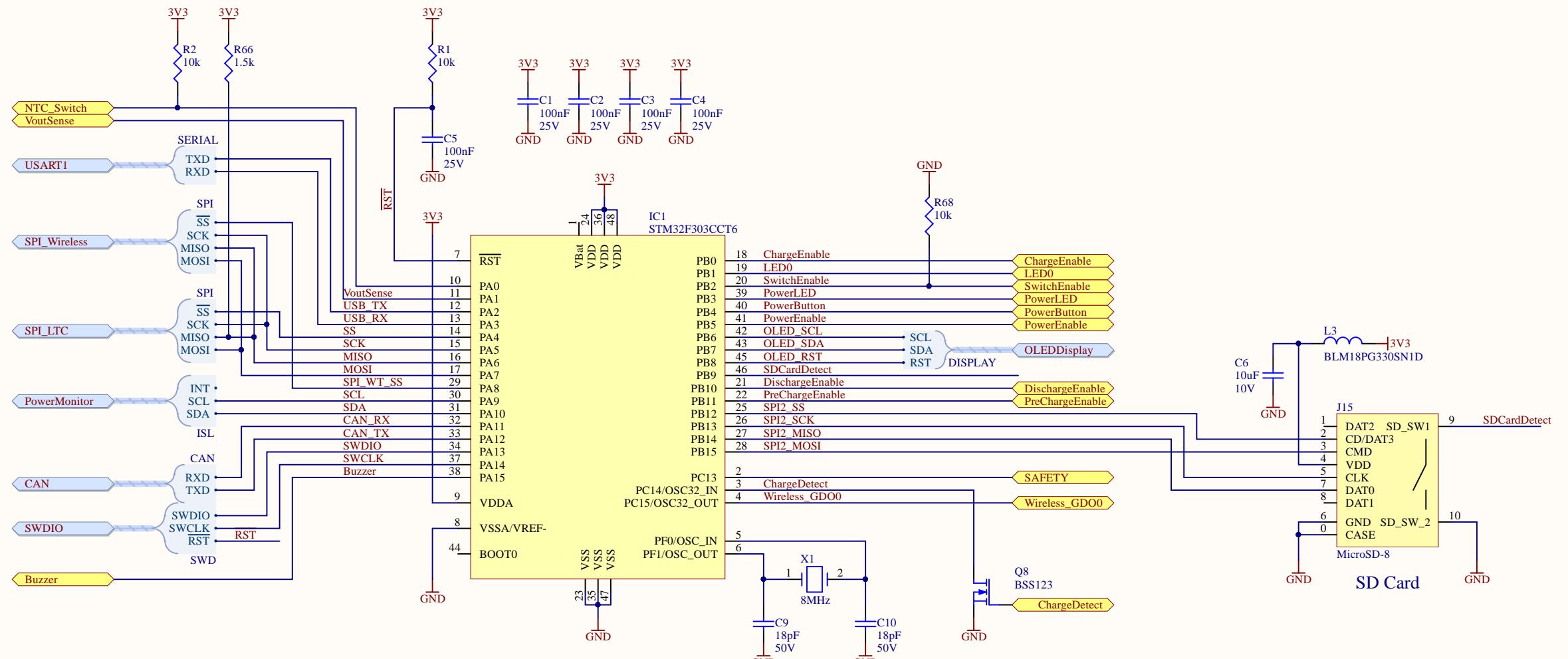


B



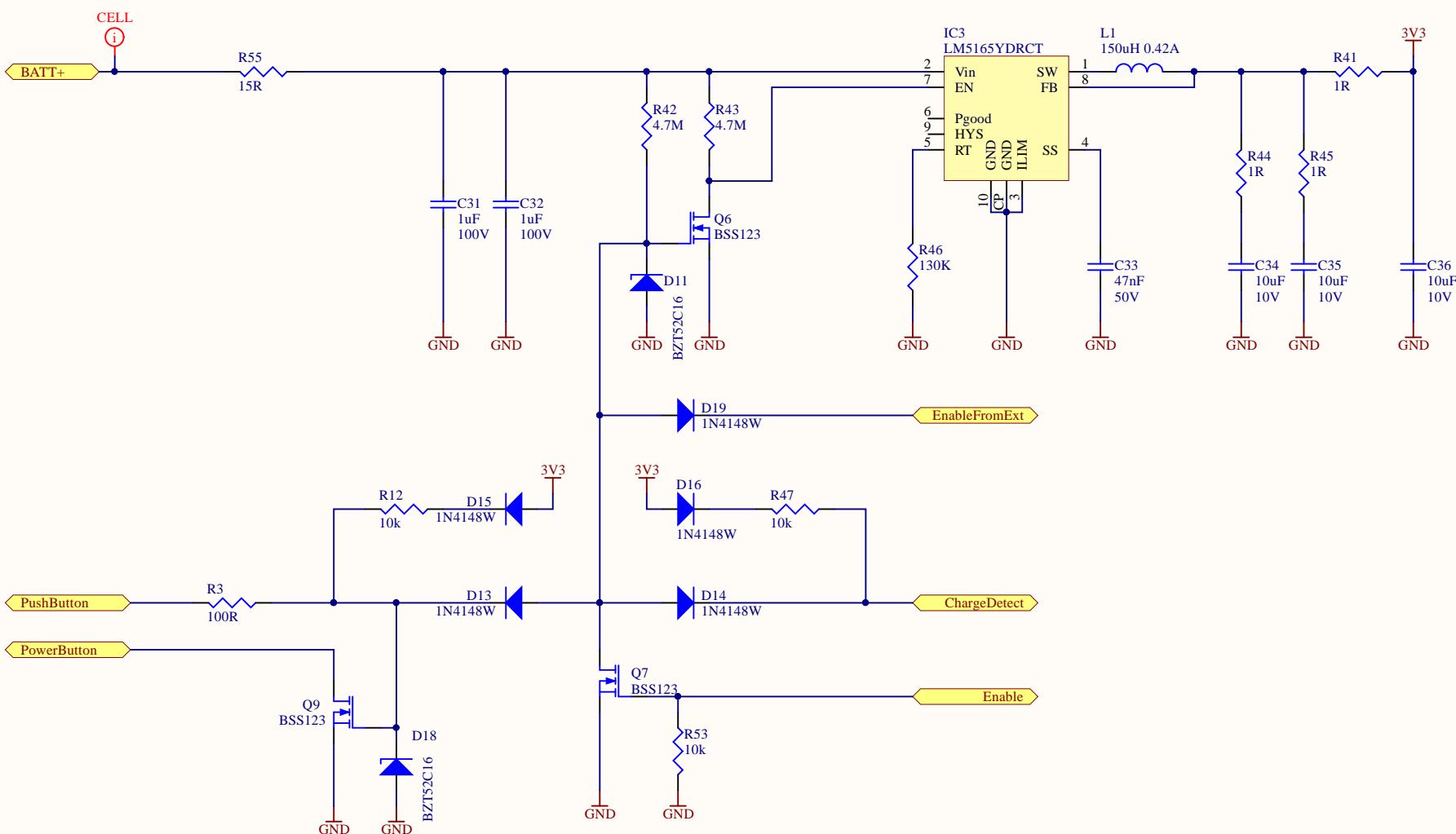
D

A A



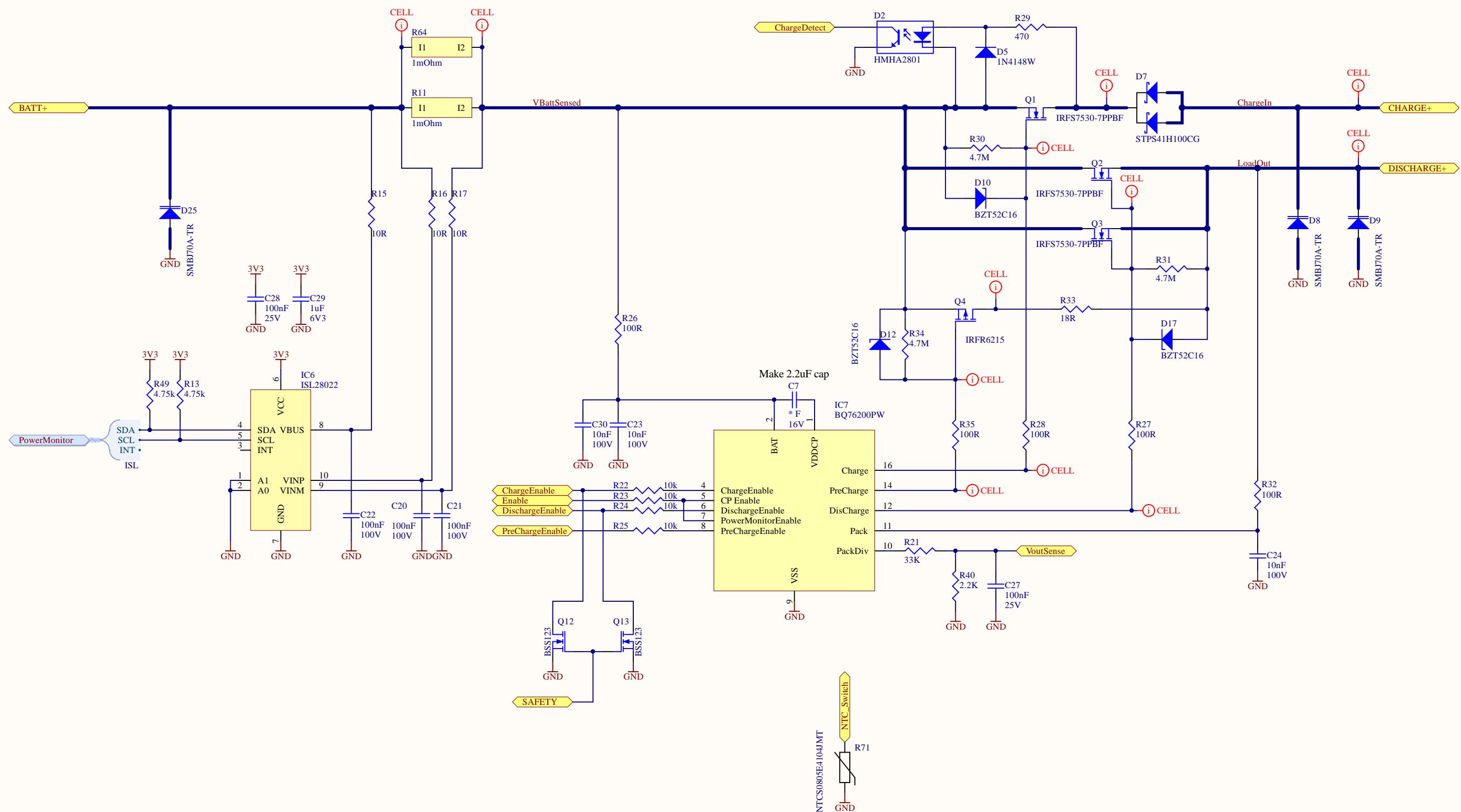
Title: Controller	*	*	*
Size: A3	Number:*	Revision:*	*
Date: 8-8-2017	Time: 23:52:26	Sheet3 of 7	*
File: C:\Projecten\DieBieMS\GitHub\Hardware\DB10005_Controller.SchDoc			

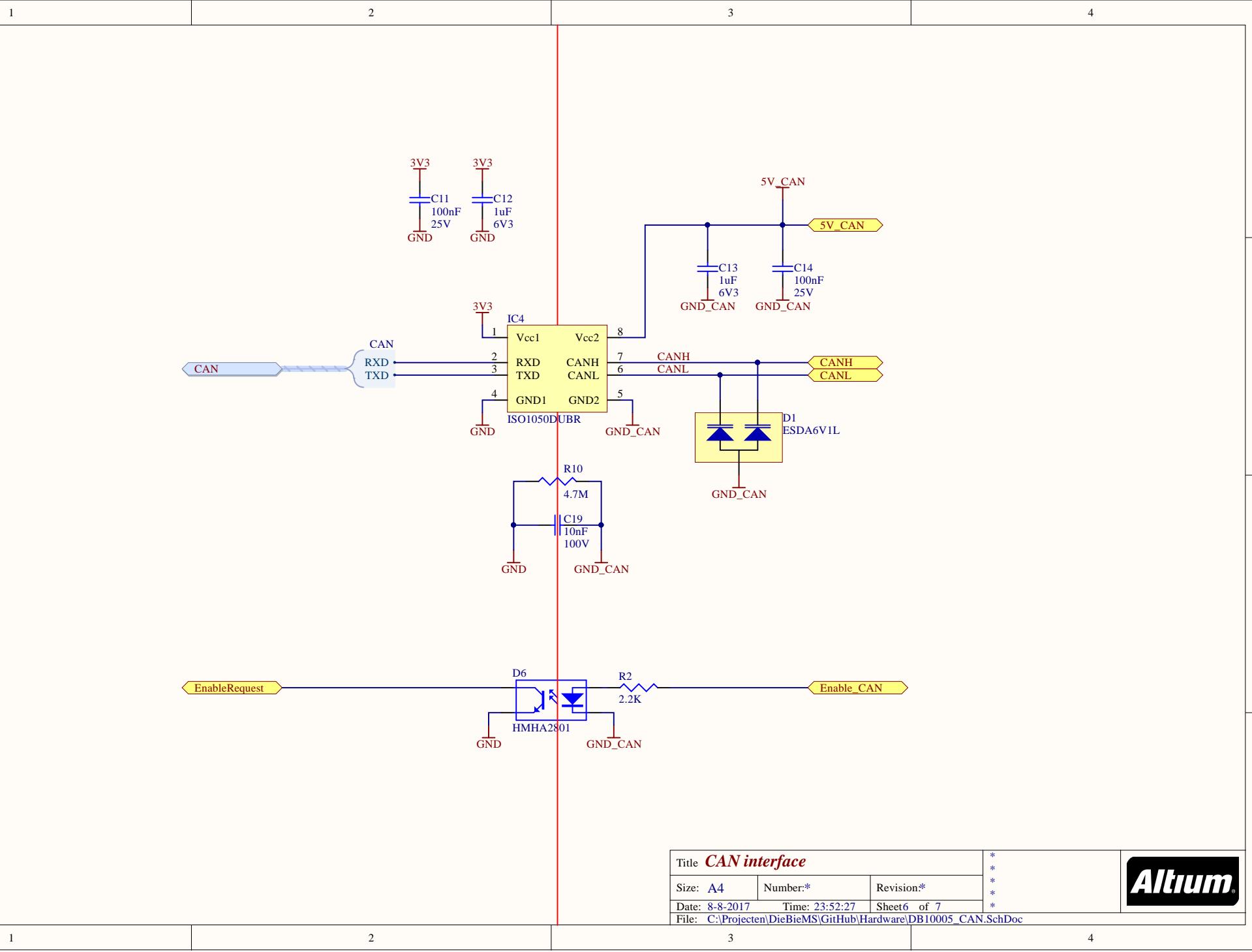




Title <b>Power management</b>			*
Size: <b>A4</b>	Number:*	Revision:*	*
Date: 8-8-2017	Time: 23:52:26	Sheet4 of 7	*
File: C:\Projecten\DieBieMS\GitHub\Hardware\DB10005_PowerManagement.SchDoc			*

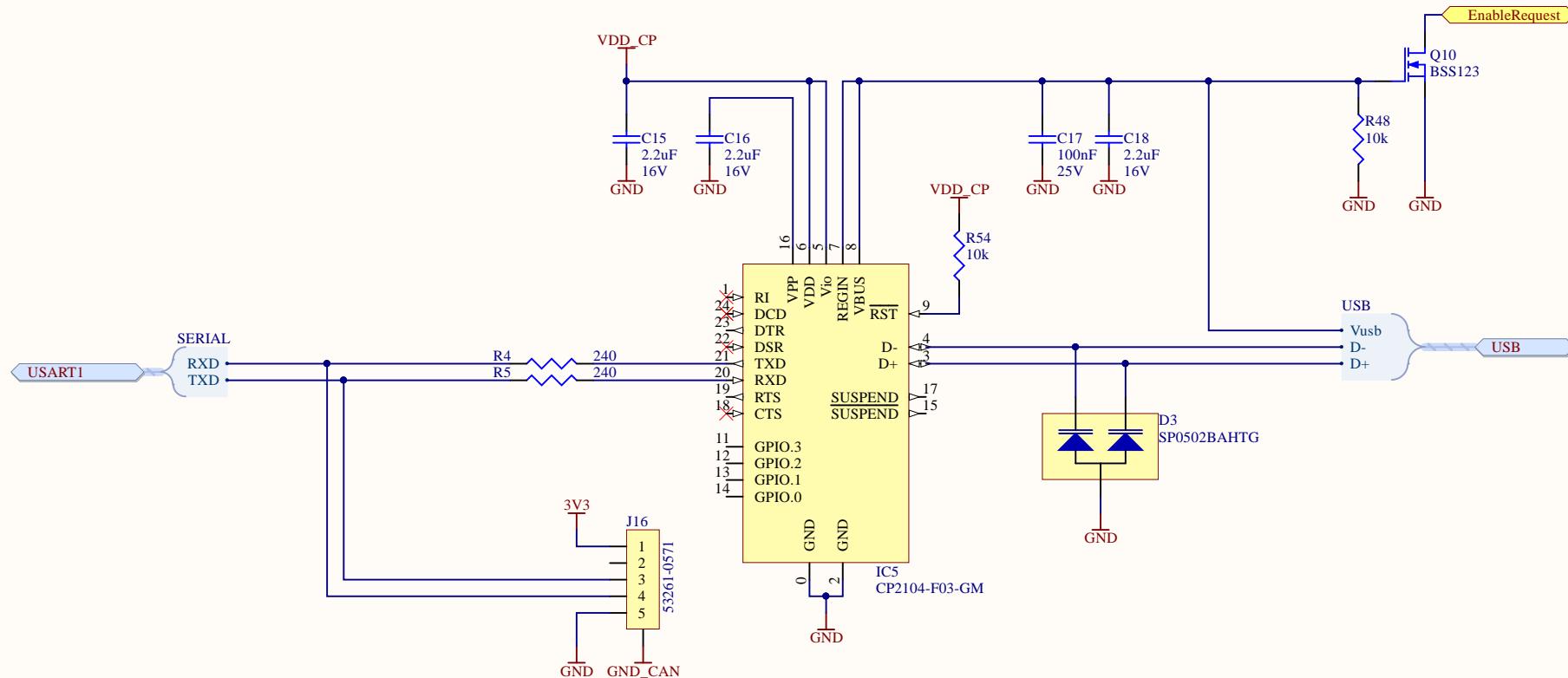
**Altium**





Title <b>CAN interface</b>		
Size: A4	Number:*	Revision:*
Date: 8-8-2017	Time: 23:52:27	Sheet 6 of 7
File: C:\Projecten\DieBieMS\GitHub\Hardware\DB10005_CAN.SchDoc		

**Altium**



Title <b>USB Serial interface</b>		
Size: A4	Number:*	Revision:*
Date: 8-8-2017	Time: 23:52:27	Sheet 7 of 7
File: C:\Projecten\DieBieMS\GitHub\Hardware\DB10005_USBSerial.SchDoc		

**Altium**

A

A

B

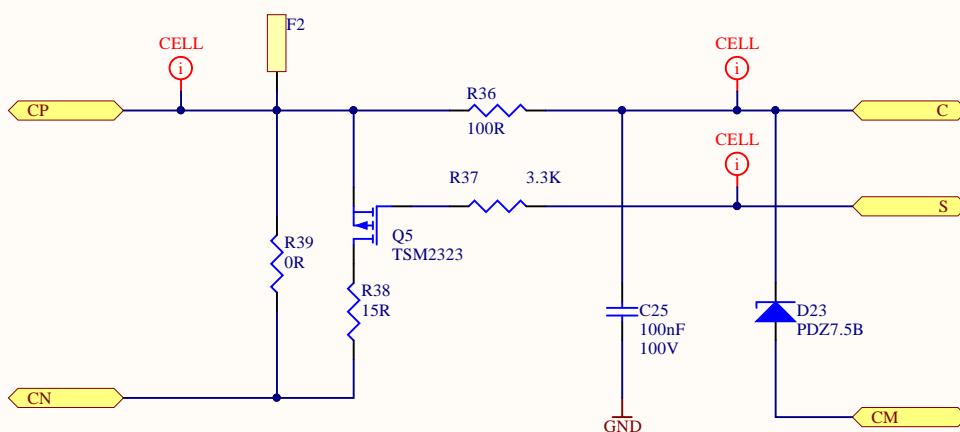
B

C

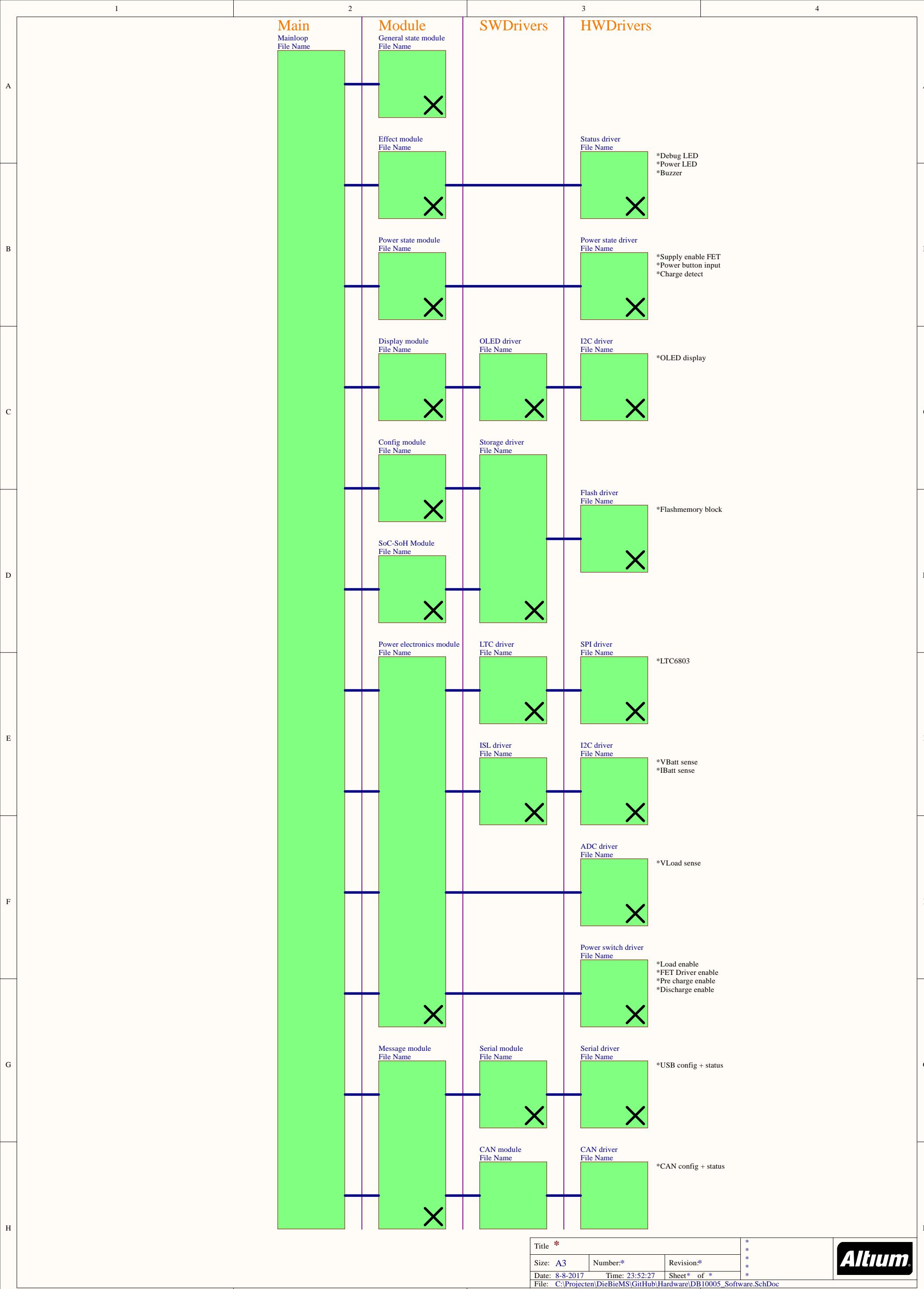
C

D

D



Title		
Size	Number	Revision
A4		
Date: 8-8-2017	Sheet of	
File: C:\Projecten\..\DB10005_CellNode.Sch	Drawn By:	



A

A

B

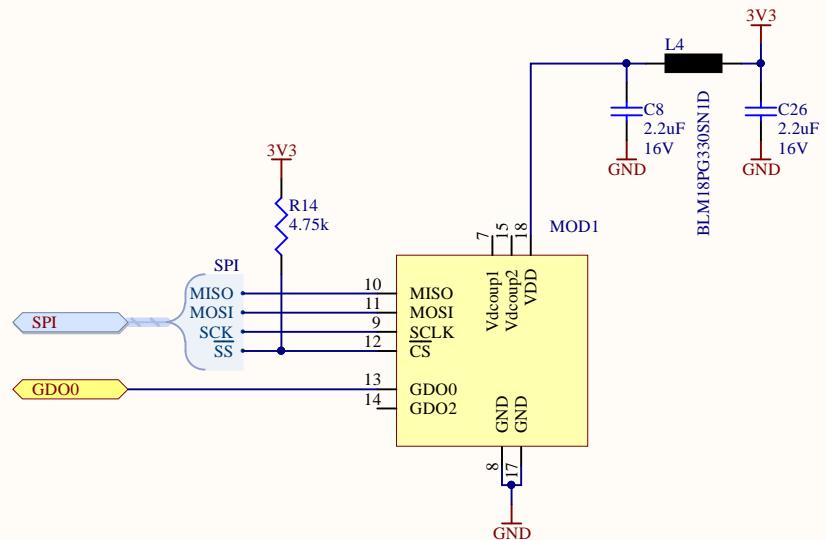
B

C

C

D

D



Title		
Size	Number	Revision
A4		
Date:	8-8-2017	Sheet of
File:	C:\Projecten\..\DB10005_Wireless.SchDoc	Drawn By:

