

The diagrams show the following pin connections:

- Right Header (J1):** Pin 10 to +3.3V, Pin 1 to GND.
- Left Header (J7):** Pin 10 to +3.3V, Pin 9 to +5V, Pin 1 to GND.
- Front Left Pins (J8):** Pin 4 to GND, Pin 1 to +3.3V.
- Front Right Pins (J10):** Pin 4 to +3.3V, Pin 1 to GND.

Diagram illustrating the pin configuration for the J6 SWD connector:

- Pin 1: + BATT, Vtref
- Pin 2: RESET
- Pin 3: GND
- Pin 4: SWDCLK/TCK
- Pin 5: SWDIO/TMS
- Pin 6: SWO/TDO
- Pin 7: NC/TDI
- Pin 8: GNDDetect

The diagrams show four instances of the DRV5015A1 comparator (U1, U2, U4, U5) connected to a microcontroller. Each comparator has a 10nF capacitor connected to its VCC pin (pin 1) and GND (pin 3). The output pin (pin 2) is connected to a microcontroller pin: P0_31 for U1, P0_21 for U2, P1_11 for U4, and P1_12 for U5. The comparators are powered by a +3.3V supply.

The diagram illustrates a circuit for controlling two LEDs, LD1 and LD2, using push-buttons P1_04 and P1_05. Each LED is connected to its respective push-button through a 100 ohm resistor (R12 for LD1, R13 for LD2). The circuit is grounded (GND).

MountingHole:MountingHole MountingHole:EdpOnly