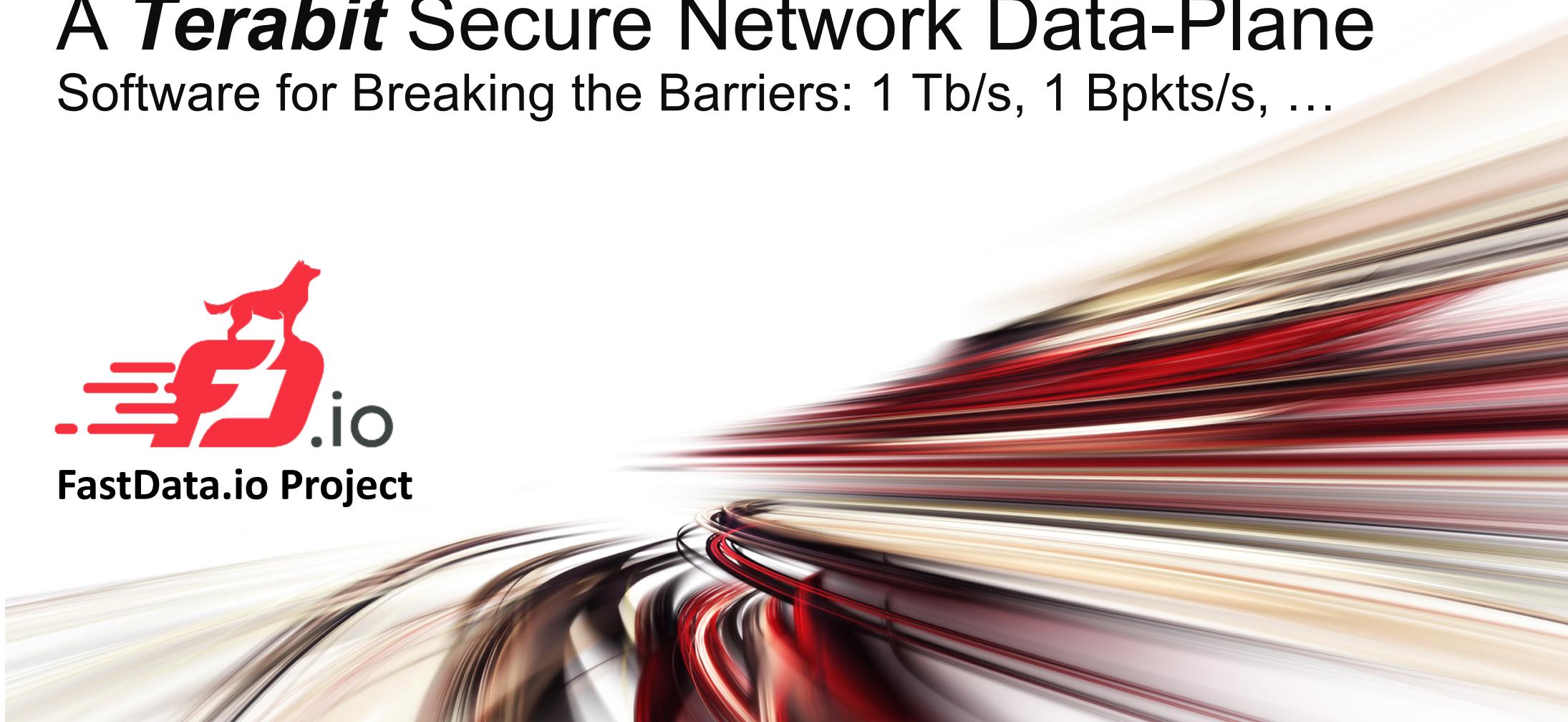


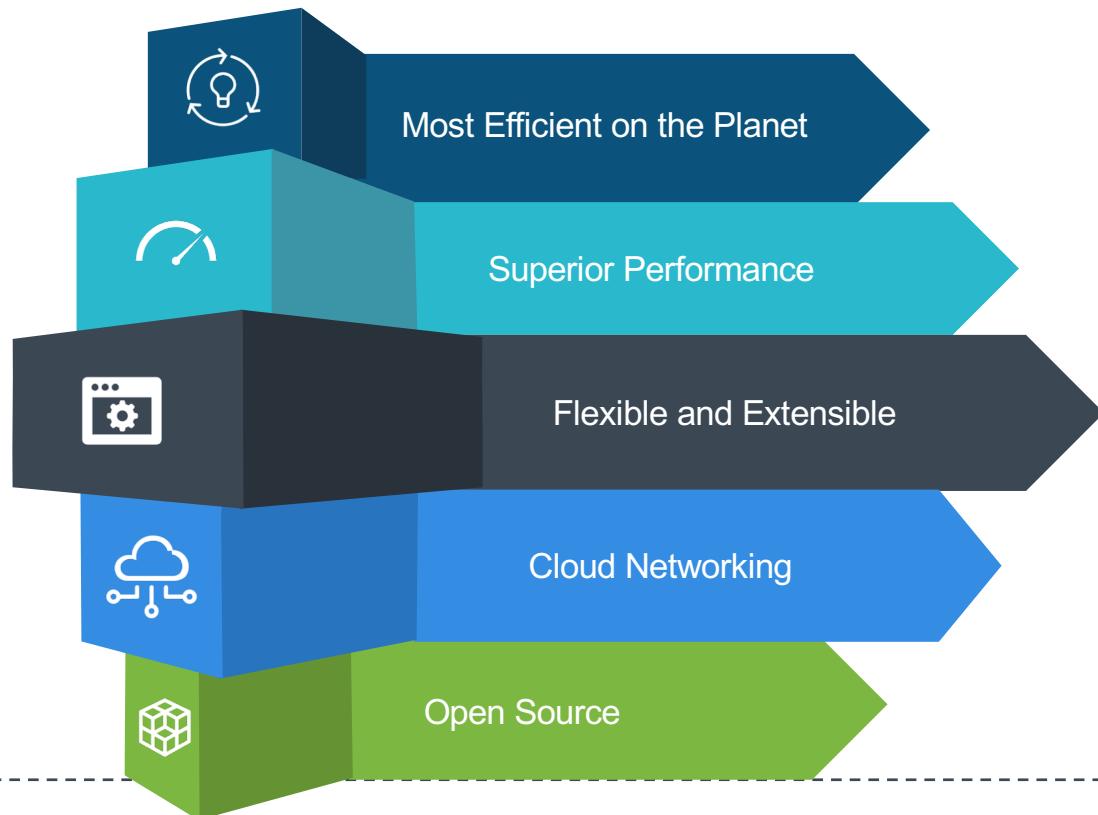
A *Terabit* Secure Network Data-Plane

Software for Breaking the Barriers: 1 Tb/s, 1 Bpkts/s, ...



A Terabit Secure Network Data-Plane

For Cloud Network Services



**Breaking the Terabit Barrier of Secure NaaS Services
on a Single 2-Socket Intel® Xeon® Server !**



EFFICIENCY

The most efficient software data plane Packet Processing on the planet



PERFORMANCE

FD.io on x86 servers outperforms specialized packet processing HW



SOFTWARE NETWORKING

Software programmable, extendable and flexible



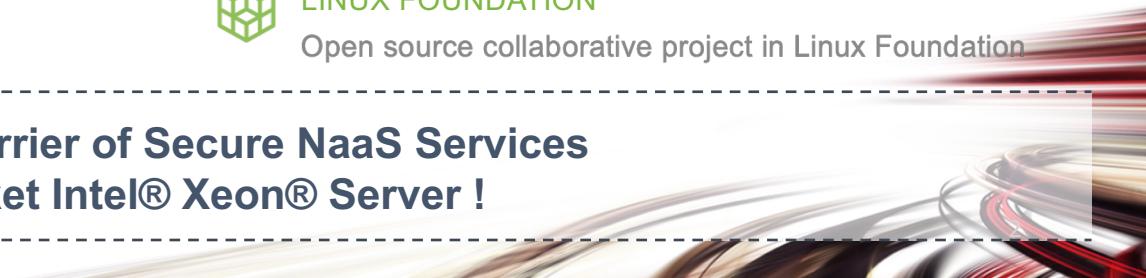
CLOUD NETWORK SERVICES

Foundation for cloud network services



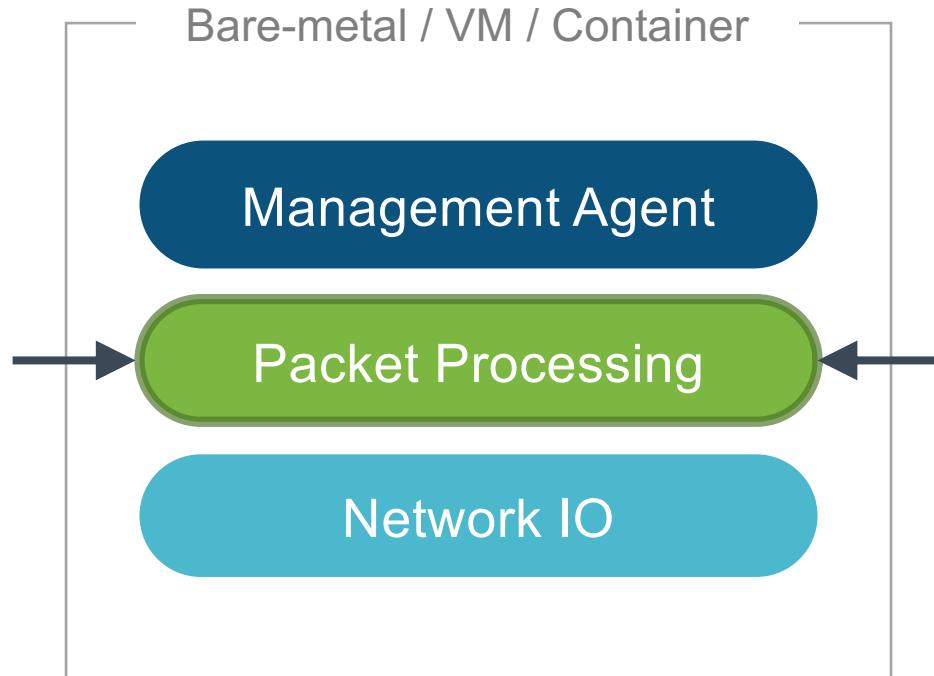
LINUX FOUNDATION

Open source collaborative project in Linux Foundation



FD.io VPP – Vector Packet Processor

Compute Optimized SW Network Platform



Packet Processing Software Platform

- High performance
- Linux user space
- Runs on compute CPUs:
- And “knows” how to run them well !



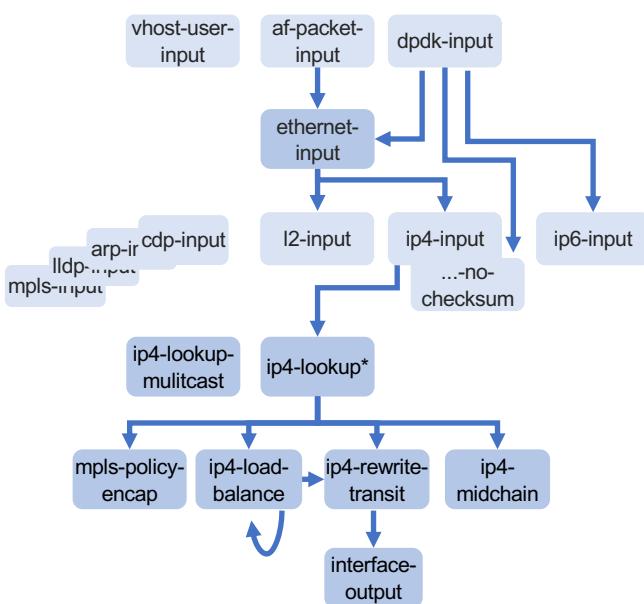
Shipping at volume in server & embedded products since 2004



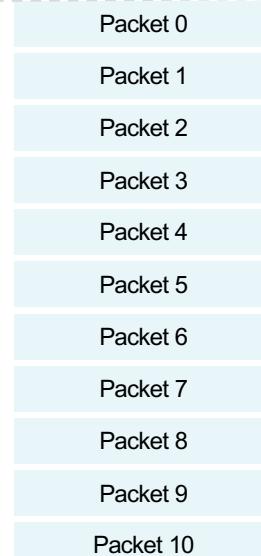
FD.io VPP – The “Magic” of Vectors

Compute Optimized SW Network Platform

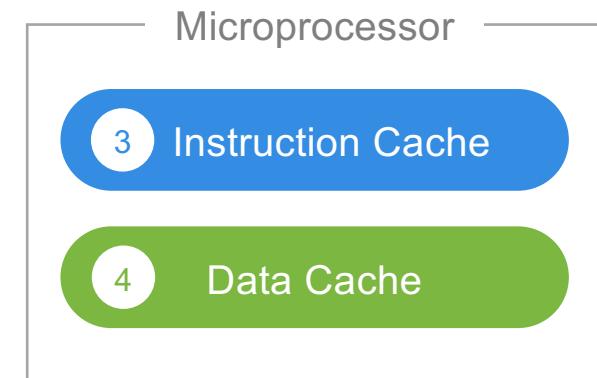
- 1 Packet processing is decomposed into a directed graph of nodes ...



- 2 ... packets move through graph nodes in vector ...



- 3 ... graph nodes are optimized to fit inside the instruction cache ...



- 4 ... packets are pre-fetched into the data cache.

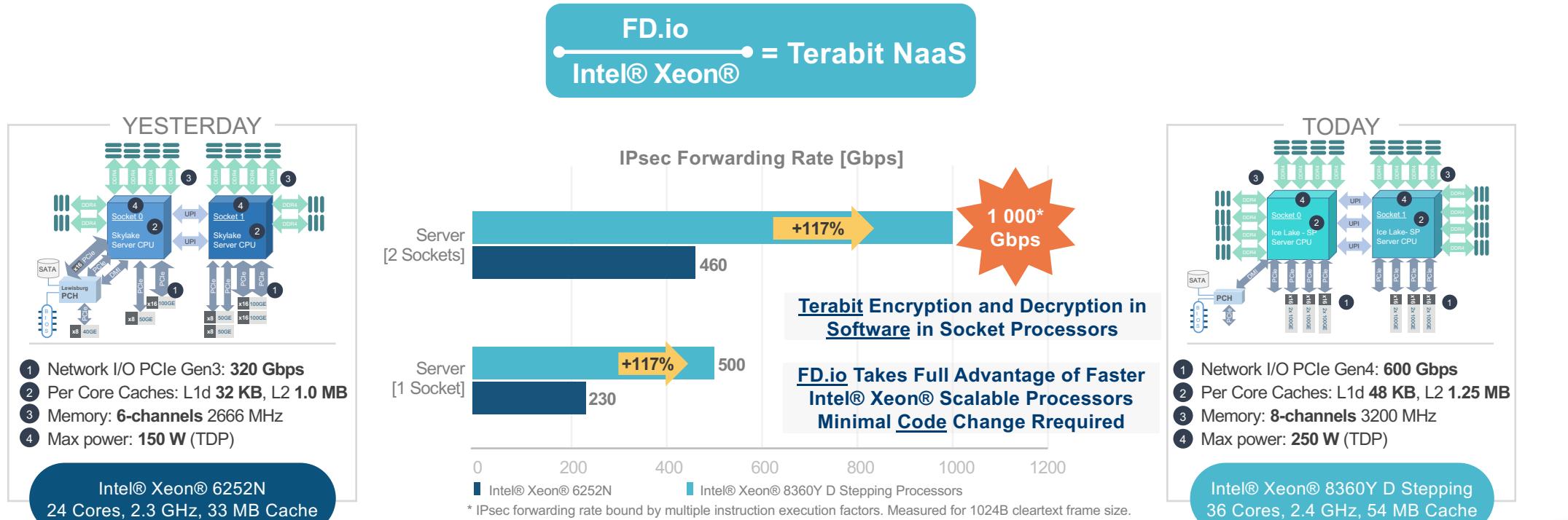
* Each graph node implements a “micro-NF”, a “micro-NetworkFunction” processing packets.



Makes use of modern Intel® Xeon® Processor micro-architectures.
Instruction cache & data cache always hot → Minimized memory latency and usage.

FD.io Benefits from Intel® Xeon® Processor Developments

Increased I/O and Cipher Processing Improve Crypto Throughput

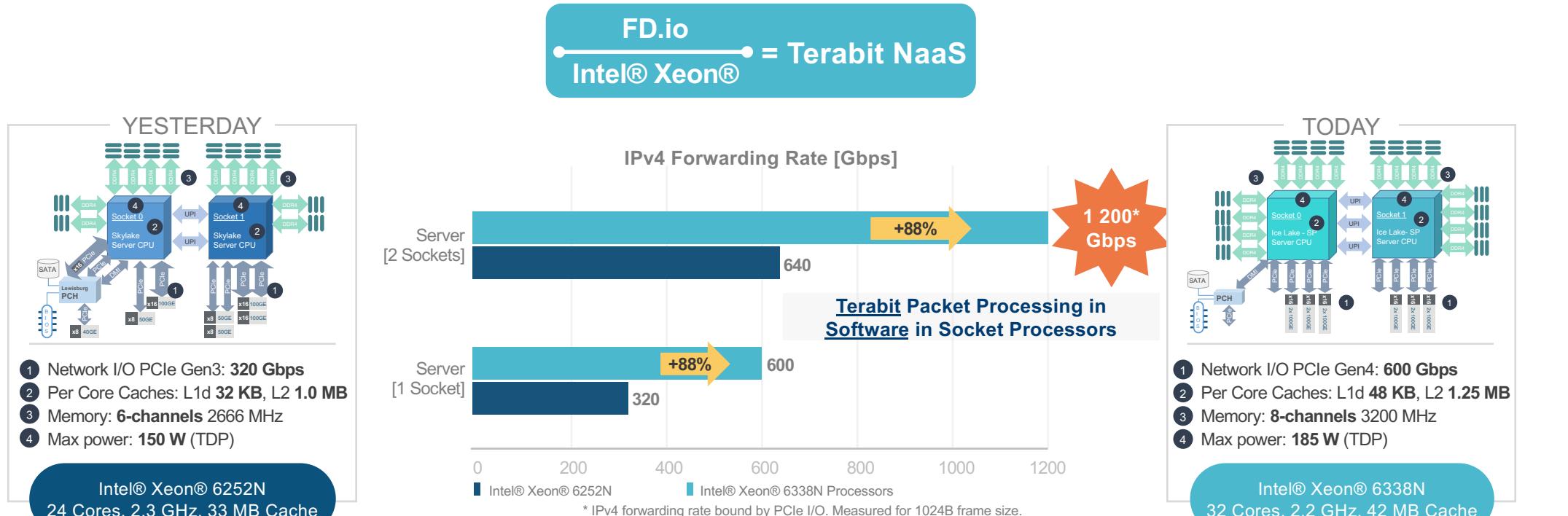


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FD.io Benefits from Intel® Xeon® Processor Developments

Increased I/O and Processing Power Improve Packet Throughput



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FD.io VPP & Cloud Network Services

Use Case Examples

FD.io
Intel® Xeon® = Terabit NaaS



SECURE NaaS CLOUD NETWORKING

Encrypted Overlays for Secure Access, Edge and Connectivity

- IPsec, TLS, QUIC, WireGuard - Fast Modern VPNs
- Horizontal Scaling with Near Linear Multi-core Speed-up
- Secure Tunnels: 40 Gbps / Core, up to 1 Tbps / 2 Sockets

Scale of Million Routes with Service Features

- Performance at Max. of PCIe I/O of Intel® Xeon® Server



PRODUCTION GRADE



SECURE NaaS CLOUD
NETWORKING



MULTI-TENANT
SERVICES



MULTI-TENANT SERVICES

Per Tenant Stateless and Stateful Packet Processing

- Rich IPv4/IPv6 Functionalities, API First Consumption Model
- Zero Cost Telemetry with Always-on Pervasive Data-plane Counters
- Service Features: ACLs, NAT, Sessions, VRFs

Multi-Tenant Scale and Elasticity

- Scale of Million Flows with Service Features



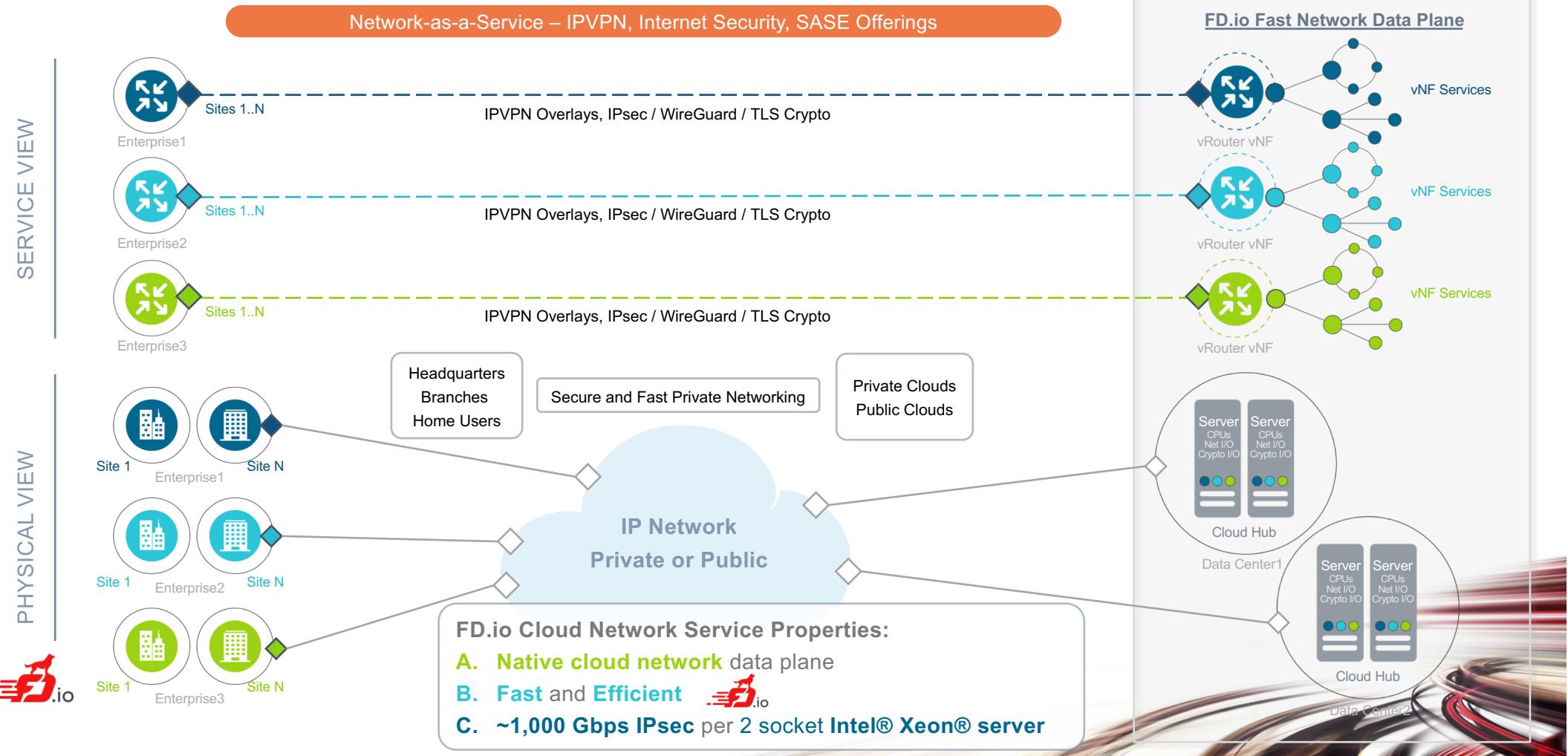
Substantial Performance and Efficiency Gains vs. Alternatives



Fast Cloud Network Services

With Terabit Secure Network Data Plane

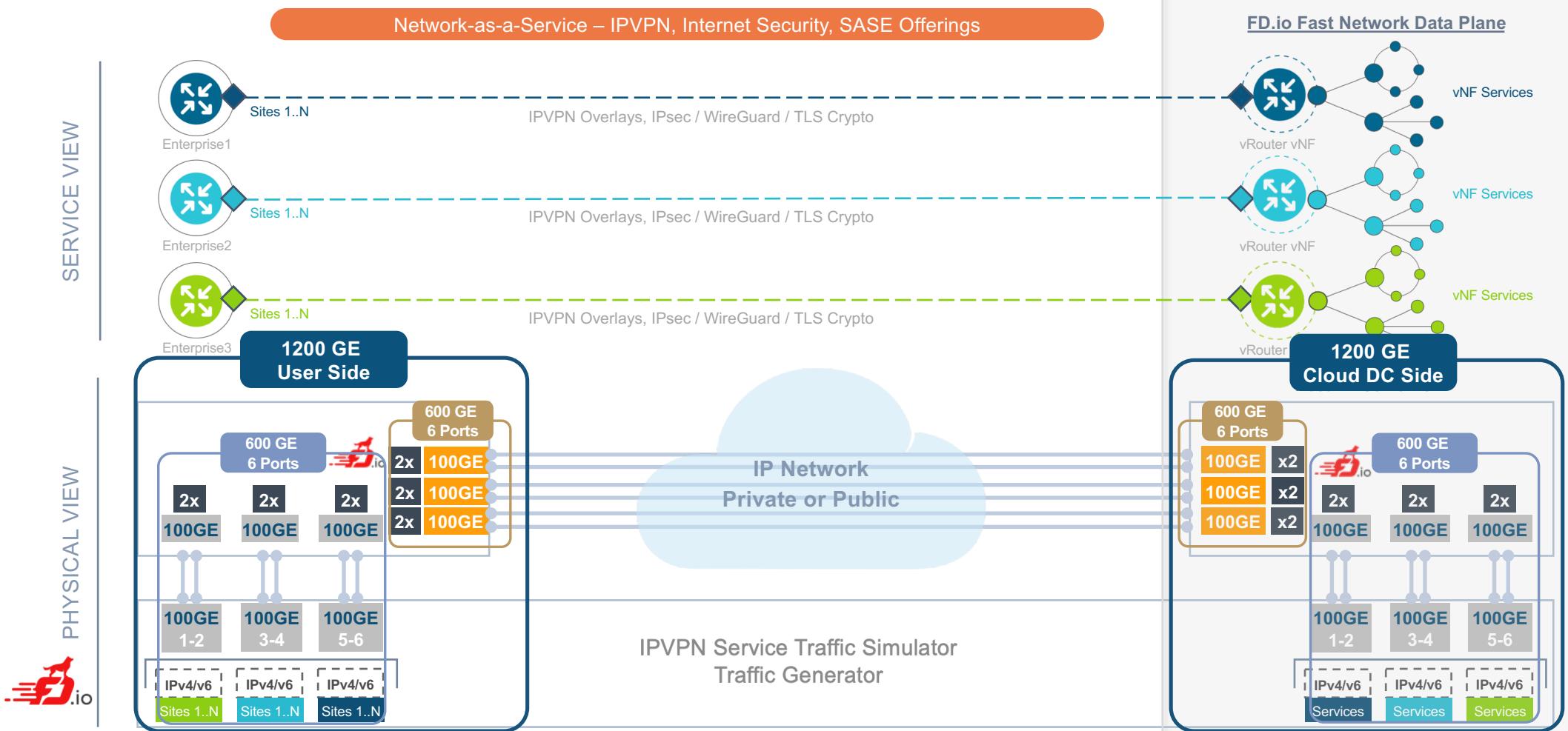
FD.io
Intel® Xeon® = Terabit Naas



Fast Cloud Network Services

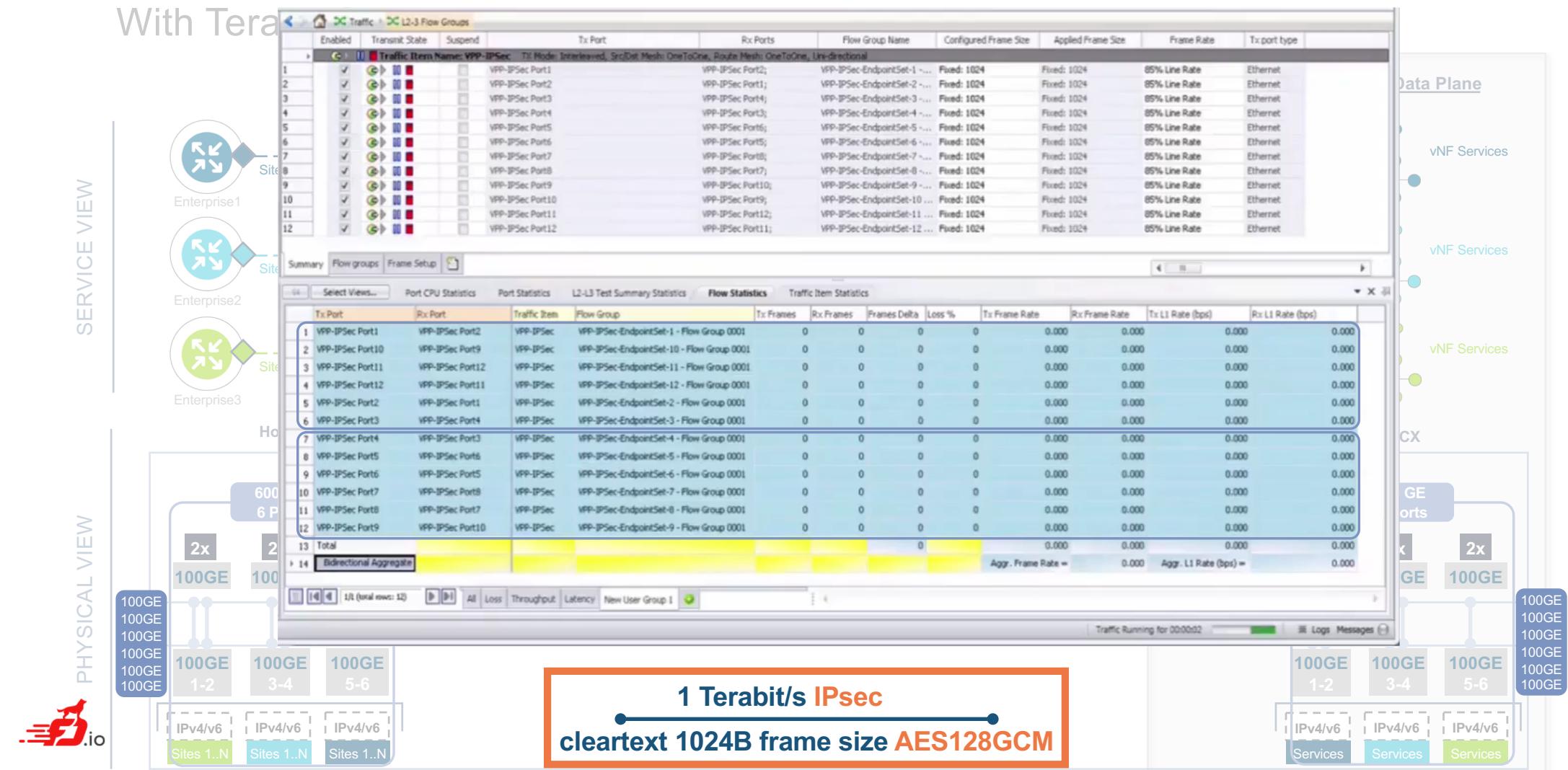
With Terabit Secure Network Data Plane

FD.io = Terabit NaaS



Fast Cloud Network Services - IPsec

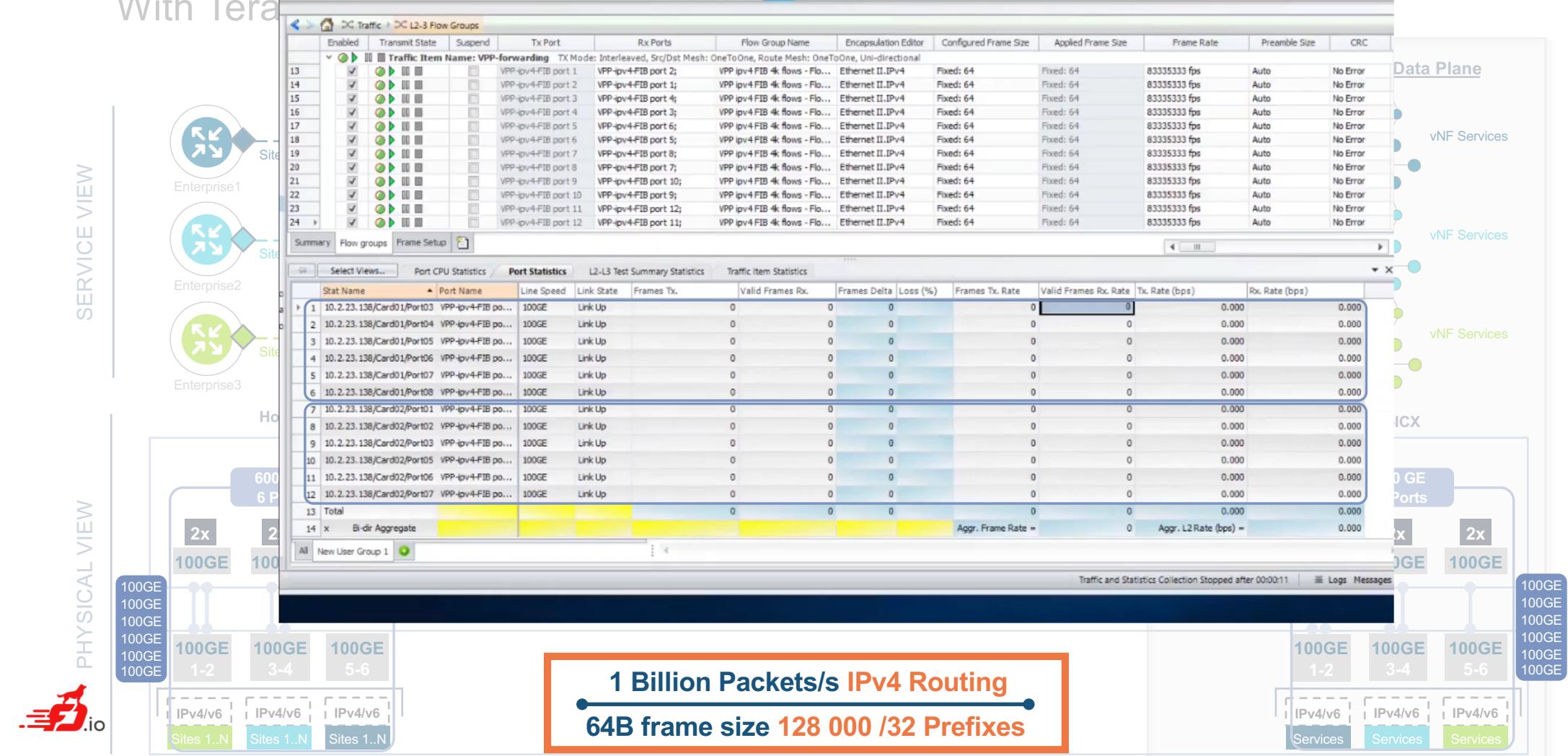
FD.io
Intel® Xeon® = Terabit NaaS



Fast Cloud Network Services – IPv4

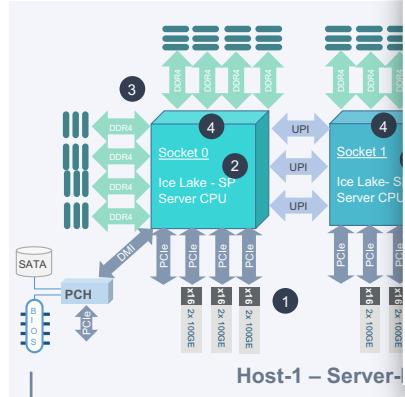
FD.io = Terabit NaaS
Intel® Xeon®

With Tera



FD.io VPP – The “Magic” Behind the Equation

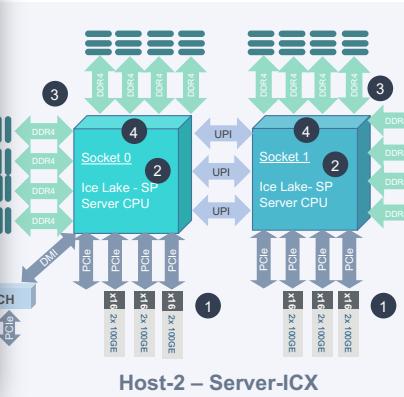
FD.io
Intel® Xeon® = Terabit NaaS



Host-1 – Server

Diagram showing the internal architecture of Host-1. It features two Intel® Xeon® 6252N processors (Ice Lake - SP Server CPU) in a 2-socket configuration. Each socket has 4 cores (labeled 1-4). The system includes DDR4 memory, PCIe slots (PCIe 3.0 x2, PCIe 3.0 x4, PCIe 3.0 x4), UPI links, and a PCH. A SATA drive is connected via the PCH.

IPsec Encryption and Decryption			
FD.io Data Plane Efficiency Metrics: { + } higher is better { - } lower is better	YESTERDAY	TODAY	
	Intel® Xeon® 6252N	Intel® Xeon® 8360Y D stp	Improvement
{ + } 2 Socket forwarding rate [Gbps]	460 Gbps	1000 Gbps	+117 %
{ - } Cycles / Packet	1147	612	-47 %
{ + } Instructions / Cycle	2.89	2.96	+2 %
{ - } Instructions / Packet	3313	1815	-45 %



Host-2 – Server-ICX

Diagram showing the internal architecture of Host-2. It features two Intel® Xeon® 8360Y D stp processors (Ice Lake - SP Server CPU) in a 2-socket configuration. Each socket has 4 cores (labeled 1-4). The system includes DDR4 memory, PCIe slots (PCIe 3.0 x2, PCIe 3.0 x4, PCIe 3.0 x4), UPI links, and a CH (Core Hub) component.

IPv4 Routing			
FD.io Data Plane Efficiency Metrics: { + } higher is better { - } lower is better	YESTERDAY	TODAY	
	Intel® Xeon® 6252N	Intel® Xeon® 6338N	Improvement
{ + } 2 Socket forwarding rate [Gbps]	640 Gbps	1200 Gbps*	+88 %
{ - } Cycles / Packet	139	124	-11 %
{ + } Instructions / Cycle	2.90	3.29	+13 %
{ - } Instructions / Packet	405	409	+1 %

* Measured 2 Socket forwarding rate is limited by PCIe I/O slot layout on tested compute machines; nominal forwarding rate for tested FD.io VPP configuration is 600 Gbps per Processor. Not all cores are used.

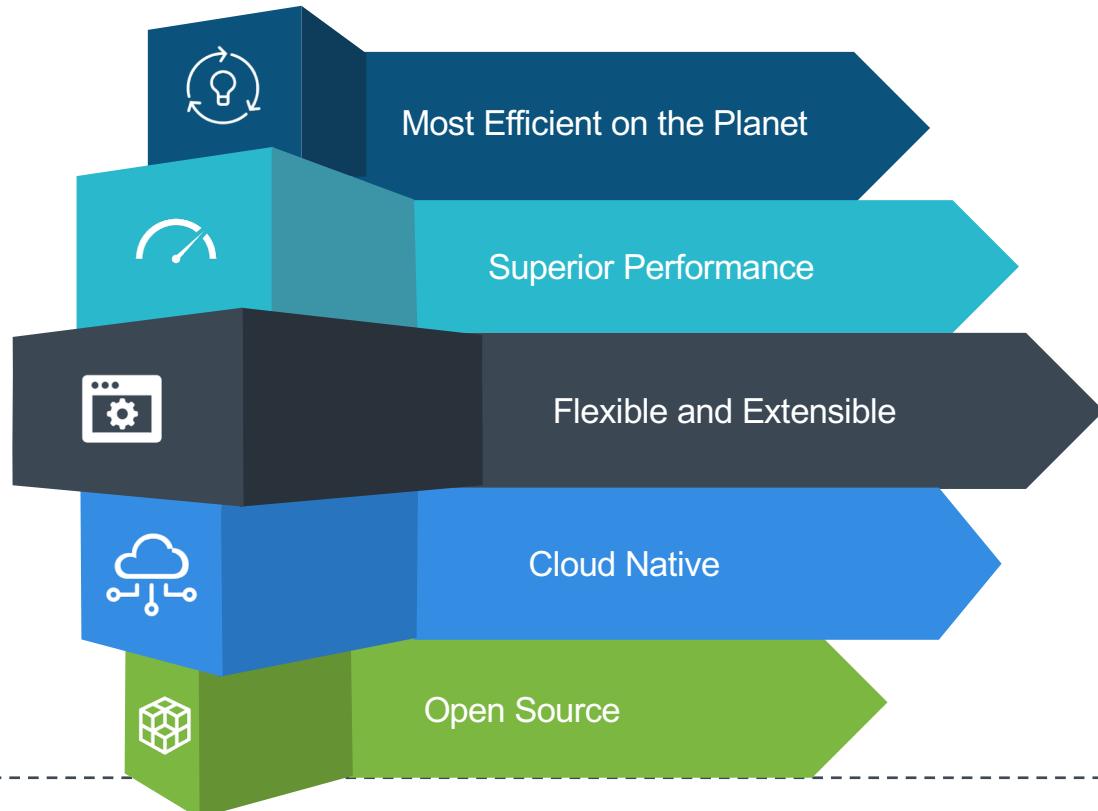
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A Terabit Secure Network Data-Plane

For Native Cloud Network Services

FD.io
Intel® Xeon® = Terabit NaaS



EFFICIENCY

Now even less CPU core cycles per packet: IPsec from 1147 to 612, IPv4 from 139 to 124



PERFORMANCE

Best-in-class performance metrics, Terabit rates with IPsec, Billion packets/sec with IPv4 at Scale



SOFTWARE NETWORKING

Many programmable NF graph nodes, ~20 extension NF plugins, maximizing flexibility



CLOUD NETWORK SERVICES

User-space data plane with optimized SW / HW interface for high density cloud native micro-services



LINUX FOUNDATION

FD.io collaboration activity: 21k+ commits, 500+ contributors, 120+ organizations



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