

| Component | Est. Complexity | Est. Spec. Size (LOC) | Est. Impl. Size (LOC) |
|-------------------|-----------------|-----------------------|-----------------------|
| Core ASM | Medium | 100 | 100 |
| UI I/O | Low | 100 | 100 |
| Instrumentation 1 | Low | 50 | 100 |
| Instrumentation 2 | Low | 50 | 100 |
| Instrumentation 3 | Low | 50 | 100 |
| Instrumentation 4 | Low | 50 | 100 |
| Actuator 1 | Low | 50 | 100 |
| Actuator 2 | Low | 50 | 100 |
| Voting 1 | Medium | 100 | 100 |
| Voting 2 | Medium | 100 | 100 |
| RISC-V CPUs | Complex | 0 | reused |
| Programming I/O | Medium | 0 | reused |
| Debugging I/O | Medium | 0 | reused |
| Total | Medium | 700 | 1000 |