

RTS System

Root

Core Finite
State
Machine

UI I/O

Programming
I/O

Debugging
I/O

Actuation Logic

Voting 1

Actuator 1

Voting 2

Actuator 2

Hardware

FPGA

Sensors

Temperature
Sensor 1

Pressure
Sensor 1

Temperature
Sensor 2

Pressure
Sensor 2

Actuators

Actuator 1

Actuator 2

Instrumentation

Instrumentation 1

Instrumentation 2

Instrumentation 3

Instrumentation 4

Computation

RISC-V
CPU 1

RISC-V
CPU 2

RISC-V
CPU 3