THI Kiến trúc máy tính và hợp ngữ (Thi Chung)

Rắt đầu và	o lúc Monday, 28 May 2018, 1:10 PM
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Thời gian	thực 1 giờ 4 phút hiện
Câu hỏi 1 Hoàn thành Đạt điểm 1,00	Consider the following assembly instruction sequence CMP DL, 0 JB x_label CMP DL, 9 JA a_label ADD DL, 30h JMP x_label a_label: CMP DL, 0Fh JA x_label ADD DL, 31h x_label: MOV AL, DL watch point: Choose correct value of AL register at watch point for different value of DL? DL=55h 85h DL=0FFh 41h DL=10 38h DL=8 0FFh
Câu hỏi 2 Hoàn thành Đạt điểm 1,00	Select correct match for AX (Decimal) at watch points: MOV AX, 1BC MOV CL, 2
	SHL AX, CL
	watch point #1:
	ADD AX, 166
	watch point #2:
	SHR AX, CL
	watch point #3:
	SHR AX, CL
	watch point #1: 1064 V
	watch point #2: 266 V
	watch point #3: 266 V

Câu hỏi 3 Hoàn thành	if the location to which the control is to be transferred lies in a segment other than the current one, then the jump instruction is called
Đạt điểm 0,50	Select one:
	o intrasegment mode
	intersegment mode
	○ intrasegment indirect mode
	intrasegment direct mode
Câu hỏi 4 Hoàn thành	Structural components of computer include:
Đạt điểm 1,00	Select one or more: ☑ System interconnection
	☐ Interrupt
	☑ Central processing unit
	☑ I/O
	☑ Memory
	□ DMA
Câu hỏi 5 Hoàn thành	Which could be correct ones for the destination operand in a data movement instruction?
Đạt điểm 0,50	Select one or more:
	☐ immediate data
	☐ all choices are correct
	☑ register
	✓ memory location
	inemory location
Câu hỏi 6 Hoàn thành	the instruction, JMP C008:2000h is an example of
Đạt điểm 0,50	Select one or more:
	☐ intrasegment mode
	☑ near jump
	☐ intersegment jump
	☑ far jump
Câu hỏi 7	Given a row of memory image in debug
Hoàn thành	0AE8:0120 13 96 D0 E0 00 40 08 42 - 99 80 3E 20 99 00 75 24
Đạt điểm 1,00	SI = 120
,	S1 = 120 The following instruction is executed:
	_
	MOV EAX, [SI+4] Assume the value in EAX is a 32-bit floating-point binary, what is the value of
	EAX in decimal?
	Answer: 4000
	Allower. 4000

Câu hỏi **8** Given a code snippet: Hoàn thành int n = 10; Đạt điểm 1,00 do { n--; } while (n > 0); Which ones are the equivalent logic sequence of instructions in Assembly Select one or more: a_label: loop a_label □ mov cx, 10 a_label: dec cx cmp cx,0 jz a_label a_label: dec cx loop a_label a_label: dec cx cmp cx, 0 jz e_label jmp a_label e_label: Câu hỏi **9** The following sequence of instructions are executed. What is the correct value of AX, CX, DX at watch point? Hoàn thành MOV AX,30 Đạt điểm 1,00 MOV CX,FFFF MUL CX watch point: CX FFFF AXFFD0 DX 002F Câu hỏi **10** Write mask byte (in hex) to set higher 4 bits in a byte value with OR instruction (LSB is the 1st bit). Không trả lời Đạt điểm 0,50 Answer:

Câu hỏi 11 Hoàn thành Đạt điểm 0,50	After executing PUSH EAX instruction, the stack pointer Select one:
Dạt diem 0,50	○ increment by 1
	decrements by 4
	O decrement by 1
	○ increment by 2
Câu hỏi 12	Given an assembly code copying the memory buffer Buff1 to Buff2:
Không trả lời	PUSH DS
Đạt điểm 1,00	POP ES
	LEA SI, Buff1
	LEA DI, Buff2 MOV CX,20
	; Start of block
	cp_loop:
	MOV AL, Byte Ptr [SI]
	MOV Byte Ptr ES:[DI], AL INC SI
	INC DI
	LOOP cp_loop
	;End of block
	Choose equivalent string operations in place of block
	Select one or more:
	□ CLD
	cp_loop:
	MOVSB LOOP cp_loop
	□ STD
	cp_loop:
	MOVSB
	LOOP cp_loop
	CLD
	cp_loop: REP MOVSB
	LOOP cp_loop
	□ CLD
	REP MOVSB
Câu hỏi 13	the instruction that is used as prefix to an instruction to execute it repeatedly
Hoàn thành	until the CX register becomes zero is
Đạt điểm 0,50	Select one:
	○ CMPS
	O SCAS
	O CMPS
Câu hỏi 14	Write mask byte (in hex) to clear all the lower 7 bits of a byte value with AND
Hoàn thành	instruction.
Đạt điểm 0,50	Appulari AND AL 01111111D
	Answer: AND AL, 01111111B

Answer: Given a row of memory image in debug 072C:FFF0 00 00 00 01 00 00 2C 07 - 07 01 2C 07 17 72 00 00 SS=072C, SP=FFF8, DS = 072C Assume the stack now stores two (2) 16-bit parameters and one (1) 16-bit return address in following order: stack top (return address) >> parameter #1 >> parameter #2. The following sequence of instructions are executed. What is the correct values at watch points? MOV BP, SP watch point #1 (BP): MOV AX, [BP+2] watch point #2 (AX): ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: watch point #1: watch point #2: watch point #2: watch point #3: The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		Convert -89.2345 to IEEE 32-bit floating point format (1 sign+ 8 exponent + 23 mantissa) in hex
Answer: Cau hoi 16 Chông trá lời O72C:FFF0 00 00 00 01 00 00 2C 07 - 07 01 2C 07 17 72 00 00 SS=072C, SP=FFR, DS = 072C Assume the stack now stores two (2) 16-bit parameters and one (1) 16-bit return address in following order: stack top (return address) >> parameter #1 >> parameter #2. The following sequence of instructions are executed. What is the correct values at watch points? MOV BP, SP Watch point #1 (BP): MOV AX, [BP+2] Watch point #2 (AX): ADD AX, [BP+4] Watch point #3 (AX): MOV DI, 120 MOV [DI], AX Watch point Chon Y Watch point #3: Chon Y Chon Y	=	munussu) in nex
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SS=072C, SP=FFFR, DS = 072C Assume the stack now stores two (2) 16-bit parameters and one (1) 16-bit return address in following order: stack top (return address) >> parameter #1 >> parameter #2. The following sequence of instructions are executed. What is the correct values at watch points? MOV BP, SP watch point #1 (BP): MOV AX, [BP+2] watch point #2 (AX): ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: watch point #2: watch point #3: Cau hoi 17 Hoàn thành Opt diểm 0,50 Select one: DEC SUB SBB	Câu hỏi 16	Given a row of memory image in debug
Assume the stack now stores two (2) 16-bit parameters and one (1) 16-bit return address in following order: stack top (return address) >> parameter #1 >> parameter #2. The following sequence of instructions are executed. What is the correct values at watch points? MOV BP, SP watch point #1 (BP): MOV AX, [BP+2] watch point #2 (AX): ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: Sale hôi 17 toàn thành Dat diếm 0,50 The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB	Không trả lời	072C:FFF0 00 00 00 01 00 00 2C 07 - 07 01 2C 07 17 72 00 00
return address in following order: stack top (return address) >> parameter #1 >> parameter #2. The following sequence of instructions are executed. What is the correct values at watch points? MOV BP, SP watch point #1 (BP): MOV AX, [BP+2] watch point #2 (AX): ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: watch point #1: watch point #2: watch point #3: Chọn > The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB	Đạt điểm 1,50	SS=072C, SP=FFF8, DS = 072C
at watch points? MOV BP, SP watch point #1 (BP): MOV AX, [BP+2] watch point #2 (AX): ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: watch point #1: watch point #2: watch point #3: Chọn #3: The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		return address in following order: stack top (return address) >> parameter #1
watch point #1 (BP): MOV AX, [BP+2] watch point #2 (AX): ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: watch point #2: watch point #2: watch point #3: Chọn The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		
MOV AX, [BP+2] watch point #2 (AX): ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: watch point Chon #1: watch point #2: watch point #3: Câu hỏi 17 Hoàn thành Dạt diểm 0,50 The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		MOV BP, SP
watch point #2 (AX): ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point Chon		watch point #1 (BP):
ADD AX, [BP+4] watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: watch point #2: watch point #3: Chọn > Câu hỏi 17 Hoàn thành Dạt điểm 0,50 The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		MOV AX, [BP+2]
watch point #3 (AX): MOV DI, 120 MOV [DI], AX watch point #1: watch point #2: watch point #3: Chọn #3: The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		watch point #2 (AX):
MOV DI, 120 MOV [DI], AX watch point #1: watch point #2: watch point #2: watch point #3: Chọn #3: The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		ADD AX, [BP+4]
MOV [DI], AX watch point #1: watch point #2: watch point #3: Chọn #3: The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		watch point #3 (AX):
watch point #1: watch point Chọn #2: watch point #3: Câu hỏi 17 Hoàn thành Dạt điểm 0,50 The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		MOV DI, 120
#1: watch point #2: watch point #3: Chọn Chọn The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		MOV [DI], AX
#2: watch point #3: Chọn The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		· Chon v
#3: The instruction that subtracts 1 from the contents of the specified register/memory location is Select one: DEC SUB SBB		· Chon V
register/memory location is Select one: Det diem 0,50 Select one: Sub Sub Sub Sub Sub		CHOILL
register/memory location is Select one: Det diem 0,50 Select one: Sub Sub Sub Sub Sub	Câu hỏi 17	The instruction that subtracts 1 from the contents of the specified
Select one: © DEC SUB SBB	Hoàn thành	The state of the s
DECSUBSBB	Đạt điểm 0,50	Soloct analy
○ SUB ○ SBB		
○ SBB		
		○ INC

Câu hỏi 18	Memory dump at 1D20:0200 shown as below:
Không trả lời	1D20:0200 00 20 10 5D 55 47 00 90 - 00 10 20 30 40 50 60 70
Đạt điểm 1,00	Given value of registers:
	DS = 1D20, ES = 1D20, DI = 20A
	The following sequence of instructions are executed:
	MOV SI,208h
	MOV AX,0040h
	MOV CX,000Ah
	CLD
	REPNZ SCASB
	watch point:
	What is the correct value of AX, SI, DI registers at watch point?
	DI Chọn 🗸
	AX Chọn 🗸
	SI Chọn ∨
Câu hỏi 19	What is the growing of Anadabilla law is an angle of the state of the
doàn thành	What is the meaning of Amdahl's law in processor performance evaluation?
Dạt điểm 1,00	Select one:
Dật tiếm 1,00	the cost reduce when moving from single-core to multicore processor
	the maximum speedup of a multicore processor
	O the potential speedup of a program using multiple processor compared to
	a single processor
	the speedup of a multicore processor when increasing system bus speed
	1
Câu hỏi 20 Hoàn thành	Which are the correct actions for LODSW string operation if DF is reset (=0)
Đạt điểm 0,50	Select one or more:
oạt diem 0,50	☐ decrease DI by 2
	☐ Load 16-bit value at memory location pointed by ES:[DI] into AX
	☑ increase SI by 2
	☑ Load 16-bit value at memory location pointed by DS:[SI] into AX
Câu hỏi 21	When many devices of different transmission speed connect to the same bus,
Không trả lời	the overall system performance suffers. How did the design engineers resolved this:
Đạt điểm 1,00	uns.
	Select one:
	O PCI Express bus
	 Multiple-Bus hierarchies
	○ PCI bus
	Split system bus into local bus and memory bus

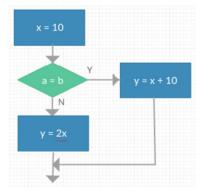
Câu hỏi 22 Hoàn thành	the instruction, CMP to compare source and destination operands by
Đạt điểm 0,50	Select one:
	o adding
	○ comparing
	○ dividing
	subtracting
Câu hỏi 23	To balance the super speed of CPU with the slow response of memory, which
Hoàn thành	of the following measures have been made by engineers in system design?
Đạt điểm 1,00	Select one or more:
	☐ Make use of both on-chip and off-chip cache memory
	☑ Make wider data bus path
	Using higher-speed bus and us hierarchy
	☑ To move data directly by DMA
Câu hỏi 24	The following acquence of instructions are executed. What is the courset
Hoàn thành	The following sequence of instructions are executed. What is the correct value of AX, DX at watch point?
Đat điểm 1,00	MOV DL,FF
24c diciii 1/00	MOV AL,42
	IMUL DL
	watch point:
	AX = FFBE Y
	DX 0000 V
Câu hỏi 25	In the DCD instruction, the contents of the destination or according to a
Hoàn thành	In the RCR instruction, the contents of the destination operand undergoes function as
Đạt điểm 0,50	Select one:
, ,	carry flag is pushed into LSB then MSB is pushed into carry flag
	overflow flag is pushed into MSB then LSB is pushed into carry flag
	overflow flag is pushed into MSB then LSB is pushed into carry flagcarry flag is pushed into MSB then LSB is pushed into carry flag
Câu hỏi 26	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag
Câu hỏi 26 Hoàn thành	carry flag is pushed into MSB then LSB is pushed into carry flag
Hoàn thành	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more:
	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more: immediate data
Hoàn thành	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more:
Hoàn thành	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more: immediate data
Hoàn thành	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more: immediate data memory location
Hoàn thành Đạt điểm 0,50	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more: immediate data memory location indirect data register
Hoàn thành	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more: immediate data memory location indirect data register Convert the 32-bit floating point number A3358000 (in hex) to decimal.
Hoàn thành Đạt điểm 0,50 Câu hỏi 27 Hoàn thành	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more: immediate data memory location indirect data register Convert the 32-bit floating point number A3358000 (in hex) to decimal. Note:
Hoàn thành Đạt điểm 0,50 Câu hỏi 27	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more: immediate data memory location indirect data register Convert the 32-bit floating point number A3358000 (in hex) to decimal.
Hoàn thành Đạt điểm 0,50 Câu hỏi 27 Hoàn thành	 carry flag is pushed into MSB then LSB is pushed into carry flag auxiliary flag is pushed into LSB then MSB is pushed into carry flag Which could be correct ones for the source operand in an instruction? Select one or more: immediate data memory location indirect data register Convert the 32-bit floating point number A3358000 (in hex) to decimal. Note: Result with exponent should be written like (e.g): 1.2345678x10^-13

watch p #2: watch p #1: âu hỏi 29 oàn thành at điểm 0,50 What is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p SI 80 AL 80 âu hỏi 31 oàn thành Select to I dec	All - 23 *
watch p watch p watch p #2: watch p #1: au hỏi 29 oàn thành at điểm 0,50 What is Oi: com poin incr com poin incr com poin Si 04: Loo O5: O6: O7: O8: watch p SI 8I AL 8I = au hỏi 31 Select t are in h at điểm 1,00 Step 1:	point #1: ADD AH, 10 point #2: $AH = 25 \lor$ point $AL = 6C \lor$ are the correct actions for SCASW string operation if DF is set (=1)
watch p #2: watch p #1: au hoi 29 oàn thành at diểm 0,50 which a Select c com poin incr com poin 02: 03: 04: Loo 05: 06: 07: 08: watch p SI 84 AL 81 au hoi 31 oàn thành at diểm 1,00 Step 1:	ADD AH, 10 point #2: AH = 25 \times point AL = 6C \times are the correct actions for SCASW string operation if DF is set (=1)
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□ com poin Câu hỏi 30 Hoàn thành Dạt điểm 1,00 What is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p SI 8! AL 8! Eâu hỏi 31 Hoàn thành Dạt điểm 1,00 Step 1:	ted by ES:[DI] and set/clear flag bits accordingly
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Câu hỏi 30 Hoàn thành Dạt điểm 1,00 What is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p SI 8(AL 8(= Câu hỏi 31 Hoàn thành Dạt điểm 1,00 Step 1:	pare the value in AX register with 16-bit value at the memory location ted by DS:[SI] and set/clear flag bits accordingly
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01: 02: 03: 04: Loo 05: 06: 07: 08: watch p SI 8I AL 8I atau hòi 31 shành bat diểm 1,00 Step 1:	
02: 03: 04: Loo 05: 06: 07: 08: watch p SI 80 AL 80 Eâu hỏi 31 Ioàn thành Oạt điểm 1,00 Step 1:	the correct value of SI, AL (in hex) at watch point:
03: 04: Loo 05: 06: 07: 08: watch p SI 88 AL = 10àn hỏi 31 shoàn thành 0at điểm 1,00 Step 1:	MOV AL 10h
04: Loo 05: 06: 07: 08: watch p SI 88 AL = 10àn hỏi 31 Hoàn thành Oạt điểm 1,00 Step 1:	MOV AL, 10h MOV CX, 7
06: 07: 08: watch p SI 80 AL 80 = Câu hỏi 31 Hoàn thành Dạt điểm 1,00 Step 1:	p_label:
07: 08: watch p SI 80 AL 81 = Câu hỏi 31 Hoàn thành Dạt điểm 1,00 Step 1:	MOV [SI], AL
watch p SI 8I AL 8I = Câu hỏi 31 Hoàn thành Dạt điểm 1,00 Step 1:	ADD AL,10h INC SI
SI 80 AL 80 = Select t are in h Opt diểm 1,00 Step 1:	LOOP Loop_label
AL = 80 Câu hỏi 31 Hoàn thành Dạt điểm 1,00 Step 1:	oint:
AL 80 Câu hỏi 31 Hoàn thành Dạt điểm 1,00 Step 1:	
= Select t are in h	oh v
= Select t are in h	26
doàn thành are in h	<u>ш</u> ,
doàn thành are in h	
doàn thành are in h	he correct convenes of instructions to compute 1034/139 (all velve-
Dạt điểm 1,00 Step 1:	he correct sequence of instructions to compute -1024/128 (all values ex).
Step 1:	
Step 2:	CWD
Step 2:	
	1004 04 00
Step 3:	MOV CX,80 V
Step 4:	MOV CL,80 V
	MOV CL,80 V
	MOV CL,80 V

âu hỏi 32	Select correct match for AL and carry flag at watch point #1:	
oàn thành	MOV BL, 8C	
ạt điểm 1,00	MOV AL, 7E	
	ADD AL, BL	
	watch point #1:	
	AL OA ~	
	Carry set v	
	flag	

Câu hỏi **33** Hoàn thành Đạt điểm 1,00

Given a flowchart of an algorithm:



Select the correct instruction sequence:

Select one or more:

- mov dl,10
 cmp al,bl
 jnz n_label
 add dl,10
 jmp e_label
 n_label:
 - n_label: mov cl,1 shl dl,cl
- e_label: mov dh,dl
- mov dl,10
 cmp al,bl
 jnz n_label
 add dl,10
 mov dh,dl
 jmp e_label
- n_label: mov cl,1 shl dl,cl
- e_label: mov dh,dl
- mov dl,10
 cmp al,bl
 jnz n_label
 add dl,10
 jmp e_label
 n_label:
 - mov cl,1 shr dl,cl e_label:
- mov dh,dl

 mov dl,10
- ☐ mov dl,10 cmp al,bl jz n_label mov cl,1 shl dl,cl jmp e_label n_label:

add dl,10 e_label: mov dh,dl

Hoàn thành Đạt điểm 0,50 Câu hỏi 36	Select one: decrements by 4 decrements by 2 increments by 4 increment by 1 Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex Answer: 191
Câu hỏi 35 Hoàn thành Đạt điểm 0,50 Câu hỏi 36 Hoàn thành	 decrements by 2 increments by 4 increment by 1 Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex
Hoàn thành Đạt điểm 0,50 Câu hỏi 36	 increments by 4 increment by 1 Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex
Hoàn thành Đạt điểm 0,50 Câu hỏi 36	increment by 1 Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex
Hoàn thành Đạt điểm 0,50 Câu hỏi 36	Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex
Hoàn thành Đạt điểm 0,50 Câu hỏi 36	
Hoàn thành Đạt điểm 0,50 Câu hỏi 36	
Đạt điểm 0,50 Câu hỏi 36	Answer: 191
Câu hỏi 36	Answer: 191
	Which of the following instructions are not valid?
	which of the following instructions are not value:
Đạt điểm 0,50	Select one or more:
Dực diem 0/30	☑ MOV DS, B800h
	☐ MOV AX, [BP+2]
	☑ MOV SP, SS:[SI+2]
	☐ MOV AX, SI
Câu hỏi 37	The following sequence of instructions are executed. What is the correct
Hoàn thành	value of flag bits at watch point?
Đạt điểm 1,00	MOV AL, 0F
	ADD AL, F1
	watch point:
	Zero flag (OF) reset
	= Teset
	Carry flag set
	(CF) =
20	
Câu hỏi 38 Hoàn thành	Major structural components of the CPU include:
	Select one or more:
Đạt điểm 1,00	☑ Registers
	☑ Arithmetic and Logic Unit
	☐ Instruction Pointer (PC)
	☑ Interconnections
	☐ Control Unit
	☐ Instruction Register
Câu hỏi 39	Consider a magnetic disk drive with 8 surfaces, 512 tracks per surface, and 64
Hoàn thành	sectors per track. Sector size is 1 kB. What is the disk capacity
Đạt điểm 1,00	. [
	Answer: 512 KB

Câu hỏi 40 Hoàn thành	What best describe the Spatial and Temporal Locality?
Đạt điểm 1,00	Tempor al be exploited by keeping recently used instruction and data in cache memory and by exploiting a cache hierarchy
	locality
	Spatial be exploited by moving data between cache and memory more efficient
	locality
Câu hỏi 41	Given a code snippet:
Hoàn thành	int ax, bx;
Đạt điểm 1,00	
	if $(ax \ge bx)$
	ax -=bx;
	else
	bx -=ax;
	What is the equivalent logic sequence of instructions in Assembly
	Select one:
	jl a_label
	sub ax,bx jmp x_label
	a_label:
	sub bx,ax x_label:
	<pre>cmp ax,bx jbe a_label</pre>
	sub ax,bx
	jmp x_label
	a_label: sub bx,ax
	x_label:
	○ cmp ax,bx
	ja a_label sub ax,bx
	jmp x_label
	a_label:
	sub bx,ax
	x_label:
	cmp ax,bx jge a_label
	sub ax,bx
	jmp x_label
	a_label: sub bx,ax
	x_label:
Câu hỏi 42	Which of the following is not a data convitance for instruction 2
Hoàn thành	Which of the following is not a data copy/transfer instruction?
Đạt điểm 0,50	Select one or more:
Dạt uleili 0,30	☑ ADC
	□ MOV
	□ LEA
	☑ DAS
	Return to: General ◆