ATTiny85 microcontroller part, SOIC-8.

C1   0.1uF

ATTINY85-20SU

VCC ──── 8 ── VCC   GND ── 4 ──── GND

U1-SUPPLY

R5
VCC ──/\/\/──
10k
RESET ──────●── 1 │ PCINT5/RESET/ADC0/DW
                   │
                   │ MOSI/DI/SDA/AIN0/OC0A/OC1A/AREF/PCINT0 ── 5 ──── PS2_DATA
                   │
                   │ SCK/USCK/SCL/ADC1/T0/INT0/PCINT2 ── 7 ──── PS2_CLK
                   │
                   │ MISO/DO/AIN1/OC0B/OC1A/PCINT1 ── 6 ──── MISO
                   │
AMIGA_KB_CLK ── 2 │ ADC3/OC1B/CLK1/XTAL1/PCINT3
                   │
AMIGA_KB_DATA ── 3 │ ADC2/OC1B/CLK0/XTAL2/PCINT4

U1

ATTINY85-20SU
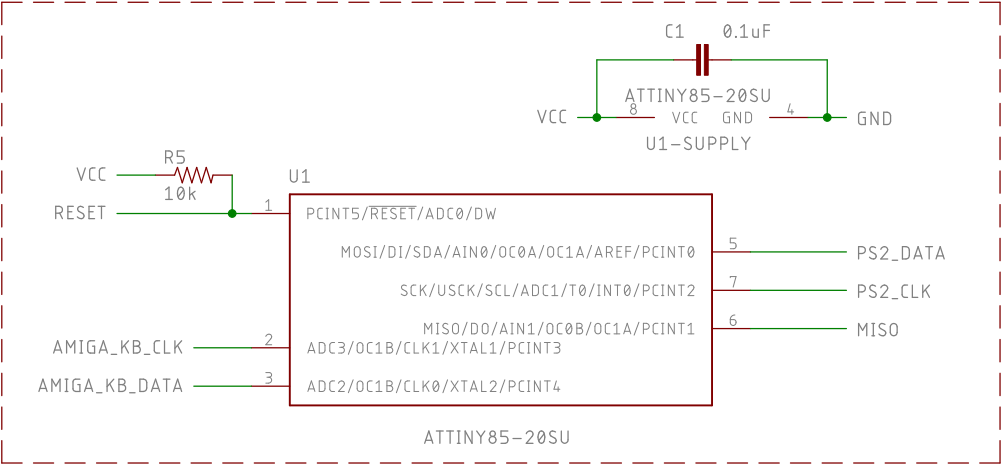
Series resistors to keyboard lines

R1
PS2_DATA ──/\/\/── PS2_DATA_CON
22ohm
R2
PS2_CLK ──/\/\/── PS2_CLK_CON
22ohm
R3
AMIGA_KB_DATA ──/\/\/── AMIGA_KB_DATA_CON
22ohm
R4
AMIGA_KB_CLK ──/\/\/── AMIGA_KB_CLK_CON
22ohm
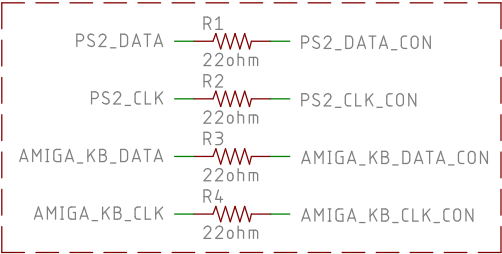
ISP programming header, bottom side of the board

ISP

MISO ──── MISO │ 1 ○ ○ 2 │ +5 ──── VCC
PS2_CLK ── SCK │ 3 ○ ○ 4 │ MOSI ── PS2_DATA
RESET ── RESET │ 5 ○ ○ 6 │ GND ──── GND

AVR_SPI_PRG_6NS
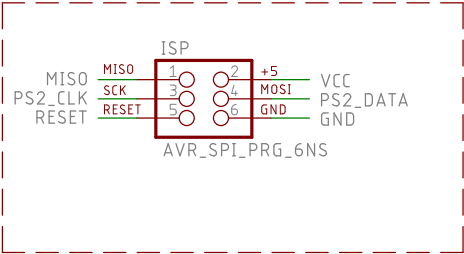
Header for keyboard(s) connections

JP2

VCC ──────── 1 ○
PS2_DATA_CON ── 2 ○
PS2_CLK_CON ── 3 ○
GND ──────── 4 ○
VCC ──────── 5 ○
AMIGA_KB_DATA_CON ── 6 ○
AMIGA_KB_CLK_CON ── 7 ○
GND ──────── 8 ○