

: count 10 0 do i 2 +loop ; count

step: 0

[entrypoint]		Data:	Return:
97	CALL 84		
98	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 1

count		Data:	Return:
84	CONST 10		98
85	CONST 0		
86	SWAP		
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 2

count		Data:	Return:
84	CONST 10	10	98
85	CONST 0		
86	SWAP		
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 3

count		Data:	Return:
84	CONST 10	0	98
85	CONST 0	10	
86	SWAP		
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 4

count		Data:	Return:
84	CONST 10	10	98
85	CONST 0	0	
86	SWAP		
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 5

count		Data:	Return:
84	CONST 10	0	10
85	CONST 0		98
86	SWAP		
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 6

count		Data:	Return:
84	CONST 10		0
85	CONST 0		10
86	SWAP		98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 7

	i
43	RSTK 1
44	RET

Data:

Return:

90
0
10
98


```
: count 10 0 do i 2 +loop ; count
```

step: 8

i	
43	RSTK 1
44	RET

Data:
0

Return:
90
0
10
98

: count 10 0 do i 2 +loop ; count

step: 9

count		Data:	Return:
84	CONST 10	0	0
85	CONST 0		10
86	SWAP		98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 10

count		Data:	Return:
84	CONST 10	2	0
85	CONST 0	0	10
86	SWAP		98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 11

[+loop]		Data:	Return:
72	RTS	2	92
73	SWAP	0	0
74	RTS		10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 12

[+loop]		Data:	Return:
72	RTS	92	0
73	SWAP	2	10
74	RTS	0	98
75	ADD		
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 13

[+loop]		Data:	Return:
72	RTS	2	0
73	SWAP	92	10
74	RTS	0	98
75	ADD		
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 14

[+loop]		Data:	Return:
72	RTS	0	10
73	SWAP	2	98
74	RTS	92	
75	ADD	0	
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 15

[+loop]		Data:	Return:
72	RTS	2	10
73	SWAP	92	98
74	RTS	0	
75	ADD		
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 16

[+loop]		Data:	Return:
72	RTS	92	2
73	SWAP	0	10
74	RTS		98
75	ADD		
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 17

[+loop]		Data:	Return:
72	RTS	0	92
73	SWAP		2
74	RTS		10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 18

[+loop]		Data:	Return:
72	RTS	10	92
73	SWAP	0	2
74	RTS		10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 19

[+loop]		Data:	Return:
72	RTS	2	92
73	SWAP	10	2
74	RTS	0	10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 20

[+loop]		Data:	Return:
72	RTS	-1	92
73	SWAP	0	2
74	RTS		10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 21

[+loop]		Data:	Return:
72	RTS	0	92
73	SWAP	0	2
74	RTS		10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 22

count		Data:	Return:
84	CONST 10	0	2
85	CONST 0	0	10
86	SWAP		98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 23

count		Data:	Return:
84	CONST 10	0	2
85	CONST 0		10
86	SWAP		98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 24

i	
43	RSTK 1
44	RET

Data:

0

Return:

90
2
10
98

```
: count 10 0 do i 2 +loop ; count
```

```
step: 25
```

The diagram shows a stack frame with two entries:

- Address 43: RSTK 1
- Address 44: RET

Data:

2
0

Return:

90
2
10
98

: count 10 0 do i 2 +loop ; count

step: 26

count		Data:	Return:
84	CONST 10	2	2
85	CONST 0	0	10
86	SWAP		98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 27

count		Data:	Return:
84	CONST 10	2	2
85	CONST 0	2	10
86	SWAP	0	98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 28

[+loop]		Data:	Return:
72	RTS	2	92
73	SWAP	2	2
74	RTS	0	10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 29

[+loop]		Data:	Return:
72	RTS	92	2
73	SWAP	2	10
74	RTS	2	98
75	ADD	0	
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 30

[+loop]		Data:	Return:
72	RTS	2	2
73	SWAP	92	10
74	RTS	2	98
75	ADD	0	
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 31

[+loop]		Data:	Return:
72	RTS	2	10
73	SWAP	2	98
74	RTS	92	
75	ADD	2	
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		


```
: count 10 0 do i 2 +loop ; count
```

step: 32

[+loop]		Data:	Return:
72	RTS	4	10
73	SWAP	92	98
74	RTS	2	
75	ADD	0	
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 33

[+loop]		Data:	Return:
72	RTS	92	4
73	SWAP	2	10
74	RTS	0	98
75	ADD		
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 34

[+loop]		Data:	Return:
72	RTS	2	92
73	SWAP	0	4
74	RTS		10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 35

[+loop]		Data:	Return:
72	RTS	10	92
73	SWAP	2	4
74	RTS	0	10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 36

[+loop]		Data:	Return:
72	RTS	4	92
73	SWAP	10	4
74	RTS	2	10
75	ADD	0	98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 37

[+loop]		Data:	Return:
72	RTS	-1	92
73	SWAP	2	4
74	RTS	0	10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 38

[+loop]		Data:	Return:
72	RTS	0	92
73	SWAP	2	4
74	RTS	0	10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 39

count		Data:	Return:
84	CONST 10	0	4
85	CONST 0	2	10
86	SWAP	0	98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 40

count		Data:	Return:
84	CONST 10	2	4
85	CONST 0	0	10
86	SWAP		98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 41

i	
43	RSTK 1
44	RET

Data:

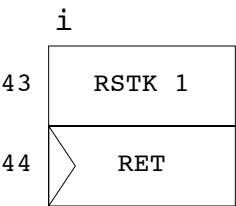
2
0

Return:

90
4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 42



Data:

4
2
0

Return:

90
4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 43

count		Data:	Return:
84	CONST 10	4	4
85	CONST 0	2	10
86	SWAP	0	98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 44

count		Data:	Return:
84	CONST 10	2	4
85	CONST 0	4	10
86	SWAP	2	98
87	STR	0	
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 45

[+loop]		Data:	Return:
72	RTS	2	92
73	SWAP	4	4
74	RTS	2	10
75	ADD	0	98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 46

[+loop]		Data:	Return:
72	RTS	92	4
73	SWAP	2	10
74	RTS	4	98
75	ADD	2	
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 47

[+loop]		Data:	Return:
72	RTS	2	4
73	SWAP	92	10
74	RTS	4	98
75	ADD	2	
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 48

[+loop]		Data:	Return:
72	RTS	4	10
73	SWAP	2	98
74	RTS	92	
75	ADD	4	
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 49

[+loop]		Data:	Return:
72	RTS	6	10
73	SWAP	92	98
74	RTS	4	
75	ADD	2	
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 50

[+loop]		Data:	Return:
72	RTS	92	6
73	SWAP	4	10
74	RTS	2	98
75	ADD	0	
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 51

[+loop]		Data:	Return:
72	RTS	4	92
73	SWAP	2	6
74	RTS	0	10
75	ADD		98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 52

[+loop]		Data:	Return:
72	RTS	10	92
73	SWAP	4	6
74	RTS	2	10
75	ADD	0	98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 53

[+loop]		Data:	Return:
72	RTS	6	92
73	SWAP	10	6
74	RTS	4	10
75	ADD	2	98
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 54

[+loop]		Data:	Return:
72	RTS	-1	92
73	SWAP	4	6
74	RTS	2	10
75	ADD	0	98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 55

[+loop]		Data:	Return:
72	RTS	0	92
73	SWAP	4	6
74	RTS	2	10
75	ADD	0	98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 56

count		Data:	Return:
84	CONST 10	0	6
85	CONST 0	4	10
86	SWAP	2	98
87	STR	0	
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 57

count		Data:	Return:
84	CONST 10	4	6
85	CONST 0	2	10
86	SWAP	0	98
87	STR		
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 58

i

43	RSTK 1
44	RET

Data:

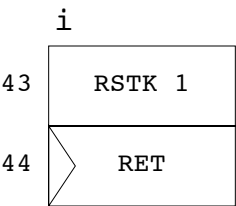
4
2
0

Return:

90
6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 59



Data:

6
4
2
0

Return:

90
6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 60

count		Data:	Return:
84	CONST 10	6	6
85	CONST 0	4	10
86	SWAP	2	98
87	STR	0	
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 61

count		Data:	Return:
84	CONST 10	2	6
85	CONST 0	6	10
86	SWAP	4	98
87	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 62

[+loop]		Data:	Return:
72	RTS	2	92
73	SWAP	6	6
74	RTS	4	10
75	ADD	2	98
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 63

[+loop]		Data:	Return:
72	RTS	92	6
73	SWAP	2	10
74	RTS	6	98
75	ADD	4	
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 64

[+loop]		Data:	Return:
72	RTS	2	6
73	SWAP	92	10
74	RTS	6	98
75	ADD	4	
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 65

[+loop]		Data:	Return:
72	RTS	6	10
73	SWAP	2	98
74	RTS	92	
75	ADD	6	
76	STR	4	
77	STR	2	
78	RSTK 2	0	
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 66

[+loop]		Data:	Return:
72	RTS	8	10
73	SWAP	92	98
74	RTS	6	
75	ADD	4	
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 67

[+loop]		Data:	Return:
72	RTS	92	8
73	SWAP	6	10
74	RTS	4	98
75	ADD	2	
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 68

[+loop]		Data:	Return:
72	RTS	6	92
73	SWAP	4	8
74	RTS	2	10
75	ADD	0	98
76	STR		
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 69

[+loop]		Data:	Return:
72	RTS	10	92
73	SWAP	6	8
74	RTS	4	10
75	ADD	2	98
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 70

[+loop]		Data:	Return:
72	RTS	8	92
73	SWAP	10	8
74	RTS	6	10
75	ADD	4	98
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 71

[+loop]		Data:	Return:
72	RTS	-1	92
73	SWAP	6	8
74	RTS	4	10
75	ADD	2	98
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		


```
: count 10 0 do i 2 +loop ; count
```

step: 72

[+loop]		Data:	Return:
72	RTS	0	92
73	SWAP	6	8
74	RTS	4	10
75	ADD	2	98
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 73

count		Data:	Return:
84	CONST 10	0	8
85	CONST 0	6	10
86	SWAP	4	98
87	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 74

count		Data:	Return:
84	CONST 10	6	8
85	CONST 0	4	10
86	SWAP	2	98
87	STR	0	
88	STR		
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 75

i	
43	RSTK 1
44	RET

Data:

6
4
2
0

Return:

90
8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 76

i	
43	RSTK 1
44	RET

Data:
8
6
4
2
0

Return:
90
8
10
98

: count 10 0 do i 2 +loop ; count

step: 77

count		Data:	Return:
84	CONST 10	8	8
85	CONST 0	6	10
86	SWAP	4	98
87	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 78

count		Data:	Return:
84	CONST 10	2	8
85	CONST 0	8	10
86	SWAP	6	98
87	STR	4	
88	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 79

[+loop]		Data:	Return:
72	RTS	2	92
73	SWAP	8	8
74	RTS	6	10
75	ADD	4	98
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		


```
: count 10 0 do i 2 +loop ; count
```

step: 80

[+loop]		Data:	Return:
72	RTS	92	8
73	SWAP	2	10
74	RTS	8	98
75	ADD	6	
76	STR	4	
77	STR	2	
78	RSTK 2	0	
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 81

[+loop]		Data:	Return:
72	RTS	2	8
73	SWAP	92	10
74	RTS	8	98
75	ADD	6	
76	STR	4	
77	STR	2	
78	RSTK 2	0	
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 82

[+loop]		Data:	Return:
72	RTS	8	10
73	SWAP	2	98
74	RTS	92	
75	ADD	8	
76	STR	6	
77	STR	4	
78	RSTK 2	2	
79	RSTK 1	0	
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 83

[+loop]		Data:	Return:
72	RTS	10	10
73	SWAP	92	98
74	RTS	8	
75	ADD	6	
76	STR	4	
77	STR	2	
78	RSTK 2	0	
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

: count 10 0 do i 2 +loop ; count

step: 84

[+loop]		Data:	Return:
72	RTS	92	10
73	SWAP	8	10
74	RTS	6	98
75	ADD	4	
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 85

[+loop]		Data:	Return:
72	RTS	8	92
73	SWAP	6	10
74	RTS	4	10
75	ADD	2	98
76	STR	0	
77	STR		
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 86

[+loop]		Data:	Return:
72	RTS	10	92
73	SWAP	8	10
74	RTS	6	10
75	ADD	4	98
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 87

[+loop]		Data:	Return:
72	RTS	10	92
73	SWAP	10	10
74	RTS	8	10
75	ADD	6	98
76	STR	4	
77	STR	2	
		0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		


```
: count 10 0 do i 2 +loop ; count
```

step: 88

[+loop]		Data:	Return:
72	RTS	0	92
73	SWAP	8	10
74	RTS	6	10
75	ADD	4	98
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 89

[+loop]		Data:	Return:
72	RTS	-1	92
73	SWAP	8	10
74	RTS	6	10
75	ADD	4	98
76	STR	2	
77	STR	0	
78	RSTK 2		
79	RSTK 1		
80	GTR		
81	NOT		
82	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 90

count		Data:	Return:
84	CONST 10	-1	10
85	CONST 0	8	10
86	SWAP	6	98
87	STR	4	
88	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 91

count		Data:	Return:
84	CONST 10	8	10
85	CONST 0	6	10
86	SWAP	4	98
87	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

```
: count 10 0 do i 2 +loop ; count
```

step: 92

count		Data:	Return:
84	CONST 10	8	10
85	CONST 0	6	98
86	SWAP	4	
87	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 93

count		Data:	Return:
84	CONST 10	8	98
85	CONST 0	6	
86	SWAP	4	
87	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 94

[entrypoint]		Data:	Return:	
97	CALL 84	8		
98	RET	6		
		4		
		2		
		0		