

Pengcheng Xu

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Aut inveniam viam aut faciam.
“I’ll either find a way or make one.”—Hannibal

Education

D-INFK, ETH Zürich

DOCTORATE COMPUTER SCIENCE

- Thesis supervisor: Prof. Dr. Timothy Roscoe

Zürich, Switzerland

Sept. 2021 - Jul. 2027

D-INFK, ETH Zürich

COMPUTER SCIENCE MSc

- Part of the Direct Doctorate in Computer Science program
- Thesis supervisor: Prof. Dr. Torsten Hoefler

Zürich, Switzerland

Sept. 2021 - Sept. 2023

School of EECS, Peking University

B.Sc. COMPUTER SCIENCE AND TECHNOLOGY

- “Summa cum laude”; Member of the Turing Class Honor Program
- Thesis supervisor: Prof. Yun Liang

Beijing, China

Sept. 2017 - Jul. 2021

Academic Experiences

Scalable Parallel Computing Lab (SPCL) @ ETH Zürich

MASTER THESIS (WITH PROF. DR. TORSTEN HOEFER)

Zürich, Switzerland

Mar. 2023 - Sept. 2023

NetOS, Systems Group @ ETH Zürich

SEMESTER PROJECT (WITH PROF. DR. TIMOTHY ROSCOE)

Zürich, Switzerland

Oct. 2022 - Feb. 2023

Center for Energy-efficient Computing and Applications (CECA) @ PKU

UNDERGRADUATE RESEARCH (WITH PROF. YUN LIANG)

Beijing, China

Dec. 2017 - Jul. 2021

Parallel Systems Architecture Lab (PARSA) @ EPFL

RESEARCH INTERN (WITH PROF. BABAK FALSAFI)

Lausanne, Switzerland (remote)

Jul. 2020 - Jan. 2021

XG Lab @ Alibaba DAMO Academy

ACADEMIC COLLABORATION (WITH PROF. CHENREN XU & DR. PENGYU ZHANG)

Beijing, China

Sept. 2020 - Jan. 2021

PKU Student Supercomputing Competition Team (PKUSC)

TEAM LEADER

Beijing, China

Nov. 2017 - Nov. 2020

Work Experiences

SenseTime

RESEARCH INTERN

Beijing, China

Jun. 2019 - Dec. 2019

Teaching Experiences

System Programming and Computer Architecture, ETH Zürich

ASSISTENZ (HEAD TA)

Zürich, Switzerland

2024

Publications

Anastasiia Ruzhanskaia, **Pengcheng Xu**, David Cock, Timothy Roscoe. “Rethinking Programmed I/O for Fast Devices, Cheap Cores, and Coherent Interconnects”

ARXIV

Online

Oct. 2024

Timo Schneider, **Pengcheng Xu**, Torsten Hoefler. “FPsPIN: An FPGA-based Open-Hardware Research Platform for Processing in the Network”

ARXIV

Online

May. 2024

Pengcheng Xu. “Full-System Evaluation of the sPIN In-Network-Compute Architecture”

ETH LIBRARY

ETH Zurich

Sept. 2023

Pengcheng Xu. “Enzian Firmware Resource Interface”

ETH LIBRARY

ETH Zurich

Feb. 2023

ZeJia Fan, Yuchen Gu, Zhewen Hao, Yueyang Pan, **Pengcheng Xu**, Yuxuan Yan, Fangyuan Yang, Zhenxin Fu, Yun Liang. “Critique of ‘MemXCT: Memory-Centric X-Ray CT Reconstruction With Massive Parallelization’ by SCC Team From Peking University”

IEEE TRANSACTIONS ON PARALLEL AND DISTRIBUTED SYSTEMS (TPDS)

Journal

Jan. 2022

Qingcheng Xiao, Size Zheng, Bingzhe Wu, **Pengcheng Xu**, Xuehai Qian, Yun Liang. “HASCO: Towards Agile HARDware and Software CO-design for Tensor Computation”

INTERNATIONAL SYMPOSIUM ON COMPUTER ARCHITECTURE (ISCA)

Worldwide

June 2021

Yihua Cheng, ZeJia Fan, Jing Mai, Yifan Wu, **Pengcheng Xu**, Yuxuan Yan, Zhenxin Fu, Yun Liang. “Critique of ‘Planetary Normal Mode Computation: Parallel Algorithms, Performance, and Reproducibility’ by SCC Team From Peking University”

IEEE TRANSACTIONS ON PARALLEL AND DISTRIBUTED SYSTEMS (TPDS)

Journal

Jan. 2021

Pengcheng Xu, Yun Liang. “Automatic Code Generation for Rocket Chip RoCC Accelerators”

FOURTH WORKSHOP ON COMPUTER ARCHITECTURE RESEARCH WITH RISC-V (CARRV 2020), CO-LOCATED WITH ISCA 2020

Virtual Workshop

May 2020

Honors & Awards

2020	Second Place , Virtual Student Cluster Competition at SC'20 <ul style="list-style-type: none"> • Worked as leader in charge of cloud cluster management and the mystery task • Team ranked top on the CESM (Community Earth System Model) application 	Global Event
2019	First Prize , ASC Student Supercomputing Challenge 2019 <ul style="list-style-type: none"> • Worked as leader in charge of system install and administration, benchmarks, logistics, and the mystery task 	Dalian, China
2019	SenseTime Scholarship 2019	Beijing, China
2018	Award for Scientific Research , Peking University	Beijing, China
2018	Prize of Excellence , IBM OpenPOWER/CAPI and OpenCAPI Heterogeneous Computing Design Contest <ul style="list-style-type: none"> • Worked to build an FPGA accelerator for <i>BCrypt</i> (widely-used hashing algorithm) on Xilinx UltraScale+ FPGAs • Developed on the OpenCAPI FPGA-host platform for high-performance, cloud-oriented acceleration 	Beijing, China
2018	Second Prize , Peking University Collegiate Programming Contest	Beijing, China
2018	Accepted & Passed , Google Summer of Code 2018 with Gentoo Foundation <ul style="list-style-type: none"> • Worked to develop solution to <i>modularize the Android system upgrade with Portage</i> • Enabled utilization of mature Unix technologies in mobile computing 	Global Event