



Initial Code – LG4525B for LGD 2.0" TN Panel

* Condition

1. VCI = VCC = IOVCC = 2.8V

Initialization		
Reg (Hex)	Data (Hex)	Delay (ms)
0X14	0X0030	
0X0F	0X0008	
0X11	0X0231	
0X12	0X000A	
0X13	0X145D	
0X10	0X5268	
DELAY		40
0X12	0X001A	
DELAY		40
0X10	0X5260	
0X13	0X345D	
DELAY		40
0X30	0X0000	
0X31	0X0705	
0X32	0X0706	
0X33	0X0102	
0X34	0X0204	
0X35	0X0000	
0X36	0X0104	
0X37	0X0007	
0X38	0X0000	
0X39	0X0000	
0X01	0X011B	
0X02	0X0700	
0X03	0X1030	
0X08	0X1010	
0X0A	0X4420	
0X0B	0X5030	
0X07	0X0005	
0X07	0X0025	
0X07	0X0027	
0X07	0X0037	

Power ON sequence

Display Mode
&
Gamma Setting

Display ON sequence

SLEEP ON		
Reg (Hex)	Data (Hex)	Delay (ms)
0X07	0X0036	
DELAY		20
0X07	0X0026	
DELAY		20
0X07	0X0006	
DELAY		20
0X07	0X0000	
DELAY		10
0X13	0X0000	
0X12	0X0000	
0X10	0X0008	
DELAY		10
0X10	0X000A	

STAND-BY ON		
Reg (Hex)	Data (Hex)	Delay (ms)
0X07	0X0036	
DELAY		20
0X07	0X0026	
DELAY		20
0X07	0X0006	
DELAY		20
0X07	0X0000	
DELAY		10
0X13	0X0000	
0X12	0X0000	
0X10	0X0008	
DELAY		10
0X10	0X0009	

DEEP STAND-BY ON		
Reg (Hex)	Data (Hex)	Delay (ms)
0X07	0X0036	
DELAY		20
0X07	0X0026	
DELAY		20
0X07	0X0006	
DELAY		20
0X07	0X0000	
DELAY		10
0X13	0X0000	
0X12	0X0000	
0X10	0X0008	
DELAY		10
0X10	0X000C	

↔

SLEEP EXIT		
Reg (Hex)	Data (Hex)	Delay (ms)
0X10	0X0008	
↓		
Power ON sequence		
↓		
Display ON sequence		

↔

STAND-BY EXIT		
Reg (Hex)	Data (Hex)	Delay (ms)
0X00	0X0001	
DELAY		10
0X10	0X0008	
↓		
Power ON sequence		
↓		
Display ON sequence		

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DEEP STAND-BY EXIT		
Reg (Hex)	Data (Hex)	Delay (ms)
2 times CS pin toggle		
DELAY		2
4 times CS pin toggle		
Initialization		

or

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DEEP STAND-BY EXIT		
Reg (Hex)	Data (Hex)	Delay (ms)
H/W Reset		
Initialization		

