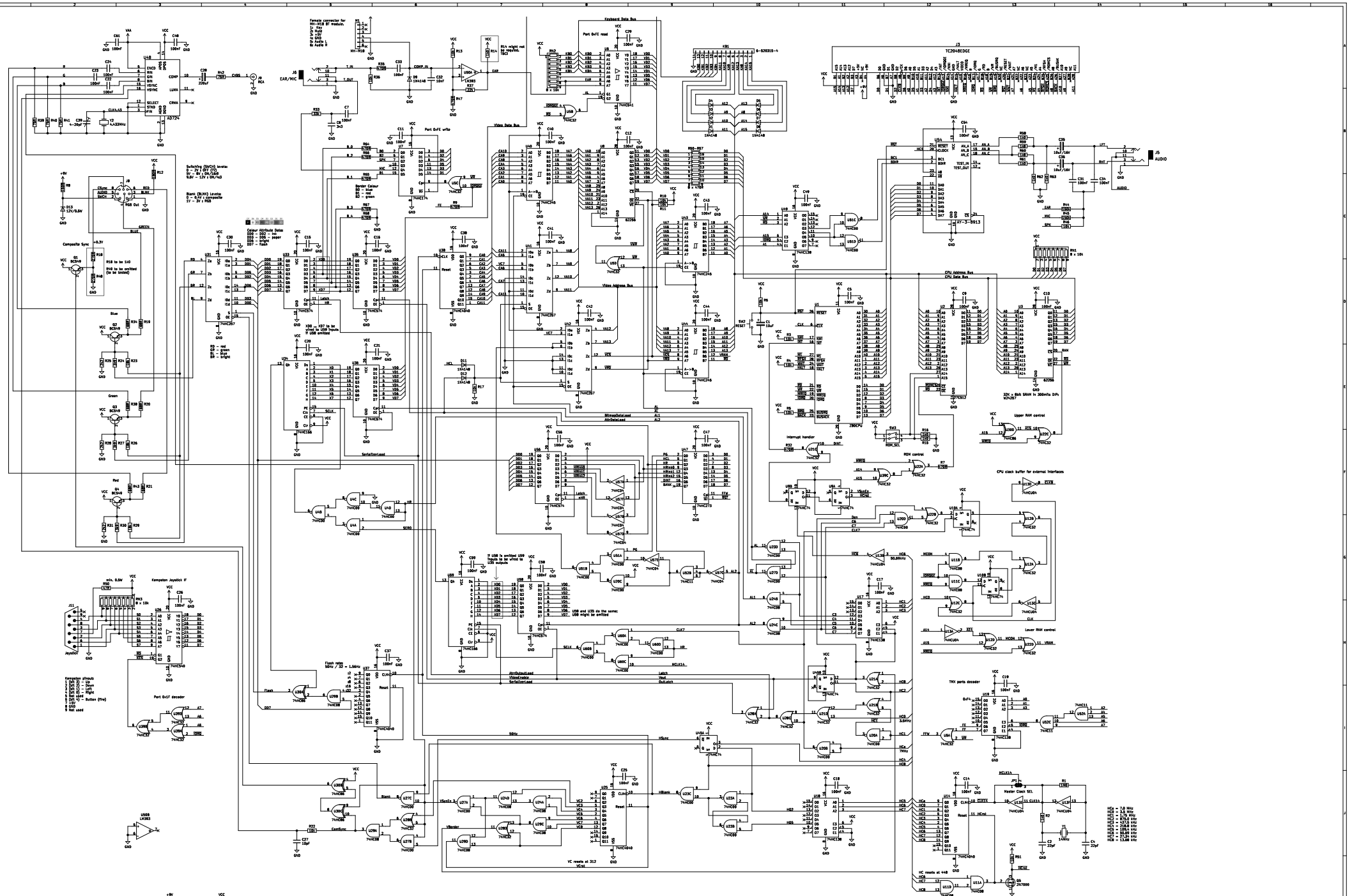
Female connector for HH-HLB BT module.
1c Key
2c Key
3c NC
4c GND
5c Audio L
6c Audio R
HH-HLB
EAR/MIC
MIC
Switching (RGCS) levels:
0V - 0.4V = CVBS
0.8V - 1.2V = RGB
0.5V - 1.3V = RGB/S
Blank (BLNK) levels:
1V - 3V = RGB
Composite Sync
Blue
Green
Red
Keyboard Data Bus
Port 0xFE read
Port 0xFE write
Video Data Bus
Video Address Bus
CPU Address Bus
CPU Data Bus
Z80CPU
Interrupt handler
ROM control
Upper RAM control
Lower RAM control
CPU clock buffer for external interfaces
TMX ports decoder
Master Clock SEL
Kempston Joystick IF
Port 0x1F decoder
Flash
SERIO
SerialInterlead
VC resets at 312
HC resets at 448
Analogue +5V rail
+9V rail
PWR_FLAG
Revisions:
0.1 - redrawn from TC2048 rev.1b by Superfo with minor modifications to the i
- for prototyping
Based on TC2048 clone (rev.1b) by Superfo
Sheet: /
File: 2021.19 Timmy HQ.sch
Title: TimmyHQ
Size: A1
Date: 2021-12-17
KiCad E.D.A. kicad (5.1.9-0-10_14)
Rev: 0.1
Id: 1/1