		JHotDraw		JUnit		JRefactory		QuickUML		PMD	
		D	TP	D	TP	D	TP	D	TP	D	TP
FM	RaM	3	2	0		N/A	N/A	0		/	/
	DPD	2	2	0		3	3	/	/	/	/
	DPF	2	1	7	1	14	1	0		8	7
	SparT	2	2	1	1	3	3	0		7	7
	(BM)	2		1		3		0		7	
AF	RaM	0		0		0		1		/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		2		1		0	
	SparT	0		0		0		0		0	
	(BM)	0		0		0		0		0	
Builder	RaM	2		0		N/A	N/A	1	0	/	/
	DPD	0		0		0		/	/	/	/
	DPF	1		0		2	2	1	0	2	
	SparT	0		0		6	6	1	1	0	
	(BM)	0		0		6		1		0	
Prototype	RaM	2	1	0		N/A	N/A	1	1	/	/
	DPD	3	3	0		0		/	/	/	/
	DPF	3	1	0		0		7	1	0	
	SparT	3	3	0		0		2	2	0	
	(BM)	3		0		0		2		0	
Singleton	RaM	2	2	0		N/A	N/A	1	1	/	/
	DPD	2	2	0		12	12	/	/	/	/
	DPF	2	2	0		6	4	1	1	5	1
	SparT	2	2	0		12	12	1	1	1	1
	(BM)	2		0		12		1		1	
Adapter	RaM	19	17	6	3	N/A	N/A	10	8	/	/
	DPD	14	14	4	4	26	23	/	/	/	/
	DPF	7	6	4	3	22	22	15	7	4	3
	SparT	14	13	6	6	31	26	9	6	3	3
	(BM)	17		6		29		9		3	
Bridge	RaM	26		0		N/A	N/A	0		/	/
	DPD	0		0		0		/	/	/	/
	DPF	26		0		0		0		0	
	SparT	0		0		0		0		0	
	(BM)	0		0		0		0		0	
Composite	RaM	1	1	1	1	N/A	N/A	2	1	/	/
	DPD	1	1	1	1	0		/	/	/	/
	DPF	1	1	1	1	0		1	1	3	0
	SparT	1	1	1	1	0		1	1	1	1
	(BM)	1		1		0		1		1	
Decorator	RaM	3	3	1	1	N/A	N/A	0		/	/
	DPD	3	3	1	1	0		/	/	/	/
	DPF	3	3	1	1	0		0		1	1
	SparT	3	3	1	1	0		0		1	1
	(BM)	3		1		0		0		1	
Façade	RaM	0		0		N/A	N/A	0		/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		0		0		0	
	SparT	0		0		0		0		0	
	(BM)	0		0		0		0		0	
Flyweight	RaM	0		0		N/A	N/A	0		/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		0		0		0	
	SparT	0		0		0		0		0	
	(BM)	0		0		0		0		0	
Proxy	RaM	0		0		N/A	N/A	1	1	/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		10	9	4	4	2	1
	SparT	0		0		12	10	6	6	2	2
	(BM)	0		0		11		6		2	
Interpreter	RaM	1		0		N/A	N/A	0		/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		0		0		0	
	SparT	0		0		0		0		0	
	(BM)	0		0		0		0		0	
TM	RaM	5	5	1	1	N/A	N/A	5	4	/	/
	DPD	5	5	1	1	17	17	/	/	/	/
	DPF	11	5	6	1	29	15	8	4	2	2
	SparT	5	5	1	1	18	18	4	4	2	2
	(BM)	5		1		18		4		2	
CoR	RaM	0		0		N/A	N/A	0		/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		0		0		0	
	SparT	0		0		0		0		0	
	(BM)	0		0		0		0		0	
Command	RaM	8	8	0		N/A	N/A	2		/	/
	DPD	8	8	0		0		/	/	/	/
	DPF	21	8	0		28		0		4	
	SparT	8	8	0		0		0		0	
	(BM)	8		0		0		0		0	
Iterator	RaM	0		0		N/A	N/A	0		/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		0		0		2	1
	SparT	0		0		0		3	3	2	2
	(BM)	0		0		0		3		2	
Mediator	RaM	0		0		N/A	N/A	0		/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		0		0		0	
	SparT	0		0		0		0		0	
	(BM)	0		0		0		0		0	
Memento	RaM	2		1		N/A	N/A	0		/	/
	DPD	0		0		0		/	/	/	/
	DPF	0		0		0		0		0	
	SparT	0		0		0		0		0	
	(BM)	0		0		0		0		0	
Observer	RaM	2	0	3	0	N/A	N/A	1	1	/	/
	DPD	3	3	1	1	1	1	/	/	/	/
	DPF	3	1	1	1	1	1	1	1	1	1
	SparT	4	3	1	1	1	1	3	3	3	2
	(BM)	3		1		1		3		2	
Stt / Str	RaM	25	9	3	2	N/A	N/A	2	1	/	/
	DPD	39	25	3	3	18	10	/	/	/	/
	DPF	44	25	9	3	39	10	12	2	21	9
	SparT	24	23	5	4	25	11	6	2	14	9
	(BM)	25		4		13		2		9	
Visitor	RaM	0		0		N/A	N/A	0		/	/
	DPD	0		0		2	1	/	/	/	/
	DPF	0		0		2	2	0		1	1
	SparT	0		0		2	2	0		1	1
	(BM)	0		0		2		0		1	

FM: FactoryMethod
AF: AbstractFactory
TM: TemplateMethod
CoR: ChianOfResponsibility
Stt: State
Str: Strategy