



Notes:

1. Decimation must be a factor of 8 for DDC8, and a factor of 4 for DDC4.
2. Clk_8x is shown here for DDC8. For DDC4, this signal would be clk_4x.
3. The fen_in period is determined by decimation value. In this example, the decimation is 64 since there are 8 ddc_out values for each fen_in pulse.

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| Title: User Block Timing | |
| Drawing number: 1 | Rev: 01 |
| Date: 01-Dec-2009 | Drawn by: CLM |
| File: User Block Timing.graffle | Sheet 1 of 1 |