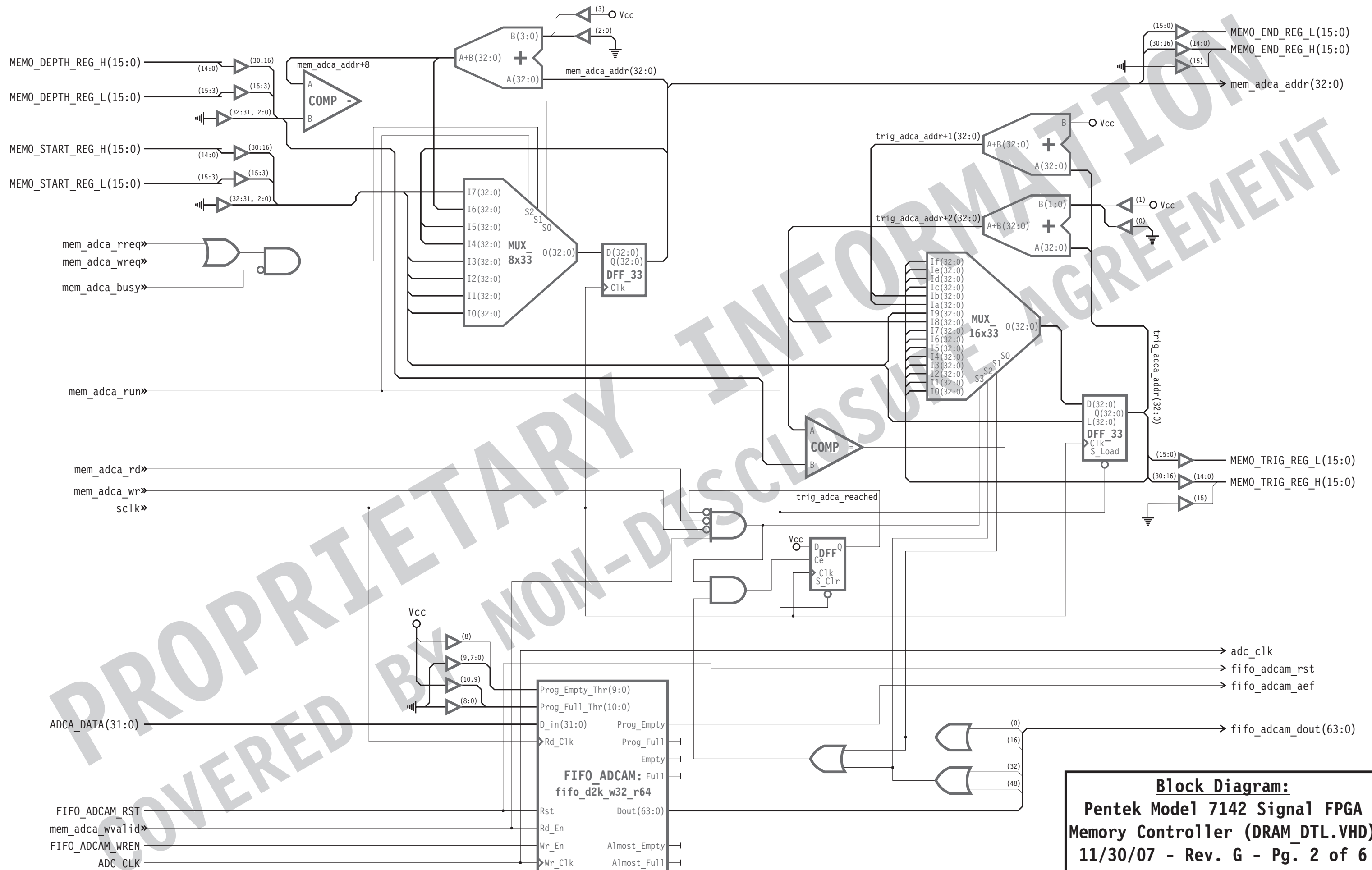
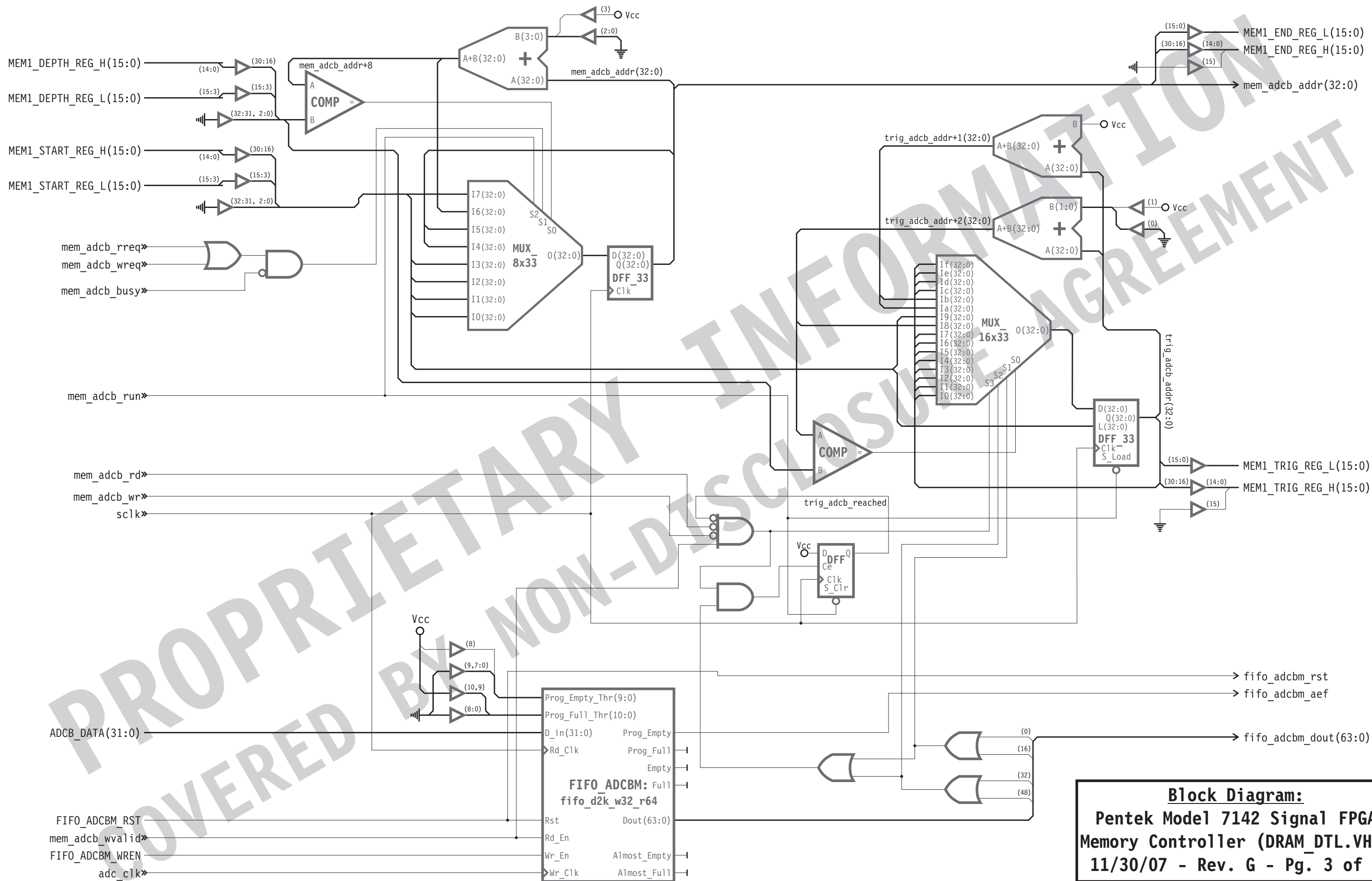
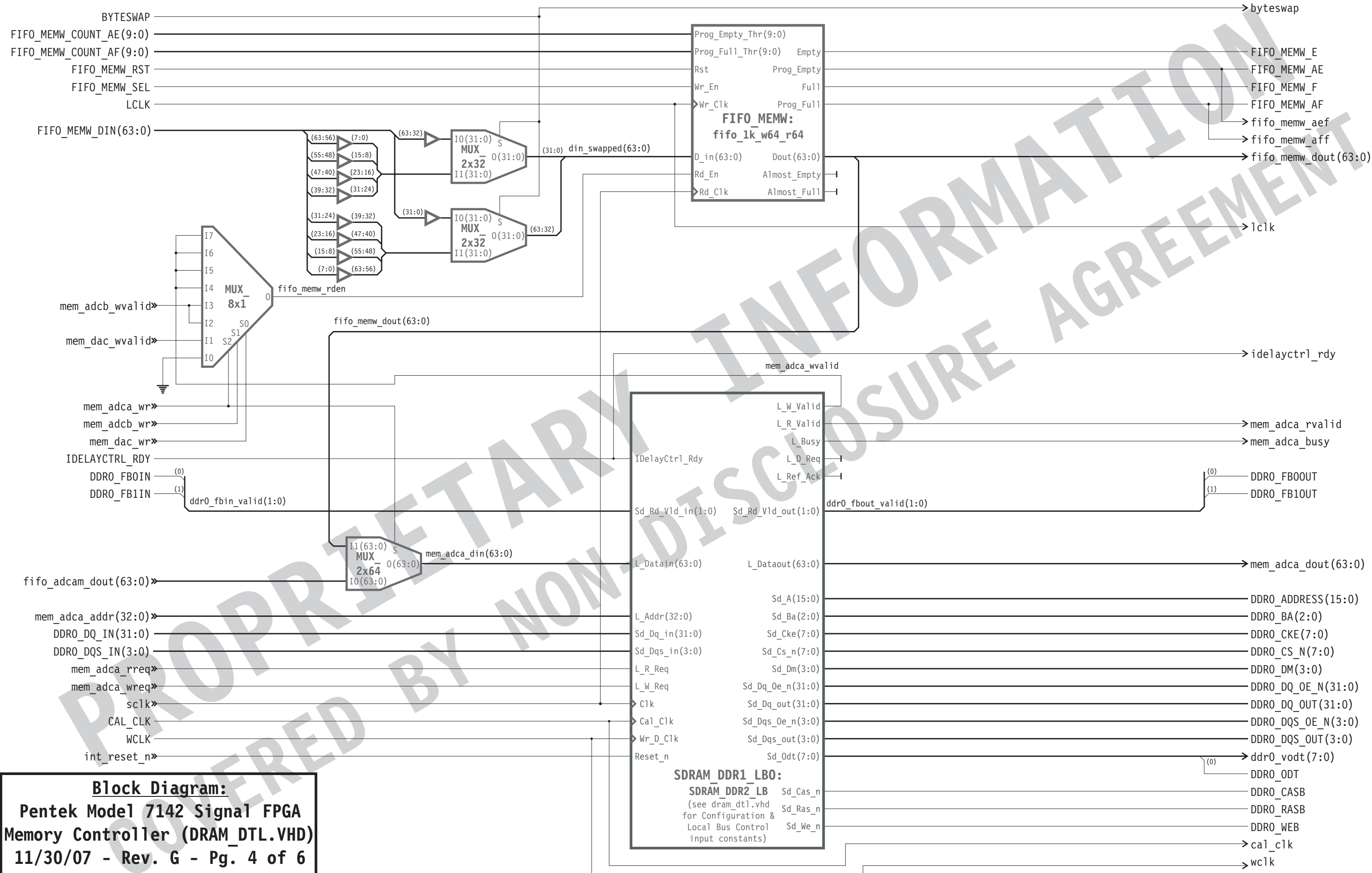


**Block Diagram:**  
**Pentek Model 7142 Signal FPGA**  
**Memory Controller (DRAM\_DTL.VHD)**  
 11/30/07 - Rev. G - Pg. 1 of 6



**Block Diagram:**  
**Pentek Model 7142 Signal FPGA**  
**Memory Controller (DRAM\_DTL.VHD)**  
**11/30/07 - Rev. G - Pg. 2 of 6**









**Block Diagram:**  
**Pentek Model 7142 Signal FPGA**  
**Memory Controller (DRAM\_DTL.VHD)**  
**11/30/07 - Rev. G - Pg. 6 of 6**

