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# Generative Model Compression and Acceleration

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NIPS EFC



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- 1 Background
- 2 Large Language Models (LLMs)
- 3 Diffusion Models
- 4 Research Summary



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2 Large Language Models (LLMs)

3 Diffusion Models

4 Research Summary



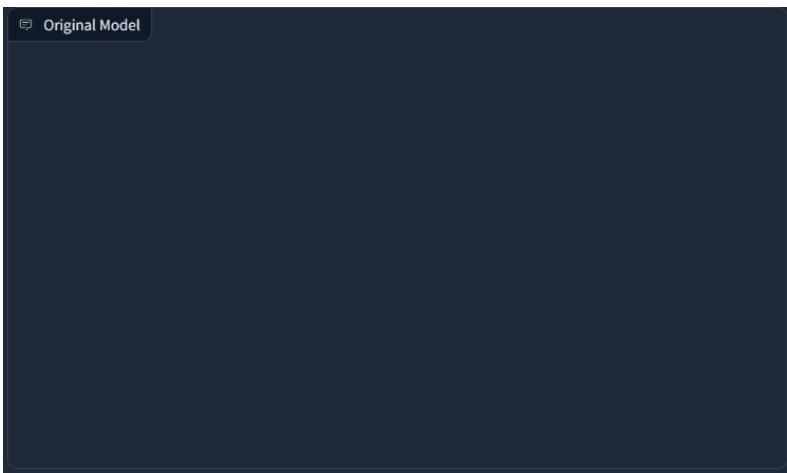
# AI-Generated Content (AIGC)

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AIGC, which uses generative models to generate content that satisfies human instructions, aims to make the content creation process more efficient and accessible<sup>[1]</sup>.

## Language Generation



Large Language Models: LLaMA-2-7B<sup>[2]</sup>

## Visual Generation



Video Diffusion Models: Sora<sup>[3]</sup>

[1] Cao, Yihan, et al. "A comprehensive survey of ai-generated content (aigc): A history of generative ai from gan to chatgpt." *arXiv* 2023.

[2] Touvron, Hugo, et al. "Llama 2: Open foundation and fine-tuned chat models." *arXiv* 2023.

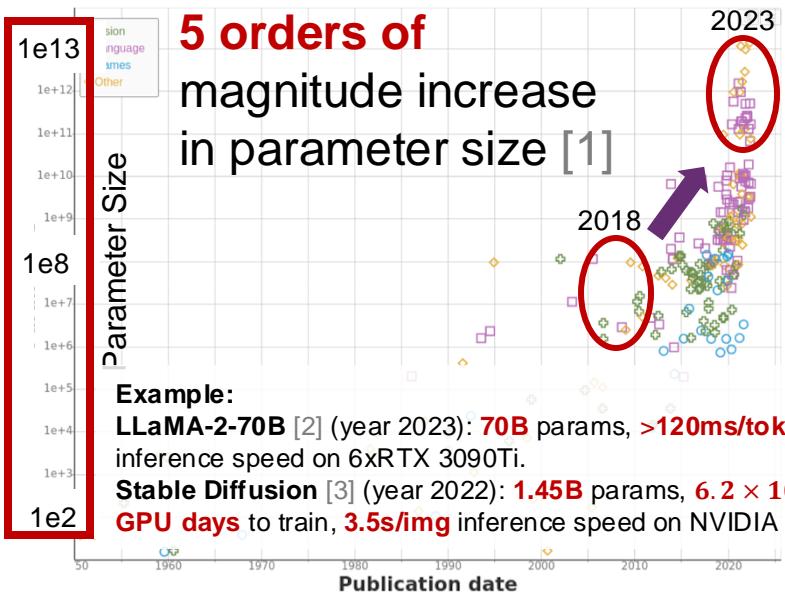
[3] Brooks, Peebles, et al., "Video generation models as world simulators." 2024.

# Trend of Generative Models

The model size of generative models has been rapidly increased

2018 - 2023

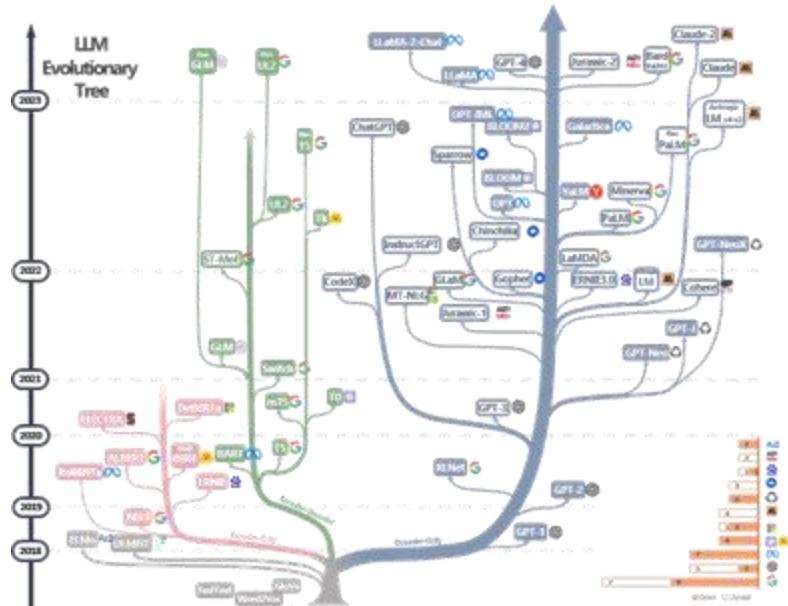
5 orders of magnitude increase in parameter size [1]



Example:

LLaMA-2-70B [2] (year 2023): 70B params,  $>120\text{ms/token}$  inference speed on 6xRTX 3090Ti.

Stable Diffusion [3] (year 2022): 1.45B params,  $6.2 \times 10^3$  GPU days to train, 3.5s/img inference speed on NVIDIA A100.



[1] Villalobos et al. "Machine Learning Model Sizes and the Parameter Gap." arXiv 2022.

[2] Touvron, Hugo, et al. "Llama 2: Open foundation and fine-tuned chat models." arXiv 2023.

[3] Rombach et al., High-Resolution Image Synthesis with Latent Diffusion Models, CVPR 2022.

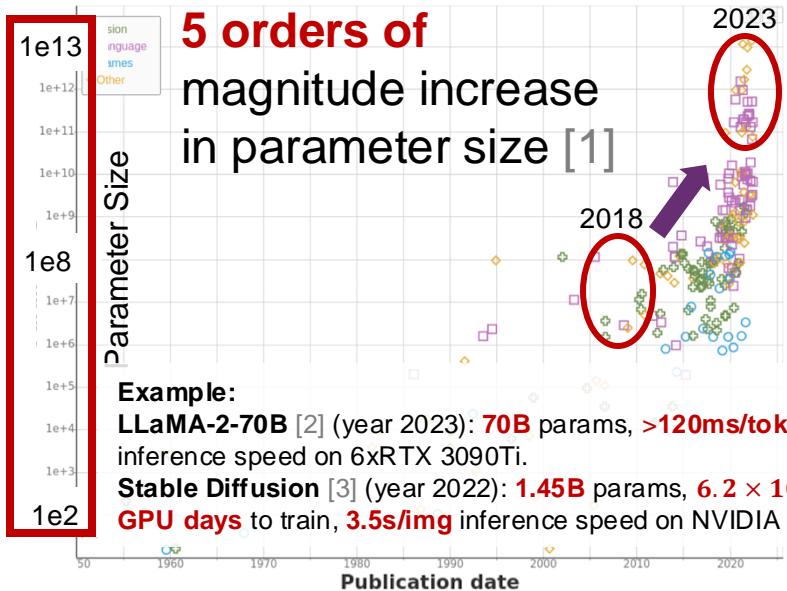
[4] Yang et al., "Harnessing the Power of LLMs in Practice: A Survey on ChatGPT and Beyond", ACM Transactions on Knowledge Discovery from Data 2023.

# Trend of Generative Models

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Stable Diffusion 1.5<sup>[3]</sup>  
~1B Params



Flux<sup>[4]</sup>  
~12B Params

[1] Villalobos et al. "Machine Learning Model Sizes and the Parameter Gap." arXiv 2022.

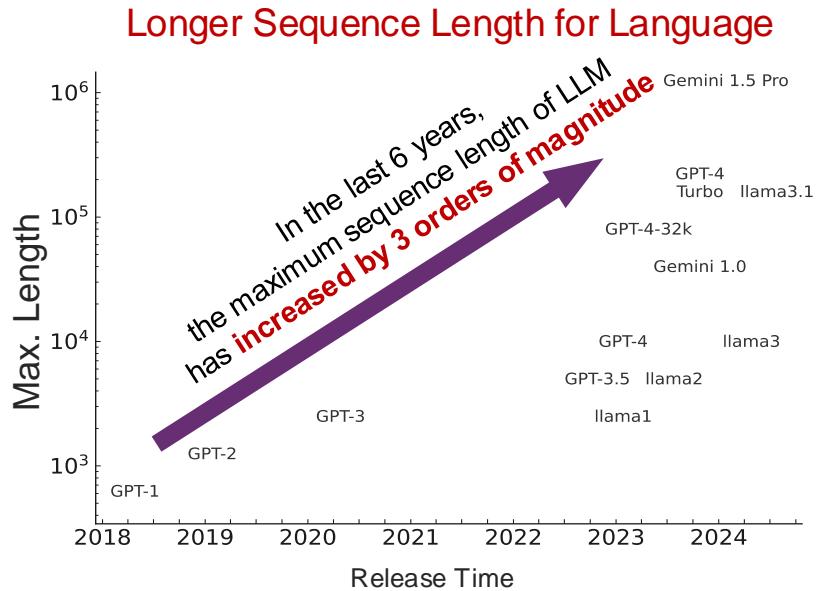
[2] Touvron, Hugo, et al. "Llama 2: Open foundation and fine-tuned chat models." arXiv 2023.

[3] Rombach et al., High-Resolution Image Synthesis with Latent Diffusion Models, CVPR 2022.

[4] black-forest-labs/flux: Official inference repo for FLUX.1 models

# Trend of Generative Models

## The generation data size has been rapidly increased



### Higher Resolution / Longer Video Length for Vision



OpenSORA<sup>[4]</sup>  
generate Videos



Pixart-sigma<sup>[5]</sup>  
generates 4K image

[1] Achiam, Josh, et al. "Gpt-4 technical report." arXiv 2023.

[2] Reid, Machel, et al. "Gemini 1.5: Unlocking multimodal understanding across millions of tokens of context." arXiv 2024.

[3] Dubey, Abhimanyu, et al. "The llama 3 herd of models." arXiv 2024.

[4] hpcatech, "Open-SoRA: Democratizing Efficient Video Production for All." <https://github.com/hpcatech/Open-Sora>

[5] Chen, Junsong et al. "PixArt-Σ: Weak-to-Strong Training of Diffusion Transformer for 4K Text-to-Image Generation." arXiv 2024.



# Challenge and Research Goal



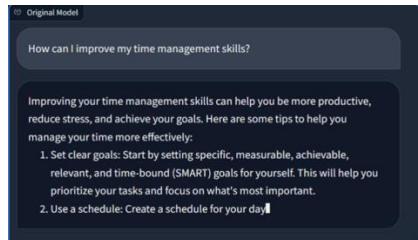
- As the model size is scaling up, the demands for computing power are increasing
- Due to real-time, usable, privacy and other application demands, physical limitations of the scenario, as well as cost control considerations, models need to be deployed on computing devices with limited computing power and low storage, and are required to run under low budgets.
- How to deploy “large” generative models and satisfy the application’s efficiency requirements while maintaining algorithmic performance?

Our goal is to **improve the efficiency (e.g., latency, throughput, storage)** of generative models to satisfy the application requirement.

# Research Overview

## Research Goal: Efficient model inference for AIGC application

### Language Generation



### Application

Large Language Models  
(e.g., LLaMA-2-7B)

### Visual Generation



Diffusion Models  
(e.g., Stable Diffusion 3)

## Methodology: Hardware-aware algorithm-level and model-level optimization

### Algorithm-level

Diffusion Timestep  
Compression

Non-Autoregressive  
Generation

### Technique

Tackling Many  
Timesteps of Diffusion

Tackling Full Autoregressive  
Generation of LLMs

### Model-level

Structure  
Design

Model  
Compression

# Research Framework

## Overview

### Survey [CSUR Submission]

Survey on efficient LLM inference techniques

## Algorithm-level

### SoT [ICLR'24]

Parallel generation via prompting.  
**1.91~2.39x speed-up**

## Model-level

### Sparse Attention

#### MoA [ICLR Submission]

Decide the heterogeneous elastic rule of the attention span for each head.  
**5.5~6.7x throughput improvement**

### Pruning

#### EEP [ICLR Submission]

Search the pruning pattern for MoE and use expert merging for finetuning.  
**48%~71% memory reduction,**  
**1.11~1.40x speed-up,**  
**better performance**

### Quantization

#### LLM-MQ [NeurIPS'23 Workshop]

Mixed-precision quantization.  
**2.8-bit quantization**

#### QLLM-Eval [ICML'24]

Evaluating the effect of quantization.  
**Providing knowledge and practical suggestions**

## Algorithm-level

### Time Step Compression

#### LCSC [ICLR Submission]

Linear combination of checkpoints.  
**15~23x training acceleration,**  
**1.25~2x timestep compression**

#### USF [ICLR'24]

#### OMS-DPM [ICML'23]

#### DD [ICLR Submission]

Search for optimal diffusion schedulers.  
**1.5~2x speed-up**

generates image in **0.01s** and can achieve **>100x** speedup for Image AR model

## Fast Compression

### FlashEval [CVPR'24]

**10x evaluation acceleration**

## Model-level Quantization

#### MixDQ [ECCV'24]

Mixed-precision quantization.  
**3x memory decrease,**  
**1.5x speed-up**

#### ViDiT-Q [ICLR Submission]

Quantization for DiT.  
**2.5x memory improvement,**  
**1.5x speed-up**

## Pruning & Sparse Attention

#### DiTFastAttn [NeurIPS'24]

Window & reused attention for DiT.  
**1.6x speed-up**

## Efficient Large Language Models

## Efficient Diffusion Models



# Acceleration Demo: LLMs

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Achieving **2x** throughput improvement with operator optimization

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Llama-2-7B

First Token Latency: 0.00 ms | Speed: 0.00 tokens/s

Once upon a time,

Generate

Undo | Retry | Clear

**LLM**

Llama-2-7B

First Token Latency: 0.00 ms | Speed: 0.00 tokens/s

Once upon a time,

Generate

Undo | Retry | Clear

**LLaMA-2-7B on AMD MI210**

Before Acceleration (right):  
**39 tokens/s**

After Acceleration (right):  
**79 tokens/s**

No performance drop

# Acceleration Demo: LLMs

- Sparse attention batch inference demo



```
rootanyu@is-c77b7irugzhi3h2-devmachine-0: /share/rootanyu/repo/MoA_release
[rootanyu@is-c77b7irugzhi3h2-devmachine-0: /share/rootanyu/repo/MoA_release] ~
(rm) rootanyu@is-c77b7irugzhi3h2-devmachine-0: /share/rootanyu/repo/MoA_release
[mod] rootanyu@is-c77b7irugzhi3h2-devmachine-0: /share/rootanyu/repo/MoA_release
[mod] I CUDA_VISIBLE_DEVICES=0 python3.8 ./evaluate/chat_demo.py --model_name lmsys/vicuna-7b-v1.5-16k --lora_path examples/lmsys-vicuna-7b-v1.5-16k/lora_4096.pt --batch_size 20
```

loading model

	w/o MoA	w MoA
Driver Version:	555.54.03	555.54.03
CUDA Version:	12.2	12.2
GPU Name	NVIDIA-SMI	nvidia-smi
Persistence-M	Off	Off
Pwr:UsageCap	63W / 400W	63W / 400W
Fan Temp	41°C	41°C
Perf	On	On
Bus-Id	PCIe 0000:00:00.0	PCIe 0000:00:00.0
Disp.A	4MB / 8192MB	4MB / 8192MB
Volatile Unerr.	0	0
ECC	0	0
Memory-Usage	8% Default	8% Default
GPU-Util	Disabled	Disabled
Compute M.		
H2 M.		

Processes:

GPU ID	GI ID	CI	PID	Type	Process name	GPU Memory Usage
N/A	38C	P0	0	0	0	0
No running processes found						

**Vicuna-7B on Nvidia-A100  
batch size 20  
end-to-end latency**

Before Sparse Attention (left):  
**Latency 42s**



After Sparse Attention (right):  
**Latency 18s**

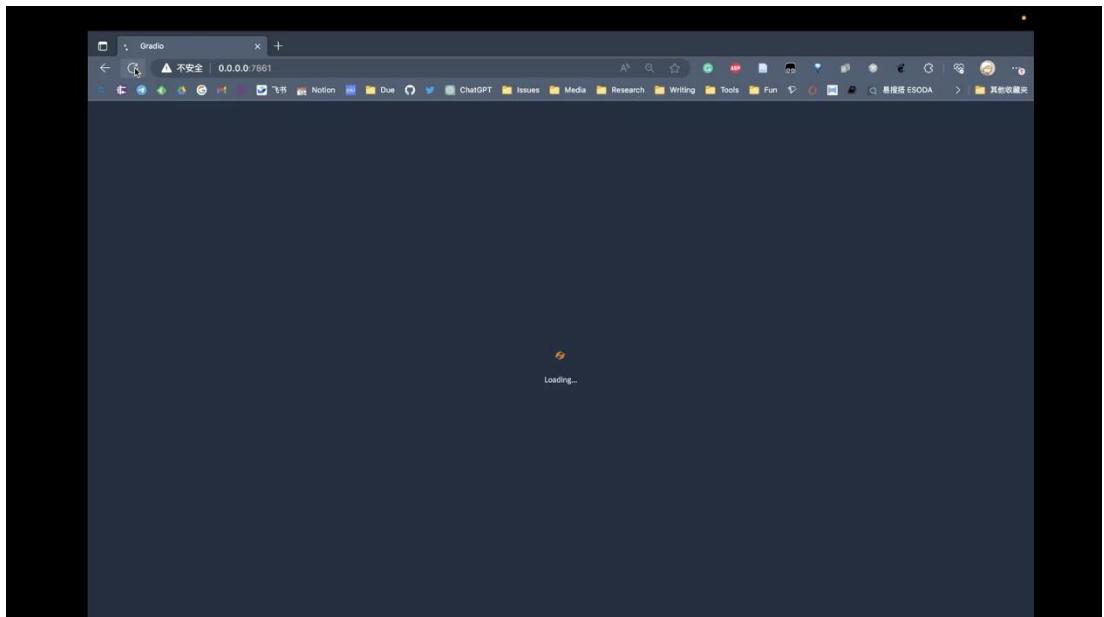
# Acceleration Demo: Diffusion Models

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## Timestep Optimization + TensorRT Deployment:

Achieving **6.9×** end-to-end speed-up and reducing **1.5×** memory



### Stable Diffusion on a single NVIDIA A100 GPU

Before Acceleration (left):  
**11.7s latency, 11.9G VRAM**



After Acceleration (right):  
**1.7s latency, 7.8G VRAM**

Almost no performance drop

# Acceleration Demo: Diffusion Models



## Efficient Attention for DiT:

Achieve up to **1.8x** latency speedup

```
(difa) difa@c7ba4d3ed4pdg3zb-devmachine-4:/share/public/hanling/DiTFastAttention$ CUDA_VISIBLE_DEVICES=5 python run_pixart.py --model PixArt->PixArt-Sigma-XL-2Kx2K-M5 --raw_mm 1 --debug  
/share/public/hanling/miniconda3/envs/difastattn/bin/python3.10/codes/run_difusers/models/training/main_ditfastattn.py --model_name pixart-sigma-xl-2kx2k-m5 --output_dir ./outputs/difastattn --deepspeed config/deepspeed_ditfastattn.json  
d will be deprecated in the future. Please use  
dels_trans instead.  
from diffusers.models.modeling_outputs import Transformer2DModelOutput, instead.  
deprecate("Transformer2DModelOutput", "1.6.x", deprecation_message="The class  
Loading checkpoint shards: 100% [██████████] 2/2 (02:38:00:00, 79.15s/it)  
Loading pipeline components... 100% [██████████] 3/5 (02:38:01:22, 41.38s/it)  
You are using the legacy behaviour of the <class 'transformers.models.t5.T5Tokenizer'>. This is expected, and simply means that the 'legacy' (previous) behavior will be used so nothing changes for you. If you want to use the new behaviour, set 'legacy': True. This should only affect you if you understand what it means thoroughly and  
are fully informed about it as explained in https://github.com/huggingface/transformers/pull/  
/24565  
Loading pipeline components... 100% [██████████] 5/5 (02:38:00:00, 31.72s/it)
```

**w/o DiTFastAttn**

```
(difa) difa@c7ba4d3ed4pdg3zb-devmachine-4:/share/public/hanling/DiTFastAttention$ CUDA_VISIBLE_DEVICES=5 python run_pixart.py --model PixArt->PixArt-Sigma-XL-2Kx2K-M5 --raw_mm 1 --debug  
/share/public/hanling/miniconda3/envs/difastattn/bin/python3.10/codes/run_difusers/models/training/main_ditfastattn.py --model_name pixart-sigma-xl-2kx2k-m5 --output_dir ./outputs/difastattn --deepspeed config/deepspeed_ditfastattn.json  
d will be deprecated in the future. Please use  
dels_trans instead.  
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deprecate("Transformer2DModelOutput", "1.6.x", deprecation_message="The class  
Loading checkpoint shards: 100% [██████████] 2/2 (02:38:00:00, 74.91s/it)  
Loading pipeline components... 100% [██████████] 3/5 (02:38:01:22, 41.38s/it)  
You are using the default legacy behaviour of the <class 'transformers.models.t5.T5Tokenizer'>. This is expected, and simply means that the 'legacy' (previous) behavior will be used so nothing changes for you. If you want to use the new behaviour, set 'legacy': True. This should only affect you if you understand what it means thoroughly and  
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/24565  
Loading pipeline components... 100% [██████████] 5/5 (02:38:00:00, 31.78s/it)
```

**with DiTFastAttn**

**Pixart-Sigma**  
**2Kx2K image, 50 steps**  
**on NVIDIA A100 GPU**

Before Acceleration (left):  
**~16s latency**



After Acceleration (right):  
**~8s latency**

Almost no performance drop

# Acceleration Demo: Diffusion Models



**Low-bit Quantization for DiT-based Image and Video Generation:**  
Achieve up to **2x** memory saving, **1.7x** latency speedup



FP16



ViDiT-Q W8A8

**OpenSORA**  
**512x512x16 Frames,**  
on NVIDIA A100 GPU

Before Acceleration (left):  
**~8.9s latency (20 step)**



After Acceleration (right):  
**~5.1s latency (20 step)**

Almost no performance drop



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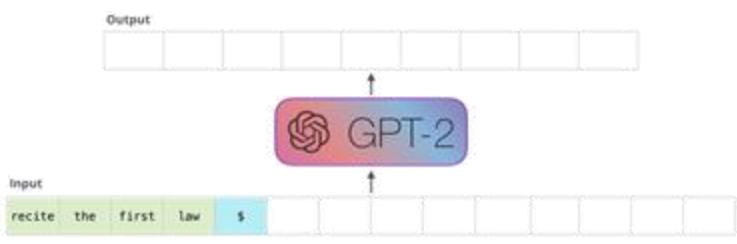
Diffusion Models

4

Research Summary

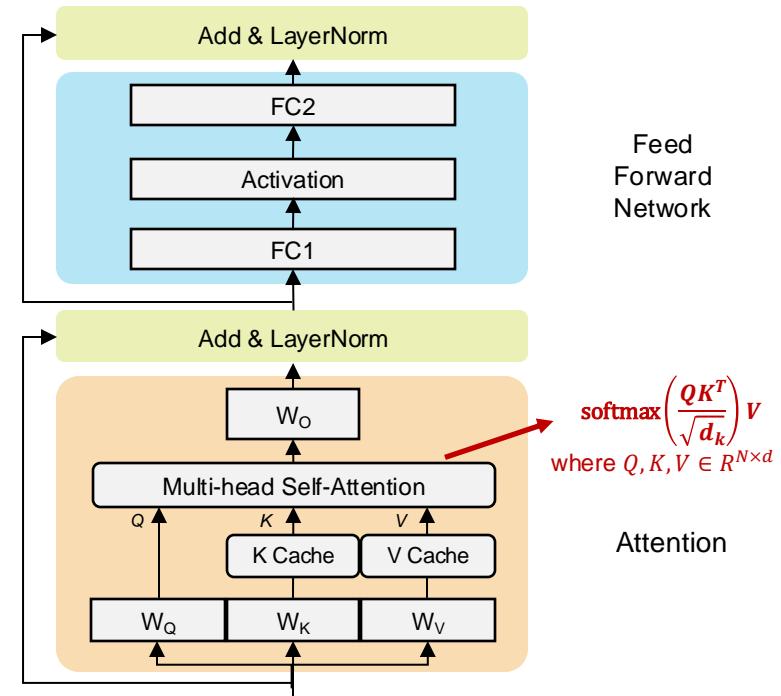
# How LLMs Do Inference

- Most LLMs are based on the Transformer architecture<sup>[1]</sup>.
- A Transformer block consists of :
  - Attention-Linear (generate matrix Q, K, V)
  - Multi-Head Attention
  - Feed Forward Network
  - Layer Norm
- A typical LLM inference process:



Example of Decoder's word-by-word translation

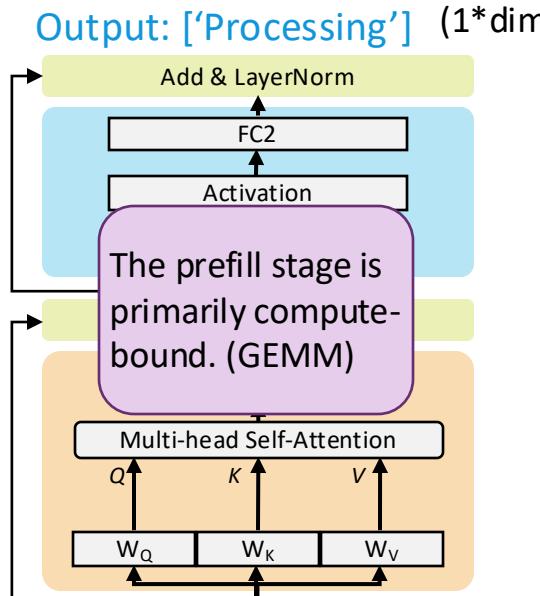
[1] Vaswani, Ashish, et al. "Attention is all you need." NeurIPS 2023.

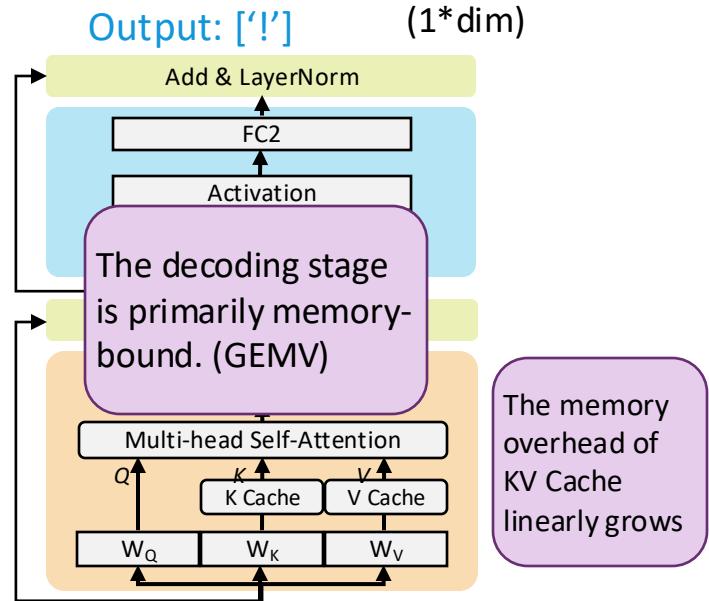


# How LLMs Do Inference

LLM Inference has two stages:

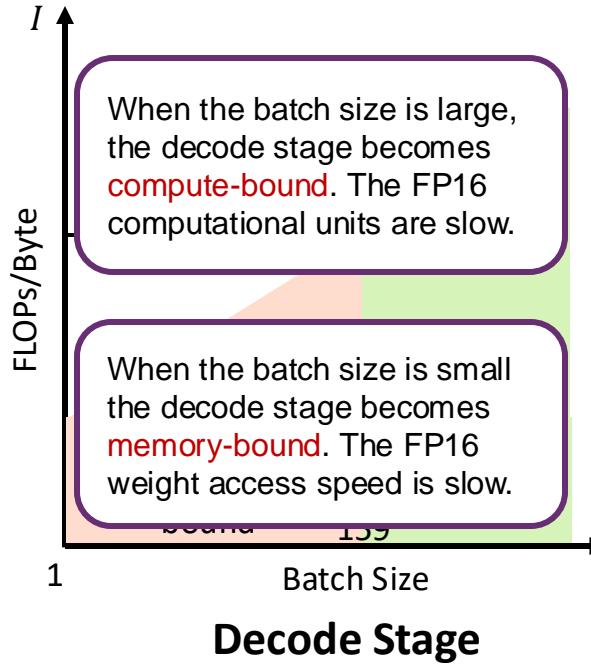
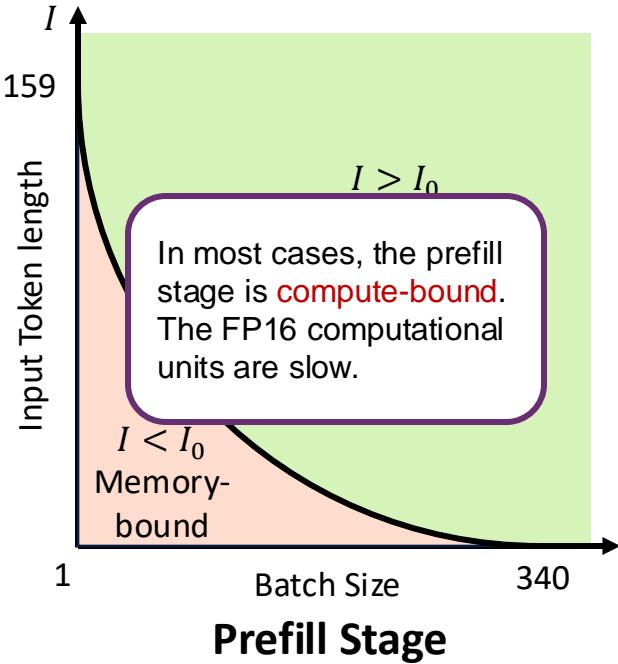
- **Prefill Stage:** takes a **prompt sequence** to generate the key-value cache (KV Cache)
- **Decode Stage:** utilizes and updates the KV cache to **generate tokens one by one**, where the current token depends on all the previously tokens



# Efficiency Analysis of LLM Inference

- Bottleneck Analysis of Large Parameter Size
  - Take LLaMA3-70B as an example:  $8192 \times 8192$  linear layer
  - A100 FP16 CUDA Core:  $I_0 = 156 \text{ FLOPs/Byte}$



# Efficiency Analysis of LLM Inference

- Bottleneck Analysis of Large Sequence Length

Llama2-7B LLM inference cost				System capability and user requirements
Seq. Length	N	2K	1M	
Compute	$O(N^2)$	28.7 TFLOP	<b>5.9×10<sup>5</sup> TFLOP</b>	A100 peak computing power* <b>312 TFLOPS</b>
Memory	$O(N)$	15 GB	<b>526 GB</b>	A100 maximum GPU memory <b>80GB</b>
First-token latency	$O(N^2)$	150 ms	<b>30 min</b>	Waiting causes customer losses; Short-term memory: <b>15-30s<sup>[1]</sup></b>
Generation speed	$O(N)$	88.50 token/s	<b>0.5 token/s</b>	Human's average reading speed: <b>5.4 token/s<sup>[2]</sup></b>

[1] Ubben, Giselle. "How long is short-term memory? Shorter than you might think." Academic Resource Center, Duke University

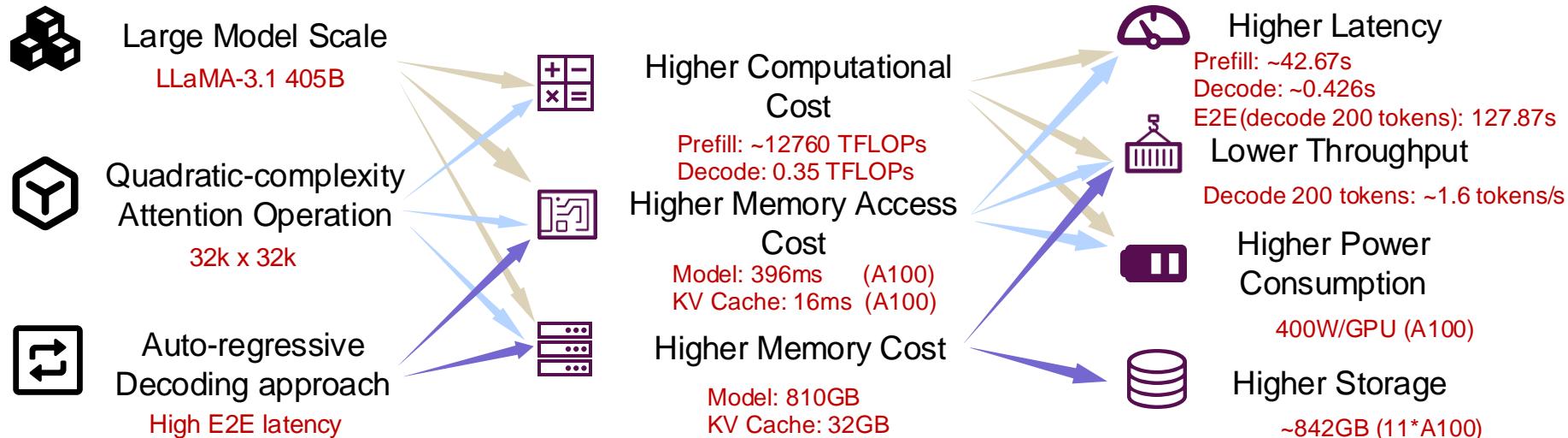
[2] Brysbaert, Marc. "How many words do we read per minute? A review and meta-analysis of reading rate." Journal of Memory and Language

\* with Llama2-7B LLM, Measured on the minimum A100-80GB graphics card that can accommodate the model; Prefill with 1 A100-80GB; Decode with 8 A100-80GB; The A100 peak performance is calculated using the FP16 TensorCore.

# Efficiency Analysis of LLM Inference

- Root causes of inefficiency during LLM Inference
  - Model scale: A large number of weights and computations.
  - Attention operation: It has quadratic complexity w.r.t. input token length.
  - Decoding approach: Generate tokens one by one (fully sequential).

For example: Deploy LLaMA-3.1 405B in the cloud server





# Efficient Techniques for LLMs

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## Directions to improve Large Language Models' efficiency



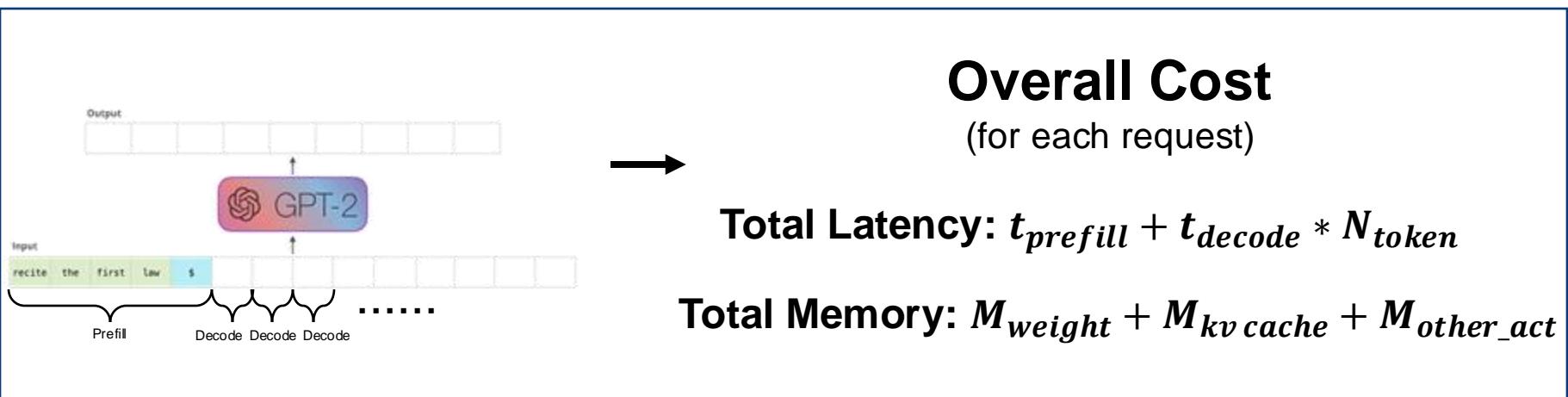
### Prefill Stage

Reduce  $t_{prefill}$ ,  
 $M_{weight}$ ,  $M_{other\_act}$



### Decode Stage

Reduce  $t_{decode}$ ,  
 $M_{weight}$ ,  $M_{kv\ cache}$



# Overview of Efficient Techniques



Lower Latency



Lower Storage



Higher Throughput



Lower Power Consumption

What algorithm property?

Cause what?

Solutions

Model Scale

- Large computation
- Large memory access
- Large memory footprint

Attention Operation

- Input-quadratic computation
- Input-quadratic memory access
- Input-quadratic memory footprint

Decoding Approach

- Low arithmetic intensity (i.e., computation / memory access) cause under-utilization
- Varying length -> Dynamically increasing KV cache cause fragmented memory, increasing both footprint and access

Data-level Optimization

Input Compression

Output Organization

Model-level Optimization

Structure Design

Model Compression

Inference Engine

Serving Framework

System-level Optimization

# Overview of Efficient Techniques



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Solutions

- Prompt pruning
- Soft prompt tuning
- ...
- Output Organization

Input Compression

Output Organization

- Dynamic MoE
- Low-complexity attention
- Quantization
- Sparse Attention
- Weight Pruning
- ...

Structure Design

Model Compression

- Graph and Operator Optimization
- Speculative decoding
- Memory Management
- Batching
- ...

Inference Engine

Serving Framework

# Overview of Efficient Techniques



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Structure Design

Model Compression

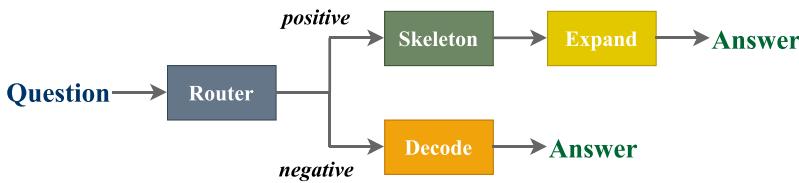
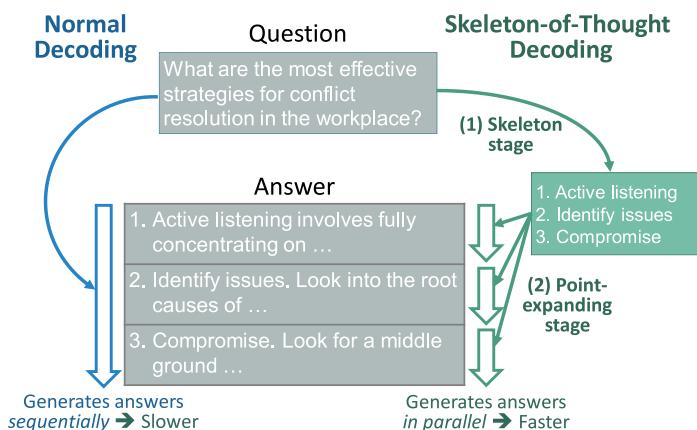
- Graph and Operator Optimization
- Speculative decoding
- Memory Management
- Batching
- ...

Inference Engine

Serving Framework

# Skeleton-of-Thought (SoT)

- Skeleton-of-Thought (SoT) consists of two stages:
  - Skeleton Stage:** Guide the LLM to output a concise skeleton of the answer.
  - Point-expanding Stage:** Guide the LLM to expand on each point from the skeleton in parallel.
- SoT can improve the hardware utilization and decrease the end-to-end latency.



We further extend **SoT with router (SoT-R)** to make the overall solution more practical.

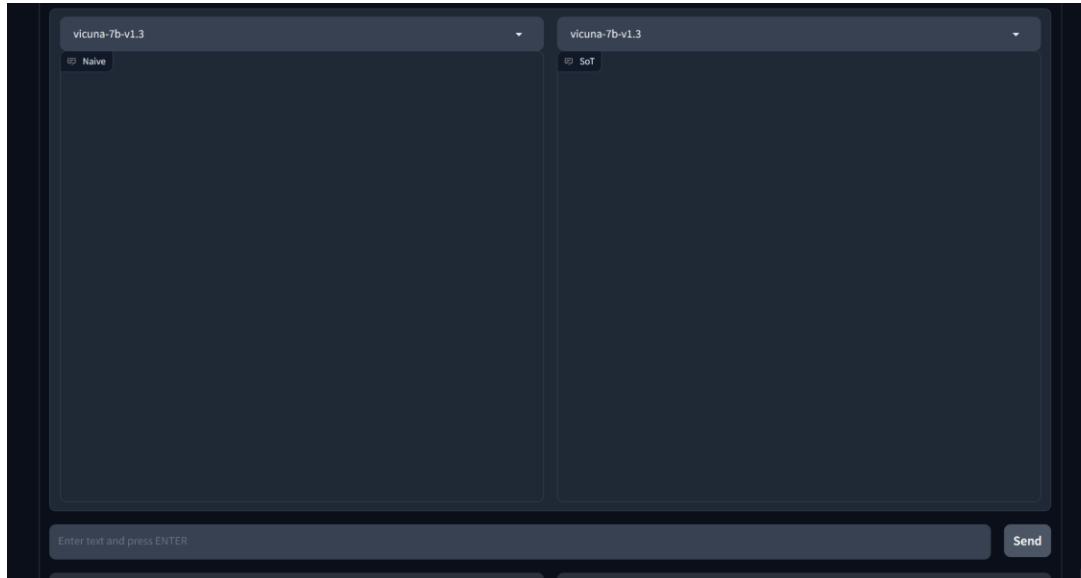
- The router first decides whether to apply the SoT decoding mode based on the user's prompt.



# Skeleton-of-Thought (SoT)



Accelerating LLM inference by up to **2.39×** end-to-end speed-up  
*without any changes to their model, system, or hardware*



Vicuna-7B model on one A100 GPU: **2.1×** end-to-end speed-up compared with sequential decoding

[1] Ning, Xuefei, et al. "Skeleton-of-Thought: Large Language Models Can Do Parallel Decoding." ICLR 2024.

# Overview of Efficient Techniques



Lower Latency



Lower Storage



Higher Throughput



Lower Power Consumption

What algorithm property?

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- Large computation
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- Output Organization

Input Compression

Output Organization

Attention Operation

- Input-quadratic computation
- Input-quadratic memory access
- Input-quadratic memory footprint

- Dynamic MoE
- Low-complexity attention
- **Quantization**
- Sparse Attention
- Weight Pruning
- ...

Structure Design

Model Compression

Decoding Approach

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- Varying length -> Dynamically increasing KV cache cause fragmented memory, increasing both footprint and access

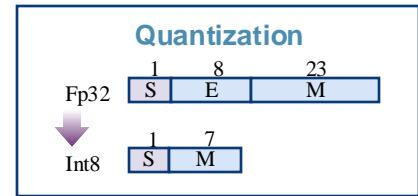
- Graph and Operator Optimization
- Speculative decoding
- Memory Management
- Batching
- ...

Inference Engine

Serving Framework

# Quantization Technique

- Quantization is a promising technique to address the aforementioned efficiency issues.
  - Taking **signed uniform** quantization as an example, quantization parameters include
    - Scaling Factor**,    **Zero Point**,    **Bitwidth**
    - $x_{\text{int}} = \text{clip} \left( \left[ \frac{x}{s_x} \right] + z; q_{\min}, q_{\max} \right), \text{ where } q_{\max} = 2^{b-1} - 1, q_{\min} = -2^{b-1}$
- The **Weight-Activation Quantization** methods enable the utilization of low-precision Tensor Cores to mitigate the compute-bounded GEMM operators in the prefill stage.
- The **Weight-only Quantization** methods prove effective to accelerate the memory-bounded GEMV operators in the decoding stage.
- The **KV Cache Quantization** methods are necessary to alleviate the large memory overhead when handling tasks with **long contexts or large batch sizes**.



# Mixed-precision Quantization (LLM-MQ)

Expected to accelerate linear operators by **1.9~2.7×** speed-up via mixed-precision quantization and sparse outliers protection technique

- Assign high bit-width to high-sensitivity layers in order to minimize the change in model output.
  - Use first-order information to estimate the sensitivity:

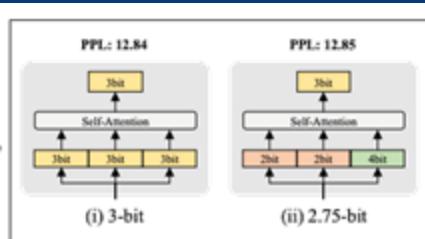
$$\mathcal{L}(Q_b(\mathbf{W}_i)) \approx \mathcal{L}(\mathbf{W}) + \mathbf{g}_i^T (\mathbf{W}_i - Q_b(\mathbf{W}_i)),$$

- For each layer, we have

$$\min |\mathcal{L}(Q_b(\mathbf{W}_i))| \leq |\mathcal{L}(\mathbf{W}_i)|$$

- We model the above constraint as the following integer programming problem:

$$\begin{aligned} & \arg \min_{c_{i,b}} \sum_i^N \sum_b c_{i,b} \cdot s_{i,b}, \\ & \text{s.t. } \sum_b c_{i,b} = 1, \quad \sum_i^N \sum_b c_{i,b} \cdot \mathcal{M}(Q_b(\mathbf{W}_i)) \leq \mathcal{B}, \\ & c_{i,b} \in \{0, 1\}, b \in \{2, 3, 4\}, \end{aligned}$$



- For zero-shot understanding tasks:
  - When the average accuracy loss is around **0.1%**, the model can be quantized to an average of **3.6** bits.
  - When the average accuracy loss is around **1%**, the model can be quantized to an average of **2.8** bits.

Does the accuracy on specific tasks sufficiently reflect the **effect of quantization** on LLMs?

	tokqa	Avg. (↑)	Wiki (↓)
0	65.08	7.89	
0	64.67	8.12	
0	63.23	9.26	
0	41.74	1056.33	
0	65.03	8.08	
0	64.02	8.81	
60	37.06	5e6	

4.0	79.49	76.31	69.30	58.50	41.20	64.96	8.03	
3.8	79.22	76.22	69.85	58.29	41.40	65.00	8.08	
3.6	79.05	75.88	69.77	58.59	42.20	65.10	8.23	
3.4	79.49	74.77	69.61	58.12	40.60	64.52	8.61	
3.2	79.33	75.12	67.96	57.87	41.60	64.38	8.43	
LLM-MQ (Ours)	3.0	79.00	75.08	68.59	57.79	41.00	64.29	8.54
	2.8	78.73	74.32	67.96	57.95	41.20	64.03	8.83
	2.6	78.35	73.81	68.03	57.32	39.40	63.38	9.35
	2.4	77.31	72.93	68.59	54.63	40.00	62.69	10.03
	2.2	76.77	70.83	67.09	55.26	38.40	61.67	10.80
	2.0	75.84	68.32	65.51	54.29	37.20	60.23	12.17

# Evaluating Quantized LLMs (QLLM Eval)



- Knowledge summary

Knowledge Level	Key Knowledge
Tensor-level	<ol style="list-style-type: none"><li><b>Tensor type (Sec. 3.2):</b> The larger the model, the higher the tolerance for Weight-only and KV Cache Quantization, while the tolerance for Activation Quantization is lower.</li><li><b>Tensor position (Sec. 3.2):</b> The sensitivity to quantization varies significantly across different tensor positions due to their distinct data distributions.</li></ol>
Model-level	<ol style="list-style-type: none"><li><b>(Sec. 3.3)</b> The relative rankings of quantized LLMs are generally consistent with those of the FP16 LLMs when the bit-width is higher than W4, W4A8, and KV4.</li><li><b>(Sec. 3.3)</b> Leveraging MoE to increase the model size can improve the model's performance but may not improve the tolerance to quantization.</li></ol>
Task-level	<ol style="list-style-type: none"><li><b>Emergent abilities (Sec. 4):</b> The tolerance of Multi-Step Reasoning and Self-Calibration to quantization is lower than that of Instruction-Following and In-Context Learning abilities.</li><li><b>Dialogue tasks (Sec. 6):</b> As the bit-width decreases, sentence-level repetition occurs first, followed by token-level repetition, and token-level randomness.</li><li><b>Long-Context tasks (Sec. 7):</b> The longer the text, the larger the performance loss caused by Weight and KV Cache quantization. Most LLMs are more sensitive to KV Cache Quantization than Weight-only and Weight-Activation Quantization.</li></ol>
Bit-width Recommendation	<ol style="list-style-type: none"><li><b>Basic NLP tasks (Sec. 3):</b> W4, W4A8, KV4, W8KV4.</li><li><b>Emergent (Sec. 4):</b> W8, W8A8, KV8 (<math>&lt; 13B</math>); W4, W4A8, KV4 (<math>\geq 13B</math>).</li><li><b>Trustworthiness (Sec. 5):</b> W8, W8A8, KV8 (<math>&lt; 7B</math>); W4, W4A8, KV4 (<math>\geq 7B</math>).</li><li><b>Dialogue (Sec. 6):</b> W8, W8A8, KV4.</li><li><b>Long-Context (Sec. 7):</b> W4, W4A8, KV4 (token <math>&lt; 4K</math>); W4, W4A8, KV8 (token <math>\geq 4K</math>).</li></ol> <p><i>(Note: Within 2% accuracy loss on the evaluated tasks. The recommended quantization bit-width may not generalize to other LLMs or tasks)</i></p>

# Overview of Efficient Techniques



Lower Latency



Lower Storage



Higher Throughput



Lower Power Consumption

What algorithm property?

Cause what?

Solutions

Model Scale

- Large computation
- Large memory access
- Large memory footprint

- Prompt pruning
- Soft prompt tuning
- ...
- Output Organization

Input Compression

Output Organization

Attention Operation

- Input-quadratic computation
- Input-quadratic memory access
- Input-quadratic memory footprint

- Dynamic MoE
- Low-complexity attention
- Quantization
- **Sparse Attention**
- Weight Pruning
- ...

Structure Design

Model Compression

Decoding Approach

- Low arithmetic intensity (i.e., computation / memory access) cause under-utilization
- Varying length -> Dynamically increasing KV cache cause fragmented memory, increasing both footprint and access

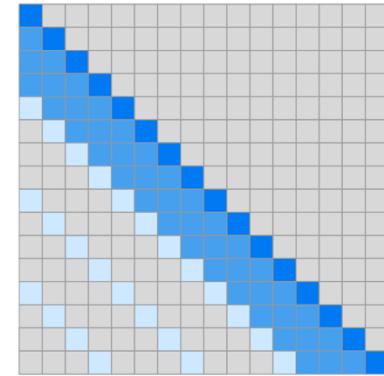
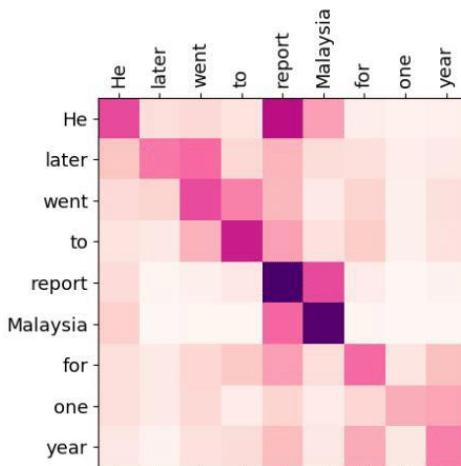
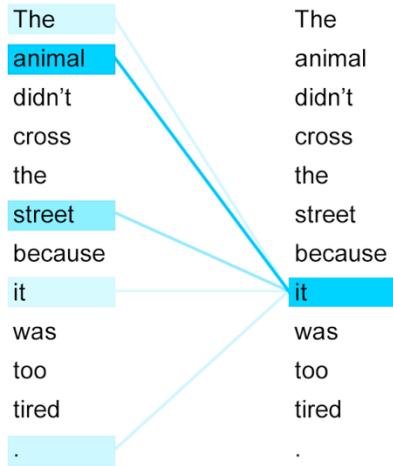
- Graph and Operator Optimization
- Speculative decoding
- Memory Management
- Batching
- ...

Inference Engine

Serving Framework



# Attention Mechanism



## Attention Mechanism

each word "looks at" other words in the sentence to determine their **relevance** (attention value) to the current word.

## Attention Matrix

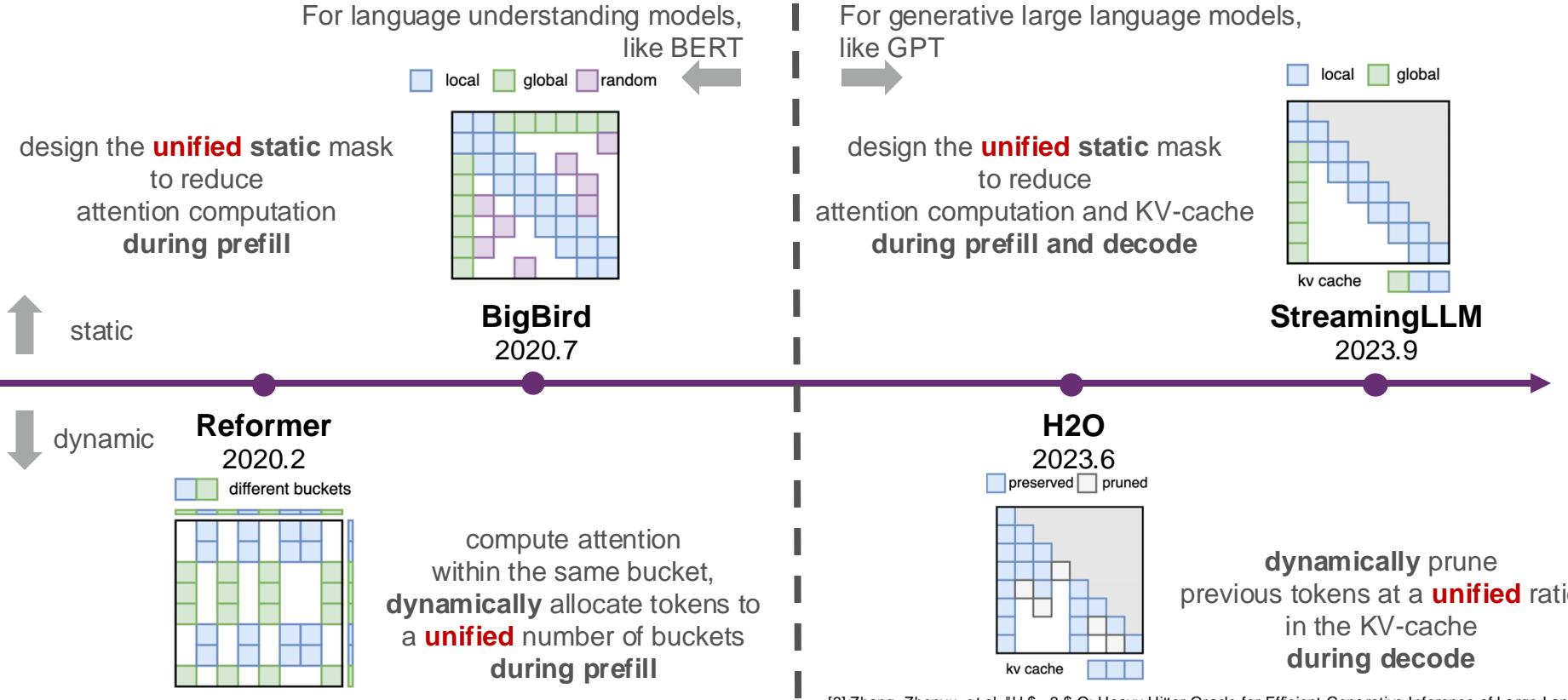
Represents the **relevance** between word pairs with **matrix**, showing the the attention values.

## Sparse Attention

Each word doesn't need to focus on all words, **only a few relevant** ones, such as nearby context.\*

[1] Child, Rewon et al. "Generating Long Sequences with Sparse Transformers.", arXiv 2019

# Sparse Attention Methods



[1] Kitayev, Nikita, Łukasz Kaiser, and Anselm Levskaya. "Reformer: The efficient transformer." arXiv 2020

[2] Zaheer, Manzil, et al. "Big bird: Transformers for longer sequences." NeurIPS 2020

[3] Zhang, Zhenyu, et al. "H \$ \_2 \$ O: Heavy-Hitter Oracle for Efficient Generative Inference of Large Language Models." arXiv 2023

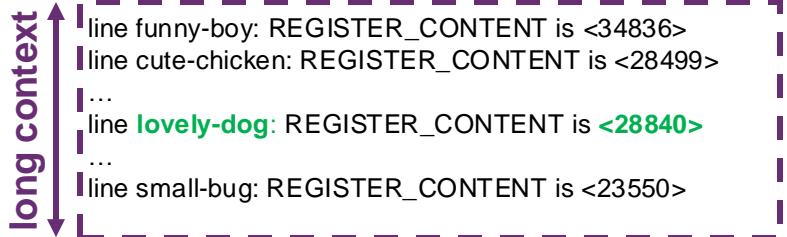
[4] Xiao, Guangxuan, et al. "Efficient Streaming Language Models with Attention Sinks." ICLR 2024.

# The Local Context Problem

## needle-in-a-haystack task



Below is a record of lines I want you to remember.  
For each line index, memorize its corresponding  
<REGISTER\_CONTENT>.



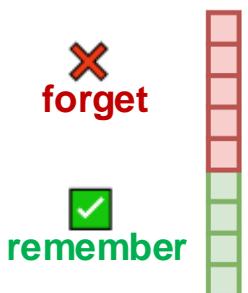
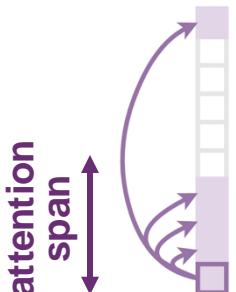
Tell me what is the <REGISTER\_CONTENT> in line **lovely-dog**? I need the number.



The <REGISTER\_CONTENT> is **<28840>** ✓

The <REGISTER\_CONTENT> is **<23550>** ✗

## local attention, local context



local attention<sup>[1]</sup>  
+ global attention on  
initial tokens

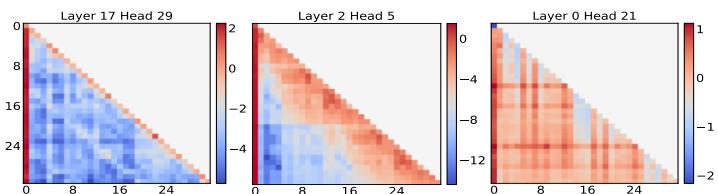
LLM **forgets** the context  
beyond the **attention span**

[1] Xiao, Guangxuan, et al. "Efficient Streaming Language Models with Attention Sinks." ICLR 2024.

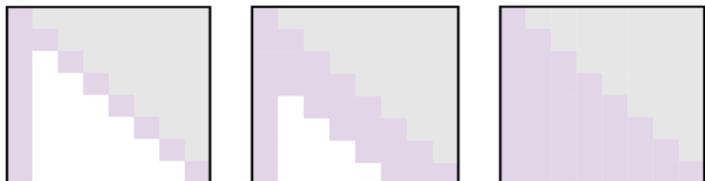
# Mixture of Attention Pattern

- Insight: different attention patterns exist in a single LLM

## For different attention heads

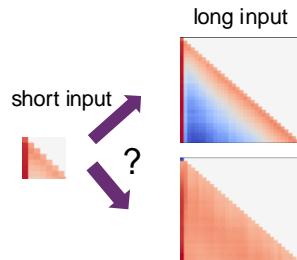


🔍 different heads show different attention spans

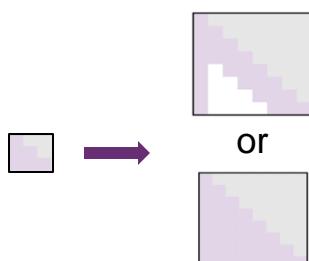


💡 find the optimal attention span for each head

## For different input lengths



🔍 different input lengths show different elastic rules



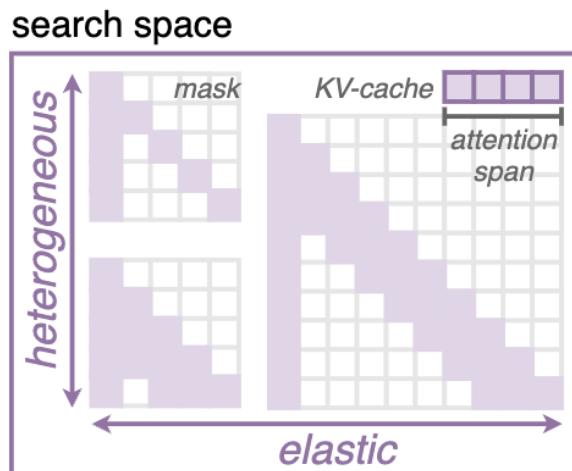
💡 find the optimal elastic rule for each head

# Mixture of Sparse Attention (MoA)



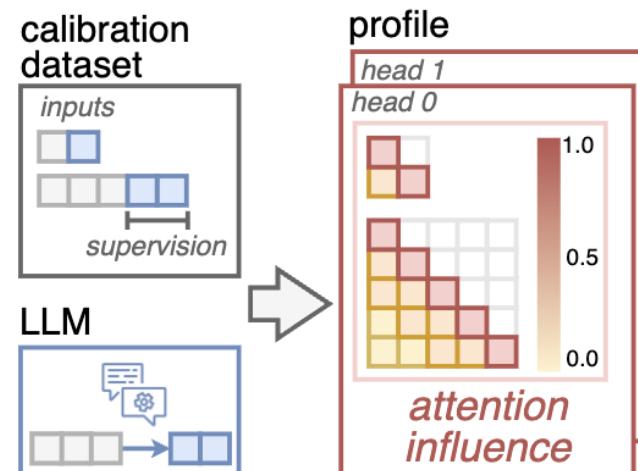
## Step1: Dataset

Construct calibration dataset using  
**long-contextual** MultiNews dataset  
along with **summarizations**  
**generated by original LLM.**



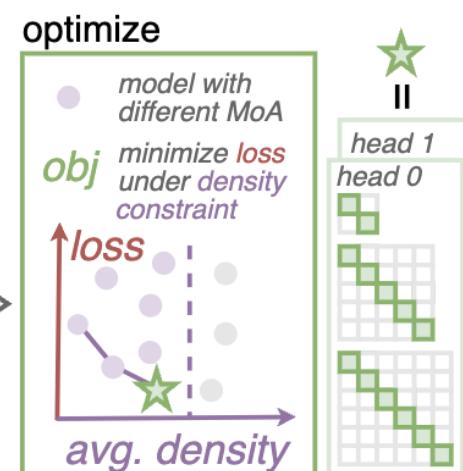
## **Step2: Profile**

**Automatically quantify** the **influence** of different attention values in LLM on final prediction results, , producing **accuracy-density trade-offs** curves for all schemes.



## Step3: Optimize

Select the **optimal elastic rule** for each attention head to **minimize the overall accuracy impact** at a given sparsity level across input lengths.

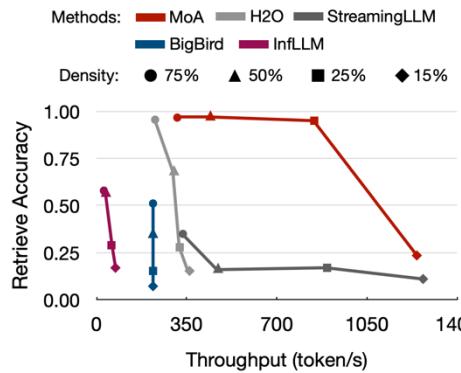


With the masks, large models can **skip** the corresponding attention **computations** and **KV-Cache**, achieving inference efficiency optimization **without needing additional training**.

# Mixture of Sparse Attention (MoA)

- Performance overview

## Accuracy-Throughput Pareto Front

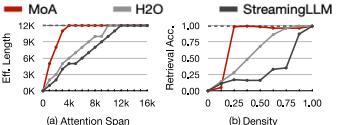


**Pareto front**  
better trade-off than baselines  
Vicuna-7B model with 8k input length



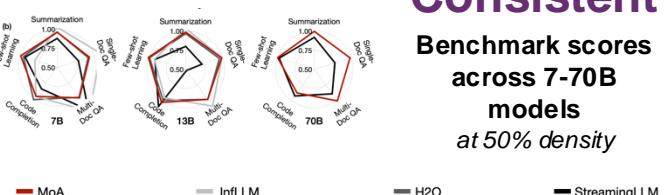
**Open source**  
project page with code  
compression pipeline,  
CUDA kernel

## Long Context Retrieval



Attention	32k	64k	128k	256k
Original	0.98	0.93	0.76	0.37
InfLLM	0.43	0.32	0.25	OOT
StreamingLLM	0.52	0.48	0.41	0.25
<b>MoA</b>	<b>1.00</b>	<b>0.92</b>	<b>0.83</b>	<b>0.46</b>

## Long Context Understanding



**3.9x**  
effective context length  
over avg. attention span

**256k**  
generalizable input length  
when profile within 8k

## Efficiency

attributed to:

- 3.0x** static-size KV-Cache
- 1.5x** reduced attention
- 1.4x** batch size
- 1.2x** GPU kernel design

**Consistent**  
Benchmark scores  
across 7-70B  
models  
at 50% density

Model	Framework	Attention	4k			8k			16k		
			Batch	Throughput	Batch	Throughput	Batch	Throughput	Batch	Throughput	Batch
7B	vLLM	PagedAttention	30	628.8	15	323.0	8	145.5			
	FlexGen	H2O	20	754.9	6	296.3	1	51.7			
	HuggingFace	InfLLM	15	62.0	10	37.5	6	19.2			
	HuggingFace	StreamingLLM	50	945.1	25	467.3	12	232.0			
	FlashAttention2	+Static KV-Cache	30	134.6	15	66.9	8	32.9			
		+Reduced Attention	30	496.1	15	219.5	8	91.6			
		+Increased Batch	50	722.5	15	369.9	8	178.3			
		+Kernel (=MoA)	50	<b>1099.0</b>	25	<b>535.7</b>	12	<b>257.3</b>			
	vLLM	PagedAttention	16	314.8	8	160.5	4	71.1			
	FlexGen	H2O	12	330.2	4	138.2	1	37.4			
	HuggingFace	InfLLM	8	30.3	5	17.63	3	11.3			
	HuggingFace	StreamingLLM	28	478.4	14	241.2	7	116.5			
13B	FlashAttention2	+Static KV-Cache	16	81.3	8	40.8	4	19.8			
		+Reduced Attention	16	264.6	8	111.3	4	62.2			
		+Increased Batch	28	329.6	8	156.4	4	87.3			
		+Kernel (=MoA)	28	<b>471.5</b>	14	<b>222.6</b>	7	<b>108.3</b>			
	vLLM	PagedAttention	28	<b>550.9</b>	14	<b>267.6</b>	7	<b>132.3</b>			

# Overview of Efficient Techniques



Lower Latency



Lower Storage



Higher Throughput



Lower Power Consumption

What algorithm property?

Model Scale

Cause what?

- Large computation
- Large memory access
- Large memory footprint

Attention Operation

- Input-quadratic computation
- Input-quadratic memory access
- Input-quadratic memory footprint

Decoding Approach

- Low arithmetic intensity (i.e., computation / memory access) cause under-utilization
- Varying length -> Dynamically increasing KV cache cause fragmented memory, increasing both footprint and access

Solutions

- Prompt pruning
- Soft prompt tuning
- ...
- Output Organization

Input Compression

Output Organization

- Dynamic MoE
- Low-complexity attention
- Quantization
- Sparse Attention
- **Weight Pruning**
- ...

Structure Design

Model Compression

- Graph and Operator Optimization
- Speculative decoding
- Memory Management
- Batching
- ...

Inference Engine

Serving Framework

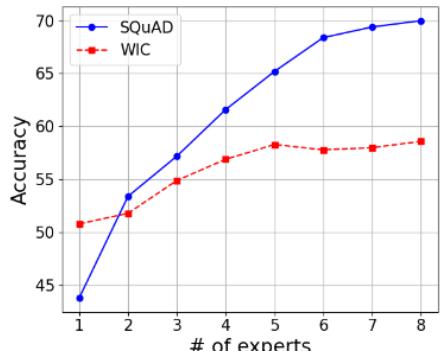
# Efficient Expert Pruning (EEP)

INFINIGENCE  
无问芯答



Construct search space of expert merging and search for coefficients.  
Can be used to prune active/total expert num.

Adjust number of active expert



**Key Observations:**

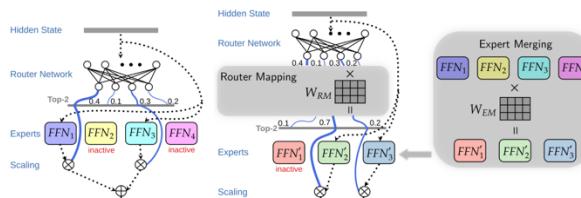
1. **Inactive expert** can benefit
2. **Redundancy** exists

## Search Space

1. Weight Merging Matrix
2. Routing weights transformation

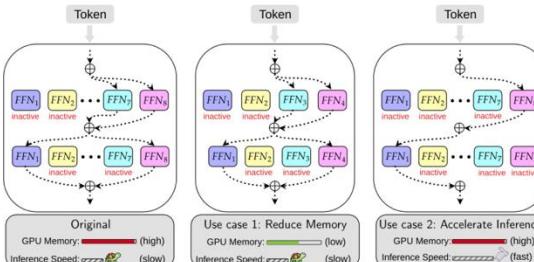
## Search Process

1. Discrete (only prune)
2. Continuous (expert merging)



## Use Cases

1. Reduce total expert
2. Reduce active expert



# Efficient Expert Pruning (EEP)

**INFINIGENCE**  
无问芯答



EEP prunes **75%/50% total/active expert** while achieves comparable and even better performance. EEP can generalize well on OOD data.

## Reduce Total Experts

Expert	Method	COPA	MultiRC	WIC	WSC	RTE	BoolQ	CB	ReCoRD	DROP	SQuAD	Avg.
Num=8	Full Model	89.0	83.0	51.8	63.5	73.2	77.4	51.7	50.3	30.6	53.4	62.4
Num=4	Random	63.8	49.4	37.6	43.3	45.1	50.2	38.7	35.1	27.4	58.3	44.9
	Frequency [37]	63.0	74.8	36.0	34.6	18.1	71.0	30.4	41.6	29.9	58.2	45.8
	Soft Activation [37]	73.0	30.6	51.4	37.5	41.9	40.4	17.9	36.8	33.3	10.2	37.3
	NAEE [34]	87.0	76.0	52.6	64.5	61.7	77.2	51.7	50.4	30.6	53.0	60.5
	EEP (Prune Only)	95.0	81.2	57.8	67.3	74.0	82.8	69.6	60.0	37.3	75.2	70.3
Num=2	EEP (Prune+Merge)	99.0	84.6	65.0	73.1	76.9	84.8	75.0	63.6	39.7	80.6	74.2
	Random	36.8	22.3	13.6	15.0	28.4	15.5	38.6	16.9	18.3	36.9	24.2
	Frequency [37]	51.0	17.6	8.8	1.9	48.4	30.6	35.7	10.4	14.9	9.2	24.9
	Soft Activation [37]	33.0	18.2	49.4	18.5	15.2	1.8	32.1	4.4	11.7	50.0	23.4
	NAEE [34]	75.0	42.4	48.4	49.0	54.5	49.8	19.6	42.0	31.2	58.2	47.0
8	EEP (Prune Only)	76.0	63.8	51.8	63.5	64.3	70.6	58.9	47.2	37.1	64.0	59.7
	EEP (Prune+Merge)	93.0	71.6	58.6	65.4	69.0	75.6	66.1	47.2	38.4	70.2	65.6

## Reduce Active Experts

Total	Active	Method	WIC	WSC	BoolQ	CB	SQuAD	Avg.
8	2	Full Model	51.8	63.5	77.4	51.7	53.4	59.6
	1	Full Model	50.8	48.1	66.0	48.2	43.8	51.4
	1.4~1.5	Dyn [34]	50.0	59.6	72.8	46.4	44.8	54.7
	1	EEP	59.2	70.2	79.0	66.1	51.8	65.3
4	1	NAEE [34]	48.6	20.2	56.2	33.9	51.8	42.1
	1.4~1.5	NAEE+Dyn [34]	43.4	61.5	36.2	53.6	53.4	49.6
	1	EEP	55.8	70.2	74.4	64.3	72.0	67.3

## Generalization Test

Budget	Method	IID (50 val. sets)	OOD (7 unseen datasets)
Num=8	Full Model	60.7	72.6
	Random	53.0±9.6	64.6±10.0
	Frequency [37]	35.2	35.0
	Soft Activation [37]	54.3	65.6
	NAEE [34]	57.5	69.4
Num=6	EEP (Prune Only)	59.6	71.4
	EEP (Prune+Merge)	61.8	71.3
	Random	45.1±6.1	50.3±10.7
	Frequency [37]	26.6	25.2
	Soft Activation [37]	46.7	53.1
Num=4	NAEE [34]	53.5	63.6
	EEP (Prune Only)	55.4	62.4
	EEP (Prune+Merge)	56.9	64.6
	Random	45.1±6.1	50.3±10.7
	Frequency [37]	26.6	25.2

Expert merging improves the performance of the pruned model



# Efficient Techniques for LLMs

**Overall Cost**  
(for each request)

**Total Latency:**  $t_{prefill} + t_{decode} * N_{token}$   
**Total Memory:**  $M_{weight} + M_{kv\ cache} + M_{other\_act}$

**SoT**  
(Skeleton-of-Thought)

**Total Latency:**  $t_{prefill} + t_{decode} * N_{token} / B$   
**Total Memory:**  $M_{weight} + M_{kv\ cache} + M_{other\_act}$

**LLM-MQ**  
(Mixed-precision quantization)

**Total Latency:**  $t_{prefill} + t_{decode} \downarrow * N_{token}$   
**Total Memory:**  $M_{weight} \downarrow + M_{kv\ cache} + M_{other\_act}$

**MoA**  
(Mixture of Attention)

**Total Latency:**  $t_{prefill} + t_{decode} \downarrow * N_{token}$   
**Total Memory:**  $M_{weight} + M_{kv\ cache} \downarrow + M_{other\_act}$

**EEP**  
(Efficient Expert Pruning)

**Total Latency:**  $t_{prefill} + t_{decode} \downarrow * N_{token}$   
**Total Memory:**  $M_{weight} \downarrow + M_{kv\ cache} + M_{other\_act}$



## 目录 Contents

1

Background

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Large Language Models (LLMs)

3

Diffusion Models

4

Research Summary

# How DMs do inference

- Forward Process: Gradually add gaussian noise of different levels
- Backward Process: Gradually denoise the gaussian noise
- Intuition: the NN learns to predict the “noise” at each timestep.

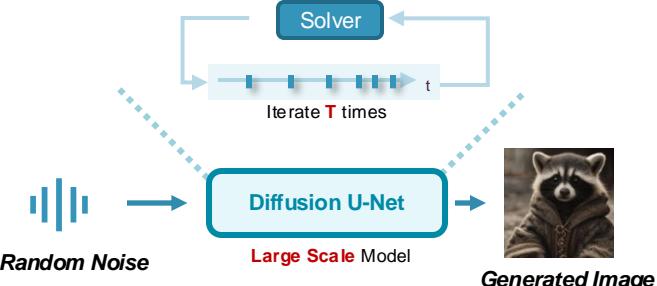


[1] Ho, Jonathan et al. "Denoising Diffusion Probabilistic Models." ArXiv 2020.

# Efficiency Analysis of DM Inference

## Current visual generation faces efficiency challenge

**Large Param Size: 2.5B (SDXL)**  
**Iterative NN Inference: 10-100x**



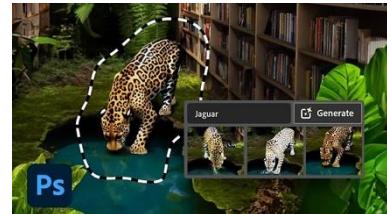
### Diffusion Model

Current SOTA  
visual generation scheme

### Latency Challenge:



Cannot Satisfy



SDXL 50 steps  
on RTX3090: **30 s**

Image Editing  
Needs **Fast (<1s)** Feedback

### Memory Challenge:



Cannot Fit In



SDXL model  
**9.7GB GPU Memory**

Desktop GPU: RTX4070  
**8GB GPU Memory**



# Efficient Techniques for DMs

## Directions to improve Diffusion Models' efficiency



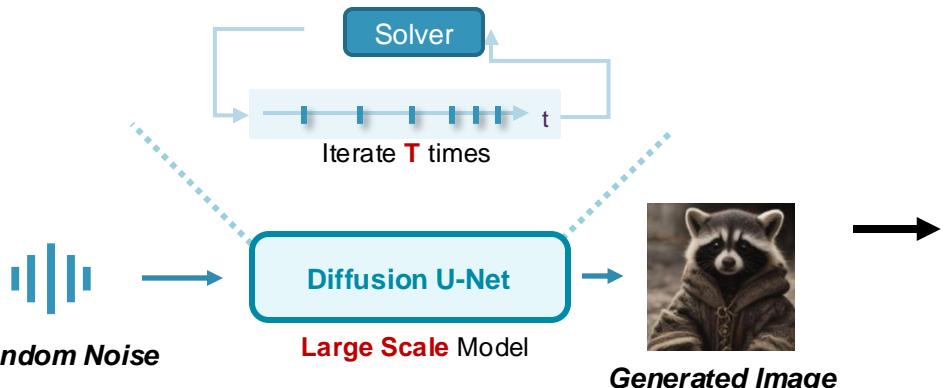
### Algorithm-level

Reduce  $N_{timestep}$



### Model-level

Reduce  $t_{model}$ ,  
 $M_{weight}, M_{act}$



**Total Latency:**  $t_{model} * N_{timestep}$   
**Total Memory:**  $M_{weight} + M_{activation}$

# Overview of Efficient Techniques

We improve diffusion model's efficiency from **Algorithm & Model & Data** level

Latency Challenge:



Cannot Satisfy



SDXL 50 steps  
on RTX3090: **30 s**

Memory Challenge:



Cannot Fit In



SDXL model  
**9.7GB GPU Memory**

Image Editing  
Needs **Fast (<1s)** Feedback

Desktop GPU: RTX4070  
**8GB GPU Memory**

## Algorithm-level Time Step Compression

**LCSC**

[ICLR Submission]

Linear combination of checkpoints.

**15~23x** training acceleration,  
**1.25~2x** timestep compression

**USF**

[ICLR'24]

**OMS-DPM**

[ICML'23]

**DD**

[ICLR Submission]

Search for optimal  
diffusion schedulers.

**1.5~2x speed-up**

generates image in **0.01s**  
and can achieve **>100x**  
speedup for Image AR model

Fast  
Compression

**FlashEval**  
[CVPR'24]

## Model-level Quantization

**MixDQ**

[ECCV'24]

Mixed-precision quantization.  
**3x memory decrease,**  
**1.5x speed-up**

**ViDiT-Q**

[ICLR Submission]

Quantization for DiT.  
**2.5x memory improvement,**  
**1.5x speed-up**

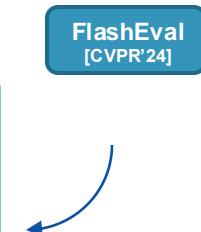
## Pruning & Sparse Attention

**DiTFastAttn**

[NeurIPS'24]

Window & reused attention for DiT.  
**1.6x speed-up**

## Efficient Diffusion Models

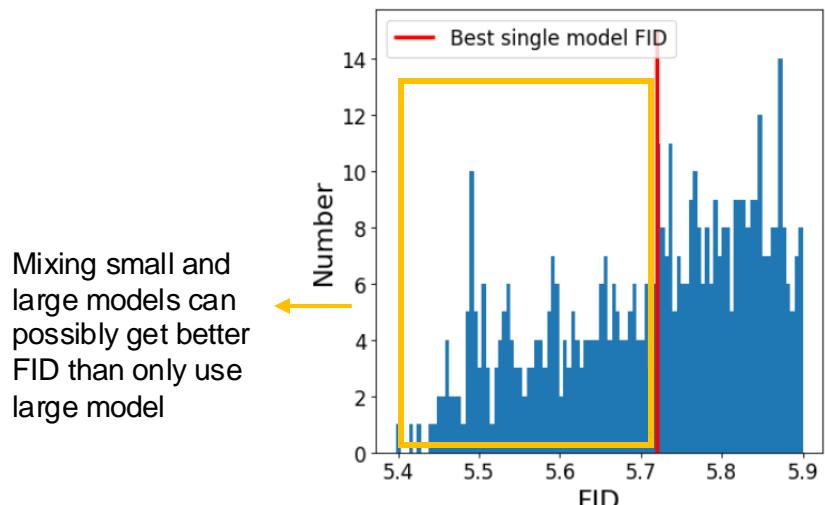


# Optimizing the Model Schedule (OMS-DPM)

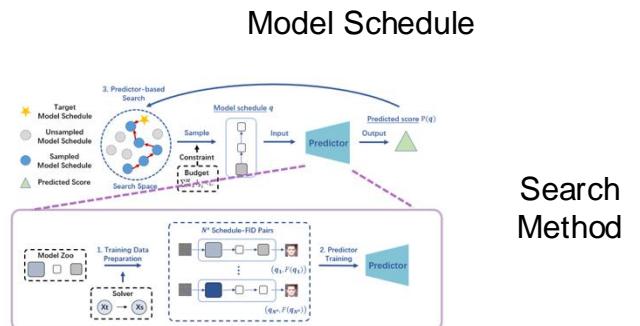
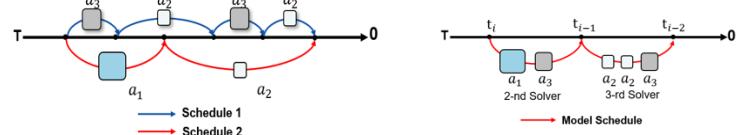


Achieving **2-5× speed-up** on typical datasets and **2× speed-up** on Text-to-Image generation

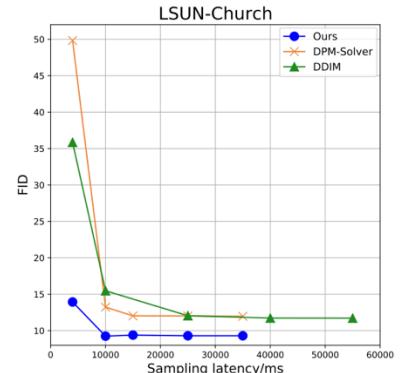
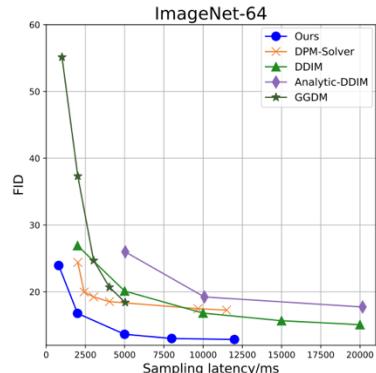
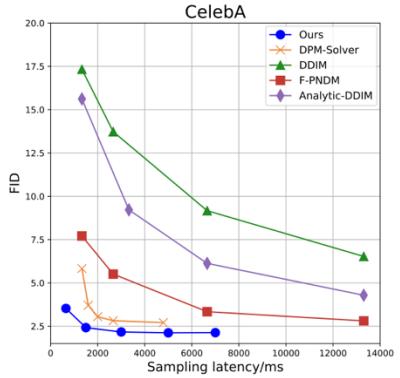
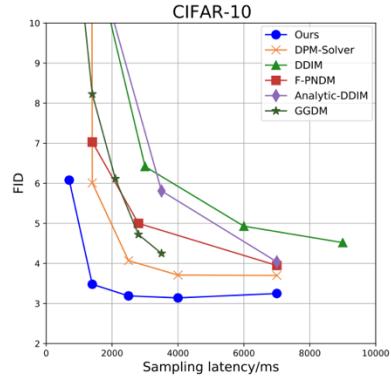
**Motivation:** Small models outperform large models at some timesteps



**Methodology:** Model Schedule & Predictor-based Search



# Optimizing the Model Schedule (OMS-DPM)



Baseline



Ours

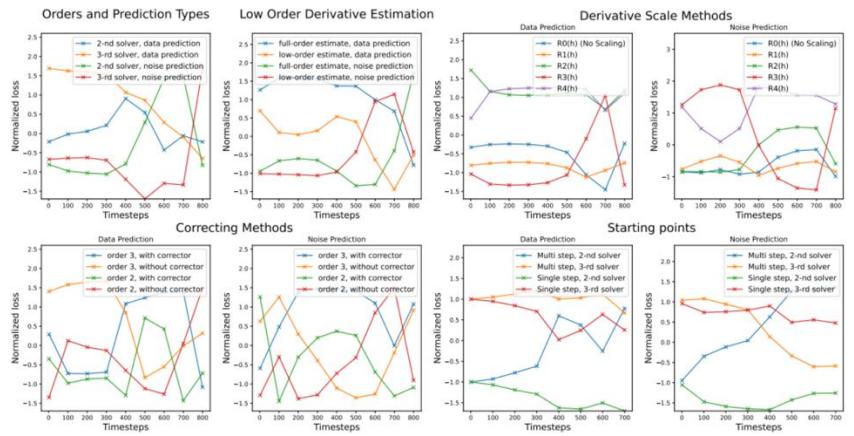
# Unified Sampling Framework (USF)

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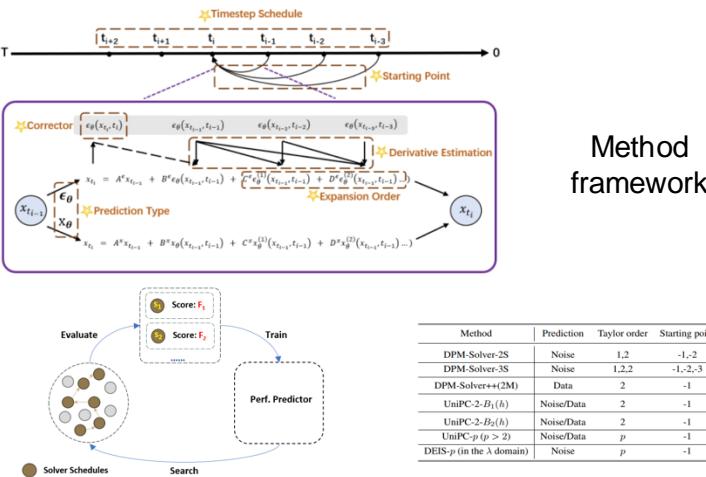
Achieving  **$2\times$  speed-up** on Text-to-Image generation and enables **sampling with very low NFE**

**Motivation:** Current solvers use sub-optimal strategies, cause poor quality with few NFE



The ranking of all strategies changes over timestep

**Methodology:** A framework that unifies all existing solvers and search based on it.



Method	Prediction	Taylor order	Starting point	Scale	Corrector
DPM-Solver-2S	Noise	1,2	-1,2	$\frac{b}{c} \epsilon_B^{(k-1)}$	None
DPM-Solver-3S	Noise	1,2,2	-1,2,3	None	None
DPM-Solver++(2M)	Data	2	-1	$\frac{b}{c-k} \epsilon_B^{(k-1)}$	None
UniPC-2-B1(h)	Noise/Data	2	-1	$\frac{b}{c} \epsilon_B^{(k-1)}$	UniC-2
UniPC-2-B2(h)	Noise/Data	2	-1	$\frac{b}{c} \epsilon_B^{(k-1)}$	UniC-2
UniPC-(p > 2)	Noise/Data	p	-1	$\frac{b}{c-p} \epsilon_B^{(p-1)}$	None
DEIS-p (in the $\lambda$ domain)	Noise	p	-1	None	UniC-p

USF unifies all solvers

# Unified Sampling Framework (USF)

**INFINIGENCE**  
无向芯查

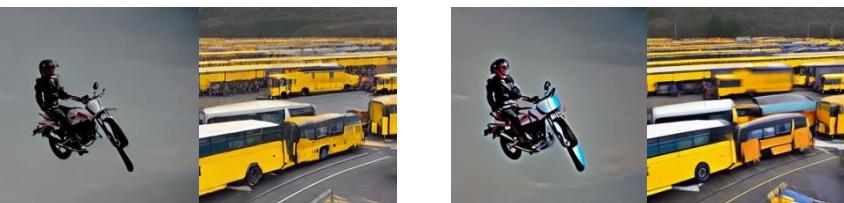


Dataset	Method	NFE						
		4	5	6	7	8	9	10
<b>CIFAR-10</b>	Baseline-W(S)	255.21	288.12	32.15	14.79	22.99	6.41	5.97
	Baseline-W(M)	61.13	33.85	20.84	13.89	10.34	7.98	6.76
	Baseline-B	57.52	23.44	10.33	6.47	5.16	4.30	3.90
	Ours	<b>11.50</b>	<b>6.86</b>	<b>5.18</b>	<b>3.81</b>	<b>3.41</b>	<b>3.02</b>	<b>2.69</b>
<b>CelebA</b>	Baseline-W(S)	321.39	330.10	52.04	17.28	16.99	10.39	6.91
	Baseline-W(M)	31.27	20.37	14.18	11.16	9.28	8.00	7.11
	Baseline-B	26.32	8.38	6.72	6.72	5.17	4.21	4.02
	Ours	<b>12.31</b>	<b>5.17</b>	<b>3.65</b>	<b>3.80</b>	<b>3.62</b>	<b>3.16</b>	<b>2.73</b>
<b>ImageNet-64</b>	Baseline-W(S)	364.60	366.66	72.47	47.84	54.21	28.22	27.99
	Baseline-W(M)	93.98	69.08	50.35	40.99	34.80	30.56	27.96
	Baseline-B	76.69	61.73	42.81	31.76	26.99	23.89	24.23
	Ours	<b>33.84</b>	<b>24.95</b>	<b>22.31</b>	<b>19.55</b>	<b>19.19</b>	<b>19.09</b>	<b>16.68</b>
<b>LSUN-Bedroom</b>	Baseline-W(M)	44.29	24.33	15.96	12.41	10.87	9.99	8.89
	Baseline-B	22.02	17.99	12.43	10.79	9.92	9.11	8.52
	Ours	<b>16.45</b>	<b>12.98</b>	<b>8.97</b>	<b>6.90</b>	<b>5.55</b>	<b>3.86</b>	<b>3.76</b>
<b>ImageNet-128</b>	Baseline-W(M)	32.08	15.39	10.08	8.37	7.50	7.06	6.80
	Baseline-B	25.77	13.16	8.89	7.13	6.28	6.06	6.03
	Ours	<b>18.61</b>	<b>8.93</b>	<b>6.68</b>	<b>5.71</b>	<b>5.28</b>	<b>4.81</b>	<b>4.69</b>
<b>ImageNet-256</b>	Baseline-W(M)	80.46	54.00	38.67	29.35	22.06	16.74	13.66
	Baseline-B	51.09	27.71	17.62	13.19	10.91	9.85	9.31
	Ours	<b>33.84</b>	<b>19.06</b>	<b>13.00</b>	<b>10.31</b>	<b>9.72</b>	<b>9.06</b>	<b>9.06</b>

Results on typical datasets

Method	NFE						
	4	5	6	7	8	9	10
Baseline-W(S)	161.03	156.72	106.15	75.28	58.54	39.26	29.54
Baseline-W(M)	30.77	22.71	19.66	18.45	18.00	17.65	17.54
Baseline-B	24.95	20.59	18.80	17.83	17.54	17.42	17.22
Ours	<b>22.76</b>	<b>16.84</b>	<b>15.76</b>	<b>14.77</b>	<b>14.23</b>	<b>13.99</b>	<b>14.01</b>
Ours-500	24.47	17.72	15.71	<b>14.60</b>	14.47	14.15	14.27
Ours-250	23.84	18.27	17.29	14.90	15.50	14.12	14.31

Results on T2I task



Ours

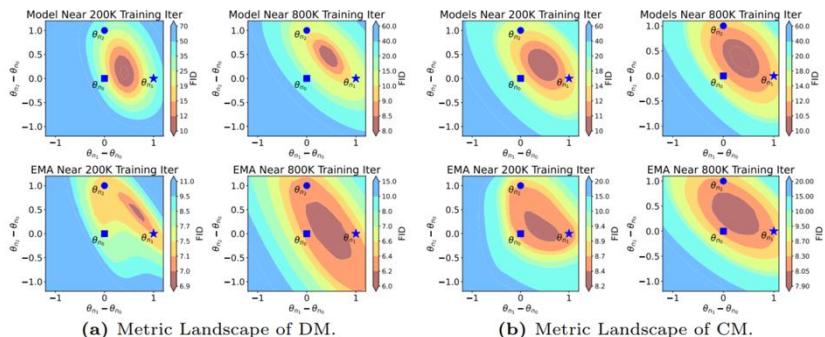
Baseline

# Linear Combination of Saved Checkpoints (LCSC)

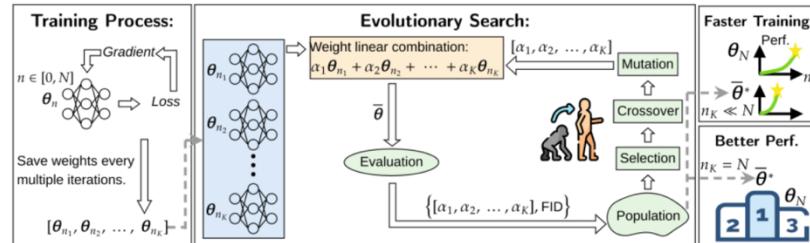


Achieving **15~23× training speed-up on Consistency Models**  
and **1.25~1.7× inference speed-up on Diffusion Models**

**Motivation:** Combination of checkpoints can improve the performance of CM/DM.



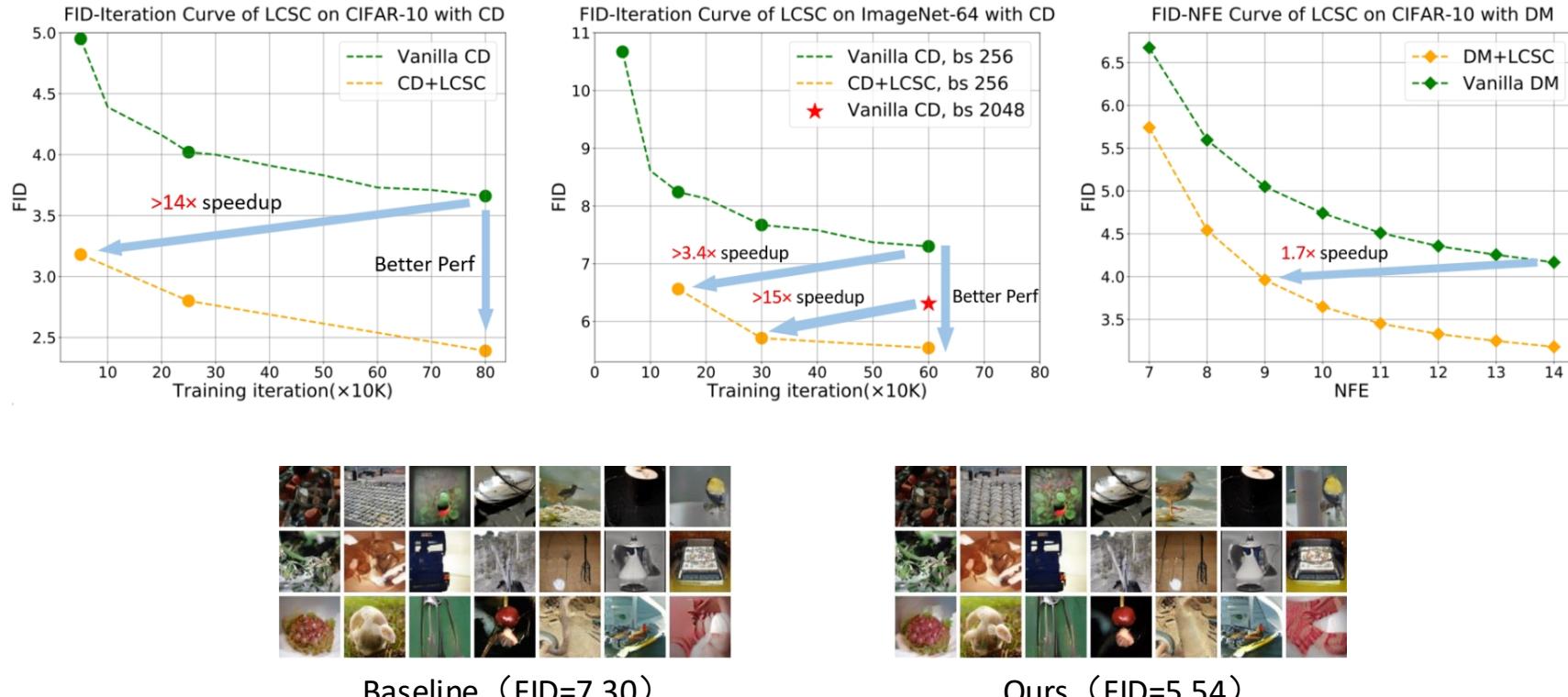
**Methodology:** Search the combination coefficients of saved checkpoints



**Use Case:** accelerate training & enhancing converged models

[1] Liu, Enshu, ..., Ning, Xuefei, et al. "Linear Combination of Saved Checkpoints Makes Consistency and Diffusion Models Better." ICLR 2025 Submission.

# Linear Combination of Saved Checkpoints (LCSC)



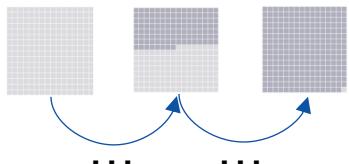
[1] Liu, Enshu, ..., Ning, Xuefei, et al. "Linear Combination of Saved Checkpoints Makes Consistency and Diffusion Models Better." ICLR 2025 Submission.

# Distilled Decoding of Image AR model (DD)



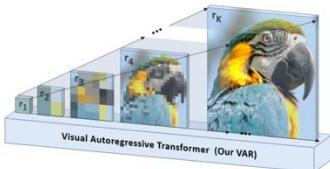
**Motivation1:** Auto-regressive (AR) image generation model takes too many steps to generate

LlamaGen<sup>[2]</sup>



>200 steps  
>5s/img

VAR<sup>[3]</sup>



10 steps  
~0.13s/img

**Motivation2:** Typical solution don't work: modeling the distribution of multiple steps simultaneously

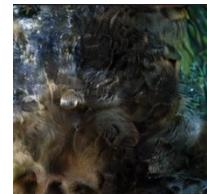


- Known/predicted
- to predict at this step

Ignore the **correlation** and introduce the gap between

$$\prod_{i=k+1}^m p(q_i|q_k, \dots, q_1) \& p(q_m, \dots, q_{k+1}|q_k, \dots, q_1)$$

1step generation



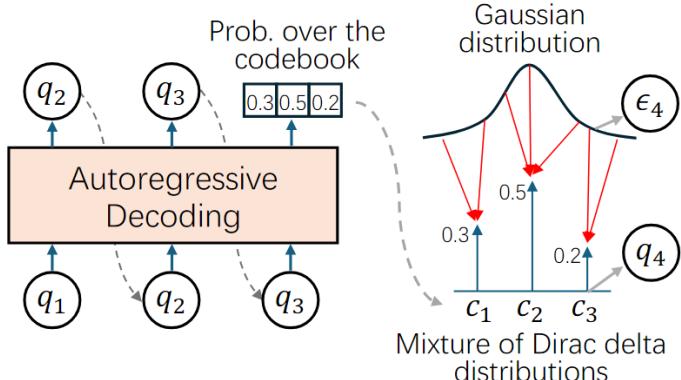
[1] Liu, Enshu, Ning Xuefei, et al. "Distilling Autoregressive Models Into Few Steps 1: Image Generation." ICLR 2025 Submission.  
[2] Sun, Peize, et al. "Autoregressive Model Beats Diffusion: Llama for Scalable Image Generation." Arxiv 2024  
[3] Tian, Keyu, et al. "Visual Autoregressive Modeling: Scalable Image Generation via Next-Scale Prediction." NeurIPS 2024.

# Distilled Decoding of Image AR model (DD)

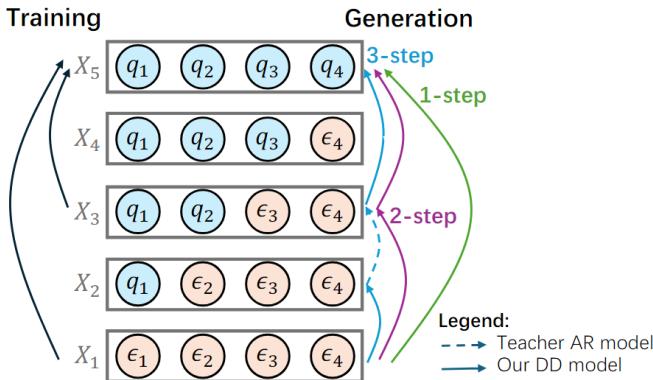
## Methodology:

- Introduce **noise token** and **flow-matching** to construct an auto-regressive trajectory
- Train the model to skip further along the trajectory

### 1. Construct the trajectory



### 2. Training & Sampling



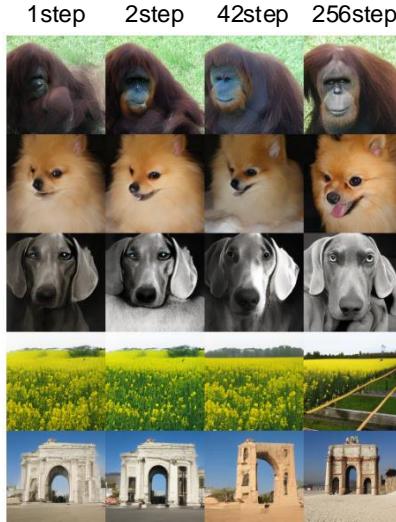
[1] Liu, Enshu, Ning Xuefei, et al. "Distilling Autoregressive Models Into Few Steps 1: Image Generation." ICLR 2025 Submission.

# Distilled Decoding of Image AR model (DD)



**Results:** DD generates image in **0.01s** and can achieve **>100x speedup** for Image AR model with acceptable performance loss.

Type	Model	FID↓	IS↑	Pre↑	Rec↑	#Para	#Step	Time
AR	VAR (Tian et al., 2024)	4.19	230.2	0.84	0.53	310M	10	0.133
AR	LlamaGen (Sun et al., 2024)	6.53	291.8	0.86	0.42	343M	256	5.01
Baseline	VAR- <i>skip-1</i>	9.52	178.9	0.68	0.54	310M	9	0.113
Baseline	VAR- <i>skip-2</i>	40.09	56.8	0.46	0.50	310M	8	0.098
Baseline	VAR- <i>onestep</i> *	157.5	—	—	—	—	1	—
Baseline	LlamaGen- <i>skip-106</i>	19.14	80.39	0.42	0.43	343M	150	2.94
Baseline	LlamaGen- <i>skip-156</i>	80.72	12.13	0.17	0.20	343M	100	1.95
Baseline	LlamaGen- <i>onestep</i> *	220.2	—	—	—	—	1	—
Ours	VAR-DD	7.86	185.1	0.80	0.41	327M	<b>1</b>	<b>0.021</b> (6.3×)
Ours	VAR-DD	10.65	168.1	0.79	0.37	327M	2	0.036 (3.7×)
Ours	LlamaGen-DD	17.98	179.6	0.79	0.20	326M	<b>1</b>	<b>0.023 (217.8×)</b>
Ours	LlamaGen-DD	11.24	235.1	0.85	0.30	326M	2	0.043 (116.5×)
Ours	VAR- <i>pre-trained-1-6</i>	5.90	241.3	0.85	0.40	327M	6	0.090 (1.5×)
Ours	VAR- <i>pre-trained-4-6</i>	6.10	229.5	0.85	0.39	327M	4	0.062 (2.1×)
Ours	VAR- <i>pre-trained-5-6</i>	6.62	208.5	0.83	0.40	327M	3	<b>0.045</b> (2.6×)
Ours	LlamaGen- <i>pre-trained-1-81</i>	10.30	271.2	0.88	0.35	326M	81	1.725 (2.9×)
Ours	LlamaGen- <i>pre-trained-41-81</i>	10.43	266.2	0.88	0.33	326M	42	0.880 (5.7×)
Ours	LlamaGen- <i>pre-trained-61-81</i>	10.62	255.4	0.87	0.31	326M	22	0.447 ( <b>11.2×</b> )



[1] Liu, Enshu, Ning Xuefei, et al. "Distilling Autoregressive Models Into Few Steps 1: Image Generation." ICLR 2025 Submission.

# Motivation: Diffusion Quantization

The text-to-image/video diffusion models are **memory-intensive**,  
and **cannot** be deployed on **Edge** Devices (Even Desktop GPU)



OPEN SORA

Open-SORA 2s Video    FP16 SDXL 512x512  
~10 GB Peak Memory    **9.7GB Peak Memory**



Desktop GPU: RTX4070    Mobile: iPhone 14  
**8GB GPU Memory**    **6GB Memory**



**Solution: Model Quantization**, **low-bit data** storing and  
computing, reduce the memory cost

# Mixed-precision Quantization (MixDQ)

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**Motivation:** Few-step text-to-image diffusion models face **additional challenge** for quantization

FP16



"Two sheep are standing side by side behind a fence."

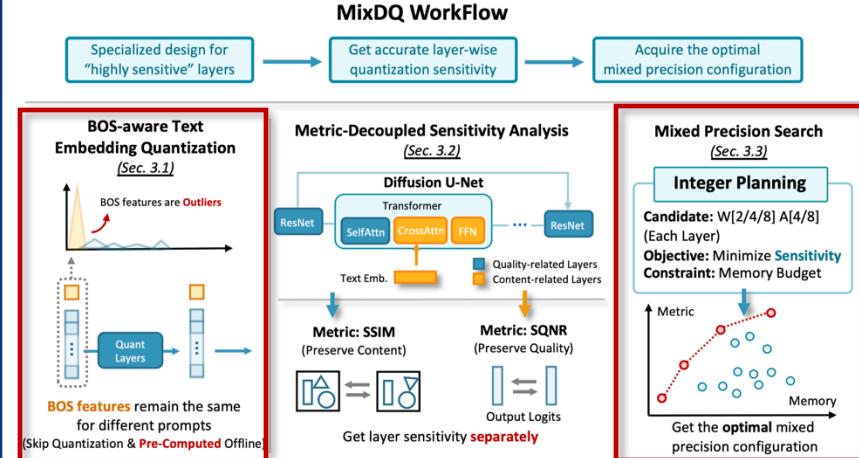
(\* Adopting Q-Diffusion for 1-step SDXL-turbo model)

Q-Diff (W8A8)



## Solutions:

- BOS-aware Quantization Technique  
*"Address Outliers in Text Embeddings"*
- Mixed-precision Bit-width Allocation  
*"Address over-sensitive layers"*



[1] Zhao, Tianchen, Ning, Xuefei, et al. "MixDQ: Memory-Efficient Few-Step Text-to-Image Diffusion Models with Metric-Decoupled Mixed Precision Quantization." ECCV 2024.

# Mixed-precision Quantization (MixDQ)

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无问芯答



**Motivation:** Quantization affects both the **image quality & content**



FP



Content **Changed**  
Quality **Retained**

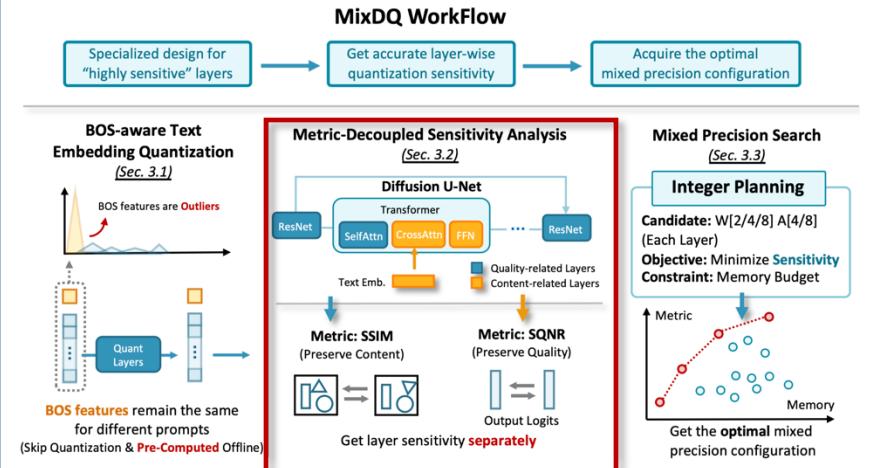


Content **Retained**  
Quality **Changed**

"A bicycle replica with a clock as the front wheel."

**Solution:**

- "Metric-decoupled" analysis and mixed precision



[1] Zhao, Tianchen, Ning, Xuefei, et al. "MixDQ: Memory-Efficient Few-Step Text-to-Image Diffusion Models with Metric-Decoupled Mixed Precision Quantization." ECCV 2024.

# Mixed-precision Quantization (MixDQ)

**Experimental Results:** MixDQ improves **both image quality & text alignment**  
Achieves W4A8 with negligible loss(+0.5 FID), while baseline methods fail at W8A8 (+50 FID)

Model	Method	Bit-width (W/A)	Storage Opt.	Compute Opt.	FID( $\downarrow$ )	CLIP( $\uparrow$ )	IR( $\uparrow$ )
SDXL-turbo (1 step)	FP	16/16	-	-	17.15	0.2722	0.8631
	Naive PTQ	8/16	2x	1x	16.89	0.2740	0.8550
		4/16	4x	1x	301.49	0.1581	-2.2526
		8/8	2x	4x	103.96	0.1478	-1.7446
		4/8	4x	8x	358.894	0.1242	-2.2815
	Q-Diffusion	8/16	2x	1x	16.97	0.2735	0.8588
		4/16	4x	1x	22.58	0.2685	0.6847
		8/8	2x	4x	76.18	0.1772	-1.3112
		4/8	4x	8x	118.93	0.1662	-1.6353
	MixDQ(Ours)	4/16	4x	1x	17.23	0.2693	0.8254
LCM-lora (4 steps)		3.66/16	4.4x	1x	17.40	0.2682	0.7528
		8/8	2x	4x	17.03	0.2703	0.8415
		5/8	3.2x	8x	17.23	0.2697	0.8307
		4/8	4x	8x	17.68	0.2698	0.7822
	FP	16/16	-	-	25.56	0.2570	0.2122
	Naive PTQ	8/8	2x	4x	23.36	0.2548	0.0517
		4/8	4x	8x	87.36	0.2055	-1.6160
MixDQ(Ours)	Q-Diffusion	8/8	2x	4x	23.92	0.2561	0.1875
		4/8	4x	8x	57.73	0.2280	-1.1863
		8/8	2x	4x	22.54	0.2552	0.1573
		4/8	4x	8x	33.48	0.2403	-0.6732

**FP16**      **Baseline  
(W8A8)**      **MixDQ  
(W8A8)**      **MixDQ  
(W4A8)**



*"A room with blue walls and a white sink and door."*



*"A cute kitten is sitting in a dishon a table."*

# Mixed-precision Quantization (MixDQ)

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Practical  $1.45\times$  speed-up and  $2\times$  memory saving on Nvidia GPU  
Open-source tool that achieves speedup and support few-step models



FP16



MixDQ W8A8

2x U-Net Memory Opt.: 4.8 GB -> 2.4 GB

1.45x U-Net Latency Opt.: 36.1 ms -> 24.9 ms

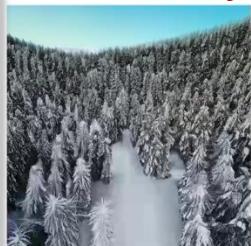
	Reduce VRAM	No Visual Degradation	Latency Speedup	Open Source	Support Few-step
Stable Diffusion WebUI - FP8	✓	✗	✗	✓	✗
Huggingface DiffusionFast: Dynamic Quantization	✓	✓	✗	✓	✗
Nvidia TensorRT: Q-Diffusion PTQ	✓	✓	✓	✗	✗
Ours: MixDQ	✓	✓	✓	✓	✓

[1] Zhao, Tianchen, Ning, Xuefei, et al. "MixDQ: Memory-Efficient Few-Step Text-to-Image Diffusion Models with Metric-Decoupled Mixed Precision Quantization." ECCV 2024.

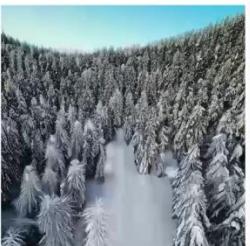
# ► Video and Image DiT Quantization (ViDiT-Q)

## DiT Quantization for Image and Video Generation

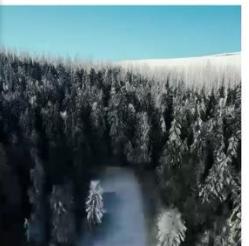
*Nearly Identical*



FP16



ViDiT-Q W8A8



Baseline W8A8

*Blurred and Blank*



FP16



Baseline W8A8



ViDiT-Q W8A8



FP16



Baseline W8A8



Baseline W4A8: "Blank Frames"

Multiple Fins

Blank Images



Baseline W4A8



ViDiT-Q W4A8

# ► Video and Image DiT Quantization (ViDiT-Q)

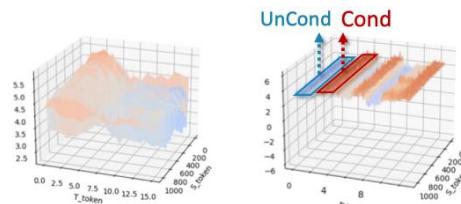
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无向芯宣



**Motivation:** DiT (Diffusion Transformers) have **unique properties** for quantization  
**Solution:** Quantization scheme tailored for DiTs

## Unique challenges for quantizing DiT

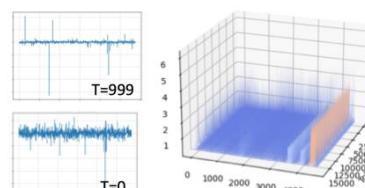
- “highly variant activation along different levels”
- “Time-varying” Channel Imbalance



Token-wise Variation

CFG-wise Variation

Timestep-wise Variation



Time-varying Channel-wise Variation

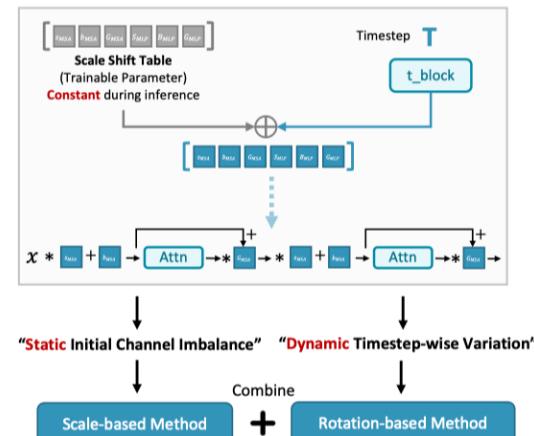
Fine-grained grouping and Dynamic Quantization (Sec 4.1)

Static-Dynamic Channel Balance (Sec 4.2)

**Static-Dynamic Channel Balance**

- Combine the advantage of current **scale-based (AWQ)** and **rotation-based (Quarot)** channel balancing methods

## Static-Dynamic Channel Balance (Sec. 4.2)





# Video and Image DiT Quantization (ViDiT-Q)

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**Motivation:** Video generation task have **unique properties** for quantization

**Solution:** Quantization scheme tailored for visual generation task

Quantization has effects on **multiple aspects** of visual generation

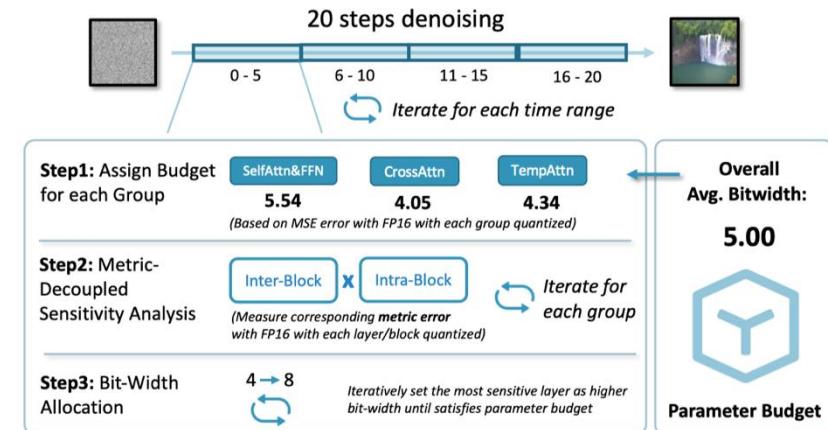
T *Text Alignment*

 *Visual Quality (Fidelity)*

 *Time Consistency*

Decouple the quantization's effect on multiple aspects  
To preserve performance for multiple aspects

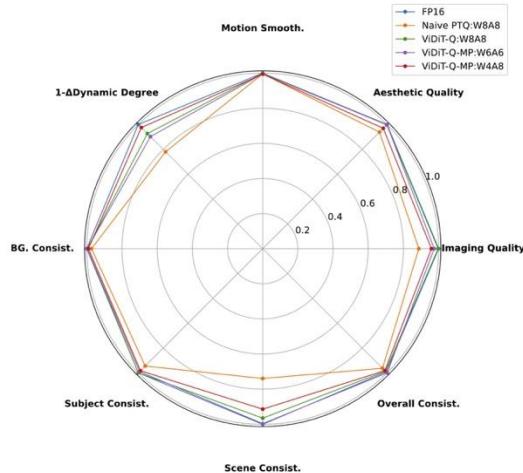
## Metric-Decoupled Mixed Precision (Sec. 4.3)



# ► Video and Image DiT Quantization (ViDiT-Q)

Achieve superior performance for multiple aspects

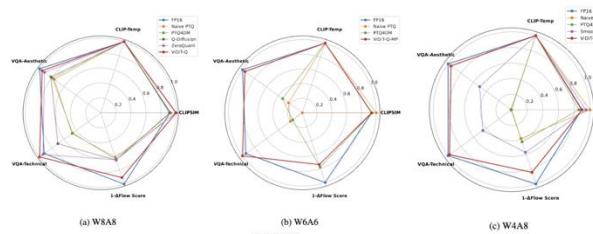
## Comprehensive Benchmark



Similar performance with FP16

## Multiple Metrics

Method	Bit-width (W/A)	CLIPSIM	CLIP-Temp	VQA-Aesthetic	VQA-Technical	Δ Flow Score. (↓)
-	16/16	0.1797	0.9988	63.40	50.46	-
Q-Diffusion	8/8	0.1781	0.9987	51.68	38.27	0.328
Q-DiT	8/8	0.1788	0.9977	61.03	34.97	0.473
PTQ4DiT	8/8	0.1836	0.9991	54.56	53.33	0.440
SmoothQuant	8/8	0.1951	0.9986	59.78	51.53	0.331
Quarot	8/8	0.1949	0.9976	58.73	52.28	0.215
ViDiT-Q	8/8	0.1950	0.9991	60.70	54.64	0.089
Q-DiT	6/6	0.1710	0.9943	11.04	1.869	41.10
PTQ4DiT	6/6	0.1799	0.9976	59.97	43.89	0.997
SmoothQuant	6/6	0.1807	0.9985	56.45	48.21	29.26
Quarot	6/6	0.1820	0.9975	61.47	53.06	0.146
ViDiT-Q	6/6	0.1791	0.9984	64.45	51.58	0.625
Q-DiT	4/8	0.1687	0.9833	0.007	0.018	3.013
PTQ4DiT	4/8	0.1735	0.9973	2.210	0.318	0.108
SmoothQuant	4/8	0.1832	0.9983	31.96	22.85	0.415
Quarot	4/8	0.1817	0.9965	47.36	33.13	0.326
ViDiT-Q	4/8	0.1809	0.9989	60.62	49.38	0.153



Outperform baseline quantization methods

## Qualitative Examples

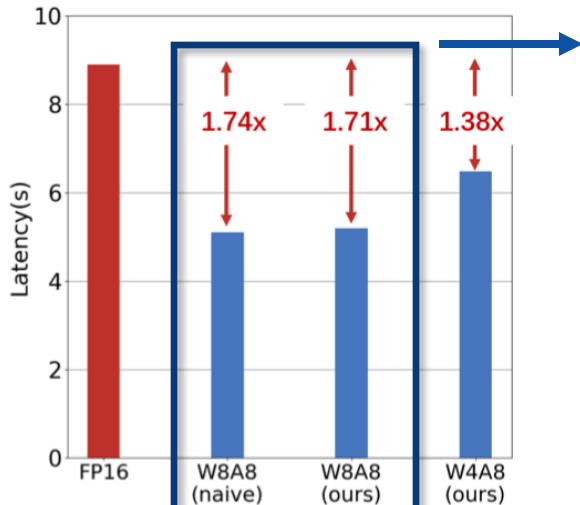
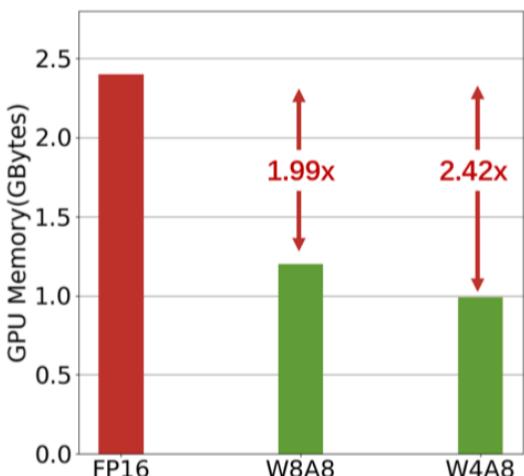


# ► Video and Image DiT Quantization (ViDiT-Q)

Achieve Efficiency Improvement with CUDA kernels

## Practical Hardware Resource Savings:

- **W8A8:** 1.99x Memory, 1.71x Latency Speedup
- **W4A8:** 2.42x Memory, 1.38x Latency Speedup



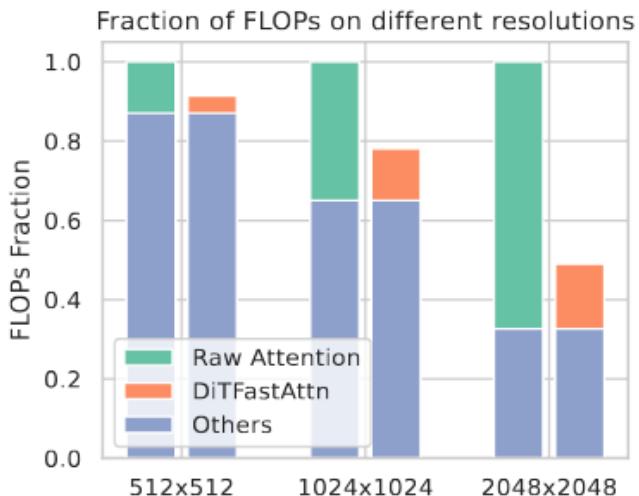
ViDiT-Q improved quantization technique introduces negligible overhead while improving performance, It achieves similar speedup compared with naïve quantization scheme

# Attention Compression (DiTFastAttn)

Reduces up to **76%** of the attention FLOPs.

Achieve up to **1.8x** speedup of DiT models on 2Kx2K generation.

Support both image generation and video generation.



Motivation:

1. Attention can be computationally costly, especially when processing a large number of tokens, as is the case in high-resolution image generation and long-form video generation tasks.
2. Diffusion Transformer (DiT) are emerging as a popular model for image and video generation tasks. There are some redundant computations involved in the generation process when using DiT models.

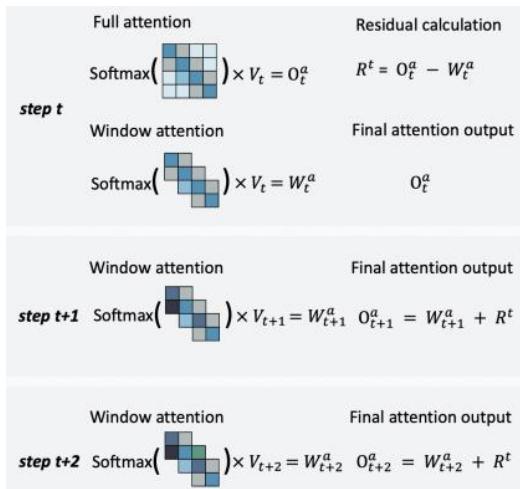
[1] Yuan Zhihang, ..., Ning Xuefei et al. "DiTFastAttn: Attention Compression for Diffusion Transformer Models". NeurIPS 2024.

# Attention Compression (DiTFastAttn)



## Method 1: Window attention with residual share

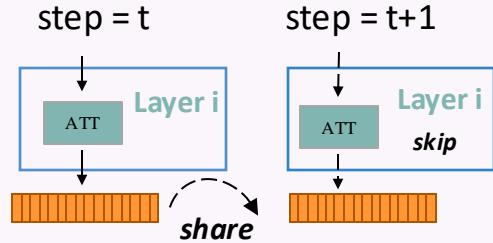
The changes in the attention output across different timesteps are primarily driven by a local attention window.



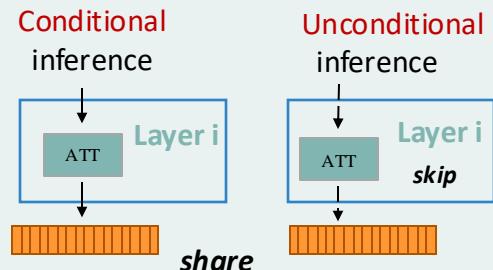
In each timestep, we only compute the local attention and then add the residual of previous global attention, without the need to recompute the full global attention.

## Method 2 & Method 3: Attention sharing across steps & CFG

### Attention Sharing across Step



### Attention Sharing across CFG



[1] Yuan Zhihang, ..., Ning Xuefei et al. "DiTFastAttn: Attention Compression for Diffusion Transformer Models". NeurIPS 2024.

# Attention Compression (DiTFastAttn)



Apply to  
2K Image  
Generation  
Experiments on  
PixArt-Sigma



Apply to Video Generation  
Experiments on OpenSORA

Raw



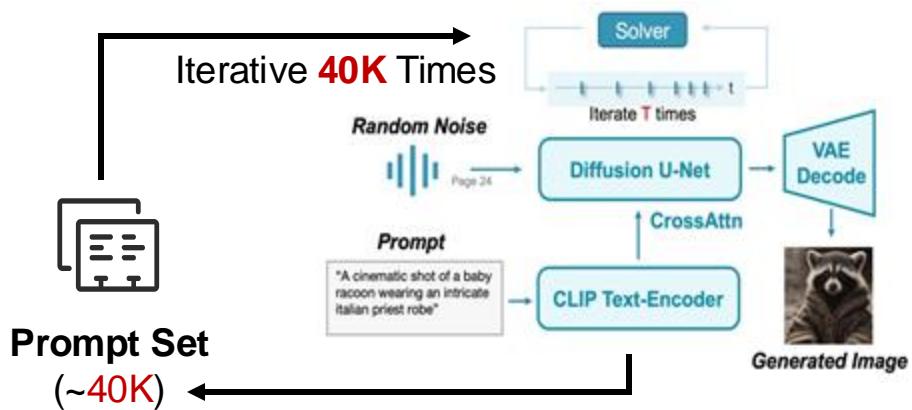
DiTFastAttn



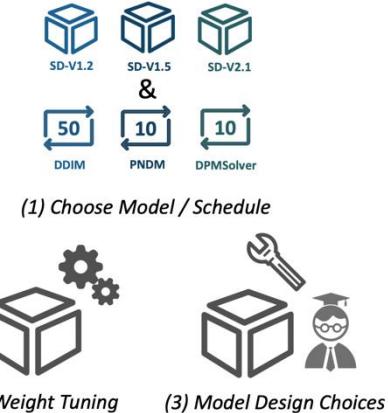
[1] Yuan Zhihang, ..., Ning Xuefei et al. "DiTFastAttn: Attention Compression for Diffusion Transformer Models". NeurIPS 2024.

# Faster Evaluation (FlashEval)

**Motivation:** Text-to-image Diffusion Evaluation is **slow**,  
Many applications require repeated evaluation



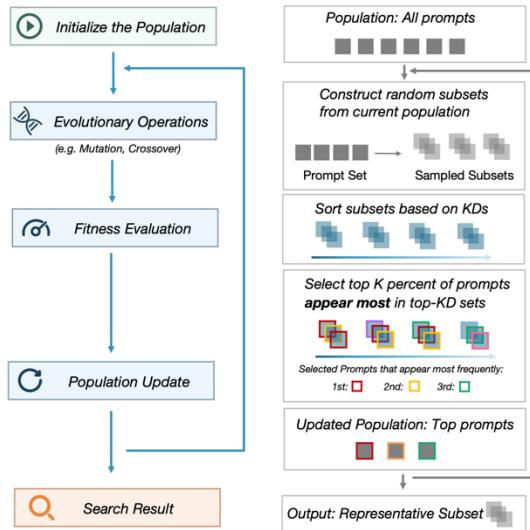
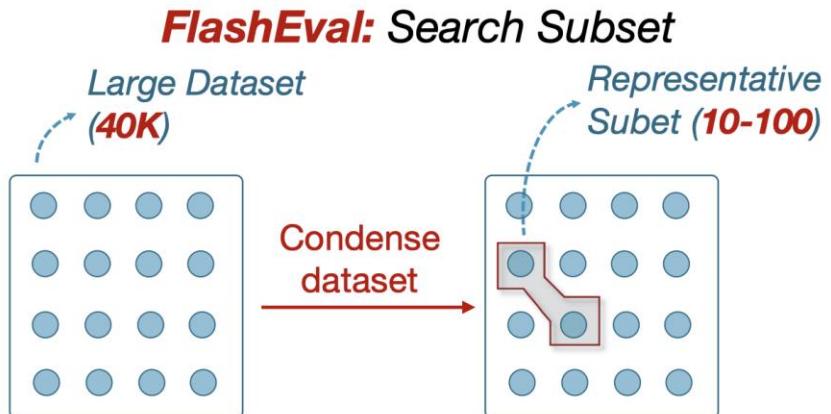
Evaluating SD v1.5 50 steps on complete COCO cost **~50 GPU hours**



Many applications require **Repeated Evaluation**

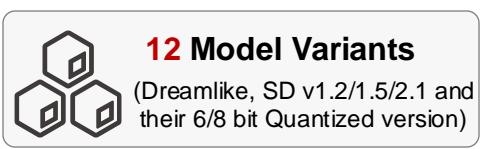
# Faster Evaluation (FlashEval)

**Methodology:** Find “Representative Subset”, by Evolutionary-inspired searching



[1] Zhao, Lin, ... Ning, Xuefei, et al. "FlashEval: Towards Fast and Accurate Evaluation of Text-to-image Diffusion Generative Models." CVPR 2024.

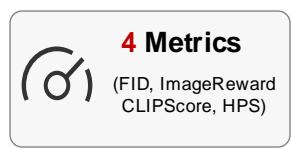
# Faster Evaluation (FlashEval)

12 Model Variants

(Dreamlike, SD v1.2/1.5/2.1 and their 6/8 bit Quantized version)

8 Schedules

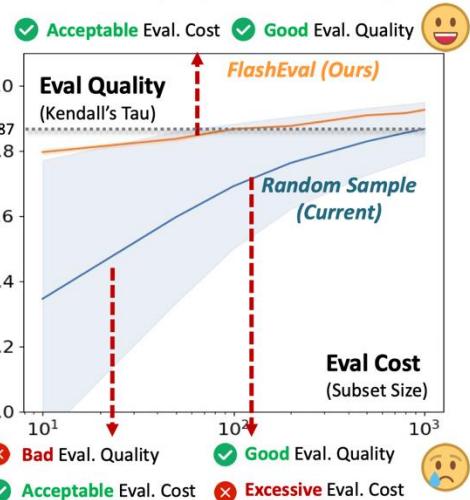
(DDIM, DPM Solver, PNND  
10/20/50 Steps)

4 Metrics

(FID, ImageReward  
CLIPScore, HPS)

Diverse Evaluation Settings

models	item size	N'=50			N'=500		
		methods \ sub-tasks	random	model variants	schedulers	random	model variants
Train	RS	0.607±0.000	0.594±0.000	0.632±0.000	0.857±0.000	0.858±0.000	0.857±0.000
	B3-prompt	0.900	0.909	0.862	0.895	0.917	0.872
	B3-set	0.895±0.002	0.912±0.002	0.894±0.002	0.971±0.002	0.970±0.001	0.966±0.002
	Ours	<b>0.956±0.004</b>	<b>0.969±0.004</b>	<b>0.960±0.004</b>	<b>0.984±0.003</b>	<b>0.986±0.003</b>	<b>0.978±0.003</b>
Test	RS	0.597±0.000	0.588±0.000	0.560±0.000	0.829±0.000	0.826±0.000	0.827±0.000
	B3-prompt	0.729	0.784	0.810	0.805	0.822	0.851
	B3-set	0.750±0.014	0.680±0.021	0.721±0.013	0.875±0.007	0.836±0.008	0.863±0.008
	Ours	<b>0.851±0.004</b>	<b>0.800±0.008</b>	<b>0.850±0.005</b>	<b>0.906±0.003</b>	<b>0.899±0.004</b>	<b>0.909±0.003</b>



Our Searched **50-item** Subset have comparable evaluation quality with **Random-sampled 500**

Better Evaluation Eff-Perf Trade-off



# Efficient Techniques for DMs

## Overall Cost

(for each iter)

**Total Latency:**  $t_{model} * N_{timestep}$

**Total Memory:**  $M_{weight} + M_{activation}$

## LCSC & OMS-DPM & USF & DD

(Schedule Optimization)

**Total Latency:**  $t_{model} * N_{timestep} \downarrow$

**Total Memory:**  $M_{weight} + M_{activation}$

## MixDQ & ViDiT-Q

(Mixed-precision quantization)

**Total Latency:**  $t_{model} \downarrow * N_{timestep}$

**Total Memory:**  $M_{weight} \downarrow + M_{activation} \downarrow$

## DiTFastAttn

(Attention Compression)

**Total Latency:**  $t_{model} \downarrow * N_{timestep}$

**Total Memory:**  $M_{weight} + M_{activation} \downarrow$



## 目录 Contents

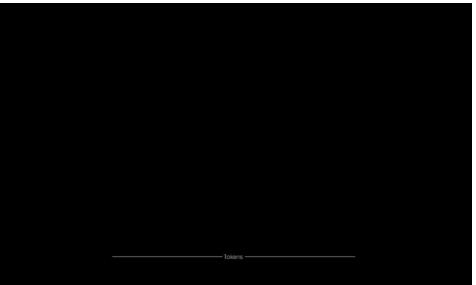
- 1 Background
- 2 Large Language Models (LLMs)
- 3 Diffusion Models
- 4 Research Summary

# Future Scenarios

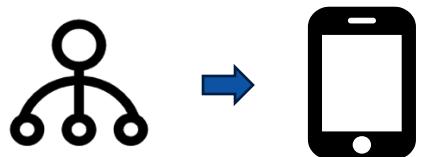
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## Language Generation

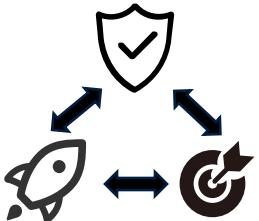


Agent and Multi-model Framework



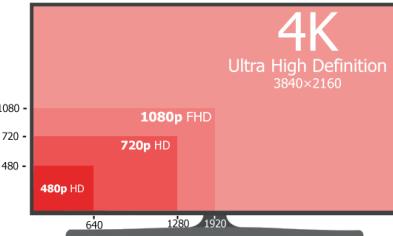
Edge Scenario Deployment

Long Context LLMs



Security-Efficiency Synergy

## Visual Generation



Spatial-Dimension:  
High-resolution Generation

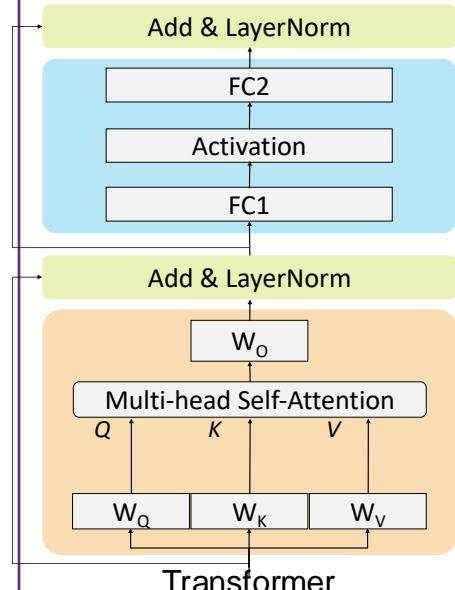


Temporal-Dimension:  
Long Video Generation

# Future Directions

**Goal: higher generation quality + better controllability and interactivity**

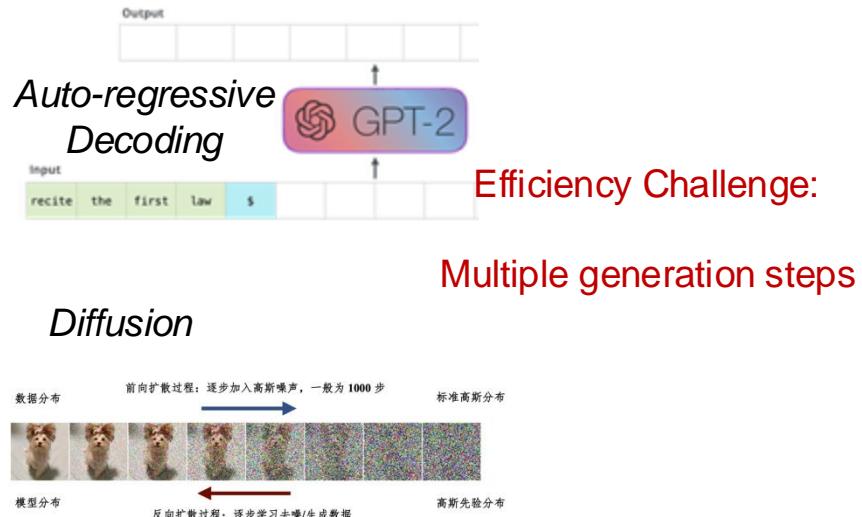
## Unified Model Architecture



Efficiency Challenge:

Quadratic complexity in context length

## Unified Generation Approach



# Research Summary

## Overview

### Survey [CSUR Submission]

Survey on efficient LLM inference techniques

## Algorithm-level

### SoT [ICLR'24]

Parallel generation via prompting.  
**1.91~2.39x speed-up**

## Model-level

### Sparse Attention

#### MoA [ICLR Submission]

Decide the heterogeneous elastic rule of the attention span for each head.  
**5.5~6.7x throughput improvement**

### Pruning

#### EEP [ICLR Submission]

Search the pruning pattern for MoE and use expert merging for finetuning.  
**48%~71% memory reduction,**  
**1.11~1.40x speed-up,**  
better performance

### Quantization

#### LLM-MQ [NeurIPS'23 Workshop]

Mixed-precision quantization.  
**2.8-bit quantization**

#### QLLM-Eval [ICML'24]

Evaluating the effect of quantization.  
**Providing knowledge and practical suggestions**

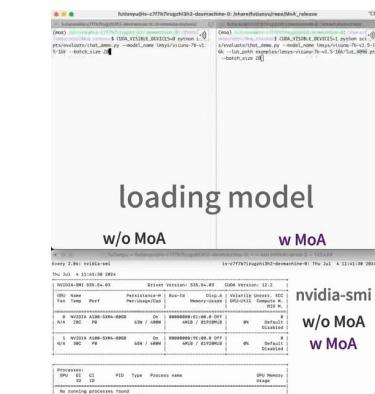
## Efficient Large Language Models

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### LLM



**LLaMA-2-7B on AMD MI210**  
**2x throughput improvement**



**Vicuna-7B on Nvidia-A100**  
**batch size 20**  
**end-to-end latency 2.3x**

# Research Summary



## Algorithm-level Time Step Compression

**LCSC**  
[ICLR Submission]

Linear combination of checkpoints.  
**15~23x** training acceleration,  
**1.25~2x** timestep compression

**USF**  
[ICLR'24]

**OMS-DPM**  
[ICML'23]

**DD**  
[ICLR Submission]

Search for optimal diffusion schedulers.  
**1.5~2x** speed-up

generates image in **0.01s**  
and can achieve **>100x**  
speedup for Image AR model

## Model-level Quantization

**MixDQ**  
[ECCV'24]

Mixed-precision quantization.  
**3x memory decrease,** **2.5x memory improvement,**  
**1.5x speed-up**

**ViDiT-Q**  
[ICLR Submission]

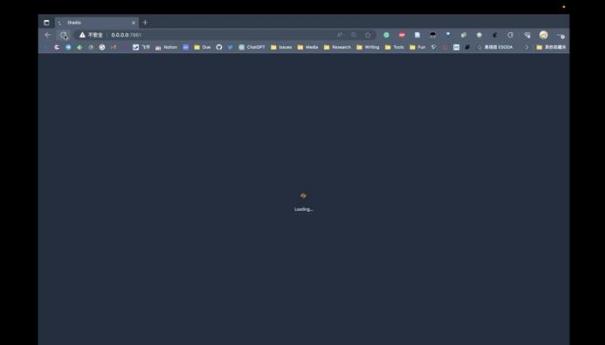
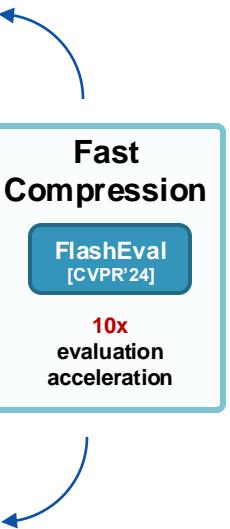
Quantization for DiT.  
**2.5x memory improvement,**  
**1.5x speed-up**

## Pruning & Sparse Attention

**DiTFastAttn**  
[NeurIPS'24]

Window & reused attention for DiT.  
**1.6x speed-up**

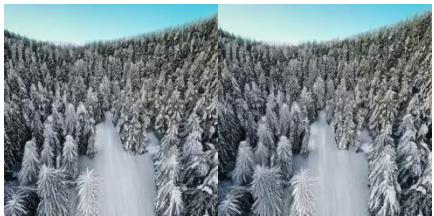
## Efficient Diffusion Models



Stable Diffusion on a single NVIDIA A100 GPU, Achieving **6.9x** speed-up and reducing **1.5x** memory



Pixart-Sigma, 2K generation  
on NVIDIA A100 GPU  
**1.8x latency speedup**



OpenSORA, 512x512x16 Frames,  
on NVIDIA A100 GPU,  
**2x Memory Savings, 1.7x latency speedup**



# 清华大学电子工程系

Department of Electronic Engineering, Tsinghua University

# Thank You !



新书：《高效  
深度学习：模  
型压缩与设计  
(全彩)》



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NICS EFC