

ARM® Cortex®-M23 32-bit Microcontroller

NuMicro[®] Family M2L31 Series BSP Revision History

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Revision 3.02.000 (Released 2024-12-17)

- 1. Updated sample code for M2L31 with only 4 KB SRAM.
- 2. Updated setting order of scatter-gather mode for PDMA and LPPDMA driver.
- 3. Updated EADC driver for ADC calibration.
- 4. Updated USB device sample code for V_{Bus} detect.
- 5. Removed LPM function from USB device sample code.
- 6. Changed PLL frequency setting from 240 MHz to 144 MHz for USB device samples.
- 7. Added RMC_DummyReadCID function for RRAM write after wake-up from Power-down mode.
- 8. Added I²C error handling when error flag asserted.
- 9. Added channel reset macro for PDMA and LPPDMA driver.
- 10. Minor changes for sample code.
- 11. Minor bug fix.

Revision 3.01.000 (Released 2024-05-16)

- 1. Removed Power Level 2 from SYS driver and SYS_PowerMode sample code.
- 2. Removed POR disable on Startup file for GCC and IAR.
- 3. Updated LPADC and EADC driver to enable ADC decoding with extra one cycle to improve ADC accuracy.
- 4. Updated I²C driver to support 10 bits salve address mode.
- 5. Added 3-wire SPI interface Macro for SPI and LPSPI drivers.
- 6. Added CANFD CAN mode samples.
- 7. Minor changes for sample code.
- 8. Minor bug fix.

Revision 3.00.000 (Released 2024-01-15)

1. Primary release version.



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