

NuMicro[®] Family
Arm[®] Cortex[®]-M0-based Microcontroller

M2L31 CMSIS BSP
Revision History

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Revision 3.01.000 (Released 2024-05-16)

1. Removed Power Level 2 from SYS driver and SYS_PowerMode sample code.
2. Removed POR disable on Startup file for GCC and IAR.
3. Updated LPADC and EADC driver to enable ADC decoding with extra one cycle to improve ADC accuracy.
4. Updated I²C driver to support 10 bits salve address mode.
5. Added 3-wire SPI interface Macro for SPI and LPSPI drivers.
6. Added CANFD CAN mode samples.
7. Minor changes for sample code.
8. Minor bug fix.

Revision 3.00.000 (Released 2024-01-15)

1. Primary release version.

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