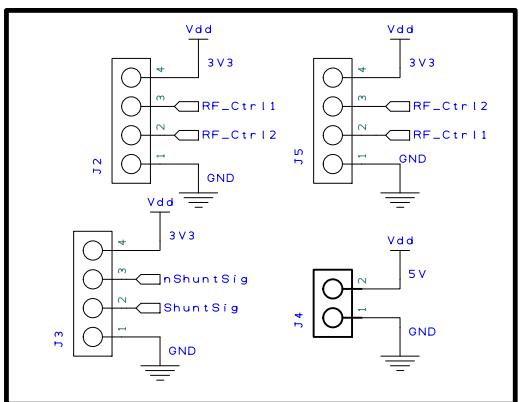
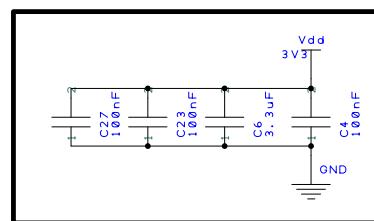


A      B      C      D      E      F

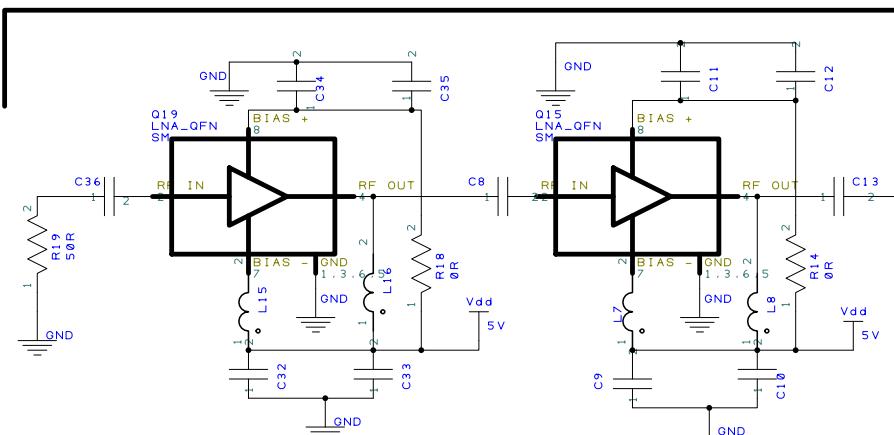
### 1 Headers



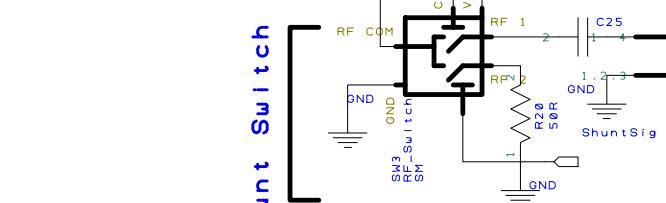
### 2 Switch VDD Decoupling



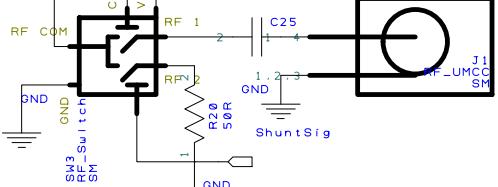
### 3 Primary Noise Source



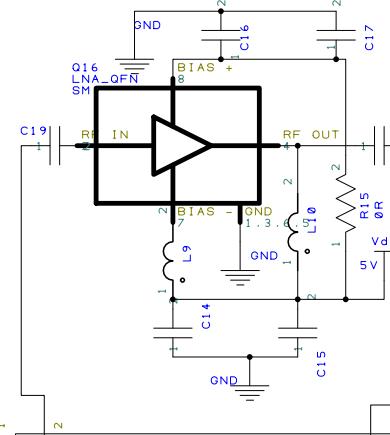
### 4 Bypass Switch



### 5 Shunt Switch



### 6 Solar Amplifier Stage



PROJECT: RADIO INTERFEROMETER  
DRAWING TITLE: NOISE SOURCE BOARD  
DRAWN BY: JOSH FIXELLE

REVISION: V 0.1

SHEET: 1 OF 1    STATUS:

DATE: NOV 08 2015

The Pennsylvania State University ----- Applied Physics Club

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