

Analysis & Synthesis Summary		
Analysis & Synthesis Status	Successful – Thu Oct 07 16:37:21 2021	
Quartus Prime Version	20.1.1 Build 720 11/11/2020 SJ Lite Edition	
Revision Name	CR16	
Top-level Entity Name	bram_top	
Family	Cyclone V	
Logic utilization (in ALMs)	N/A	
Total registers	9	
Total pins	31	
Total virtual pins	0	
Total block memory bits	16,384	
Total DSP Blocks	0	
Total HSSI RX PCSs	0	
Total HSSI PMA RX Deserializers	0	
Total HSSI TX PCSs	0	
Total HSSI PMA TX Serializers	0	
Total PLLs	0	
Total DLLs	0	